

Batch Arguments for NP from Standard Bilinear Group Assumptions

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Batch Arguments for NP

Boolean circuit satisfiability

$$\mathcal{L}_C = \{x \in \{0,1\}^n : C(x, w) = 1 \text{ for some } w\}$$

prover



(x_1, \dots, x_m)



prover has m statements and
wants to convince verifier that
 $x_i \in \mathcal{L}_C$ for all $i \in [m]$



verifier

Batch Arguments for NP

Boolean circuit satisfiability

$$\mathcal{L}_C = \{x \in \{0,1\}^n : C(x, w) = 1 \text{ for some } w\}$$

prover



(x_1, \dots, x_m)



$\pi = (w_1, \dots, w_m)$



verifier

Can the proof size be **sublinear** in the number of instances m ?

Naïve solution: send witnesses w_1, \dots, w_m and verifier checks $C(x_i, w_i) = 1$ for all $i \in [m]$

Goal: Amortize the Cost of NP Verification

Boolean circuit satisfiability

$$\mathcal{L}_C = \{x \in \{0,1\}^n : C(x, w) = 1 \text{ for some } w\}$$

prover



(x_1, \dots, x_m)



π



verifier

Proof size: $|\pi| = \text{poly}(\lambda, \log m, |C|)$

λ : security
parameter

Proof size can scale with circuit size
(not a SNARG for NP)

Goal: Amortize the Cost of NP Verification

Boolean circuit satisfiability

$$\mathcal{L}_C = \{x \in \{0,1\}^n : C(x, w) = 1 \text{ for some } w\}$$

prover



(x_1, \dots, x_m)



π



verifier

Proof size: $|\pi| = \text{poly}(\lambda, \log m, |C|)$

In general setting, verifier needs to read statements

Verification time: running time of verifier is $\text{poly}(\lambda, m, n) + \text{poly}(\lambda, \log m, |C|)$

Goal: Amortize the Cost of NP Verification

Boolean circuit satisfiability

$$\mathcal{L}_C = \{x \in \{0,1\}^n : C(x, w) = 1 \text{ for some } w\}$$

prover



(x_1, \dots, x_m)



π



verifier

Computational soundness: polynomial-time prover cannot convince verifier of (x_1, \dots, x_m) if there is any $i \in [m]$ where $x_i \notin \mathcal{L}_C$

Goal: Amortize the Cost of NP Verification

Boolean circuit satisfiability

$$\mathcal{L}_C = \{x \in \{0,1\}^n : C(x, w) = 1 \text{ for some } w\}$$

For (statistically-sound) proofs:

- With **inefficient** provers, IP = PSPACE [LFKN92, Sha92] theorem gives **interactive** proof for batch NP with communication $\text{poly}(\log m, |C|)$
- With efficient provers, we have **interactive** proofs for batch UP with communication $\text{poly}(\log m, |C|)$ [RRR16, RRR18, RR20]



verifier

Computational soundness: **polynomial-time** prover cannot convince verifier of (x_1, \dots, x_m) if there is any $i \in [m]$ where $x_i \notin \mathcal{L}_C$

Goal: Amortize the Cost of NP Verification

Boolean circuit satisfiability

$$\mathcal{L}_C = \{x \in \{0,1\}^n : C(x, w) = 1 \text{ for some } w\}$$

prover



(x_1, \dots, x_m)



π



verifier

Focus: Non-interactive setting (proof is a single message)

Goal: Amortize the Cost of NP Verification

common reference string

1100100001111110000011011111011111110111111010010010

prover



(x_1, \dots, x_m)



π



verifier



Focus: Non-interactive setting (proof is a single message)

Prover and verifier have access to a common reference string (CRS)

An Application: Succinct Argument for P

[KPY19, CJJ21]

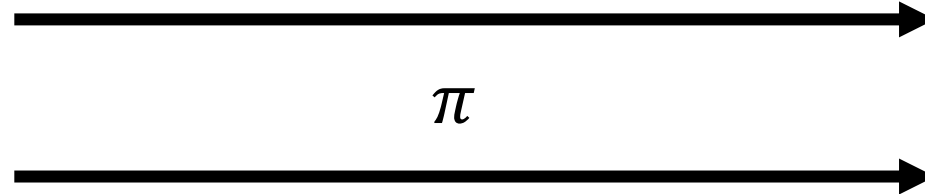
Turing machine M , input x , time bound T

Show: $M(x) = 1$ in at most T steps

prover



(x, T)



π

verifier



Proof size: $|\pi| = \text{poly}(\lambda, \log T)$

Verification time: running time of verifier is $\text{poly}(\lambda, |x|) + \text{poly}(\lambda, \log T)$

An Application: Succinct Argument for P

[KPY19, CJJ21]

(Very) high-level idea:



Prover commits to the vector of computation states (st_0, \dots, st_T)

Checking each transition can be implemented by a circuit of size $\text{poly}(\lambda)$

Each step only changes a constant number of positions in the computation state

Prover constructs a **batch argument** that all T transitions are valid

Statements are indices $1, \dots, T$ and the NP relation is checking validity of step i

Batch Arguments for NP

Special case of succinct non-interactive arguments for NP (SNARGs)

Constructions rely on **idealized models** or **knowledge assumptions** or **indistinguishability obfuscation**

Batch arguments from correlation intractable hash functions

Sub-exponential DDH (in pairing-free groups) + QR (with \sqrt{m} size proofs) [CJJ21a]

Learning with errors (LWE) [CJJ21b]

Batch arguments from pairing-based assumptions

Non-standard, but falsifiable q -type assumption on bilinear groups [KPY19]

This Work

New constructions of non-interactive batch arguments for NP

Batch arguments for NP from **standard assumptions** over bilinear maps

k -Linear assumption (for any $k \geq 1$) in prime-order bilinear groups

Subgroup decision assumption in composite-order bilinear groups

Key feature: Construction is “**low-tech**”

No heavy tools like **correlation-intractable hash functions** or **probabilistically-checkable proofs**

Direct “commit-and-prove” approach à la classic NIZK construction of Groth-Ostrovsky-Sahai

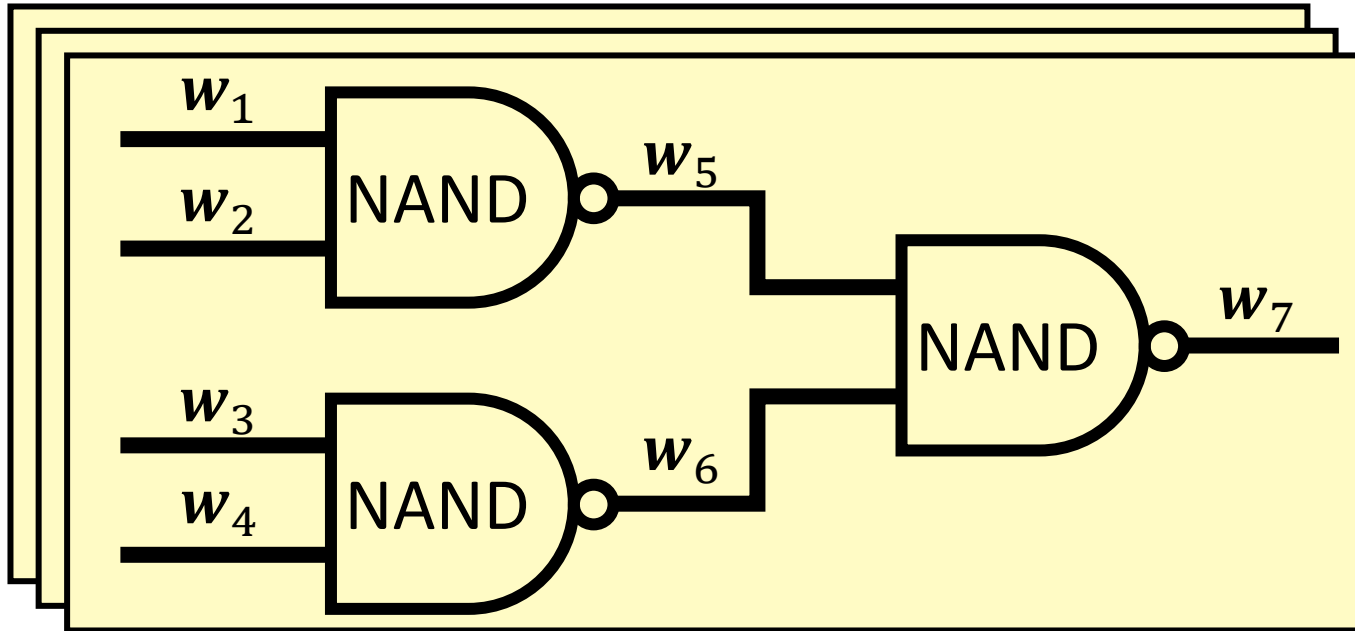
Corollary: RAM delegation (i.e., “SNARG for P”) with sublinear CRS from standard bilinear map assumptions

Previous bilinear map constructions: need non-standard assumptions [KPY19] or have long CRS [GZ21]

Corollary: Aggregate signature with bounded aggregation from standard bilinear map assumptions

Previous bilinear map constructions: random oracle based [BGLS03]

A Commit-and-Prove Strategy for Batch Arguments



Let $w_i = (w_{i,1}, \dots, w_{i,m})$ be **vector** of wire labels associated with wire i across the m instances

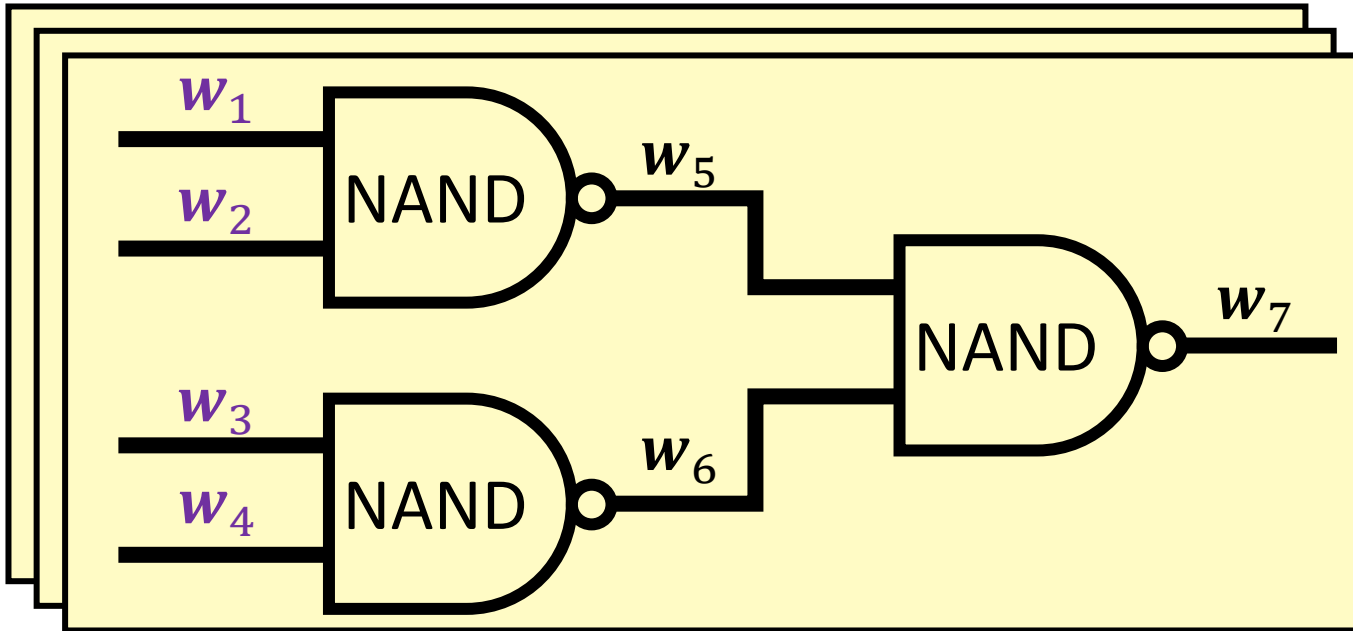
1 Prover commits to each vector of wire assignments

$$w_i = [w_{i,1} \quad w_{i,2} \quad \dots \quad w_{i,m}] \rightarrow \sigma_i$$

Requirement: $|\sigma_i| = \text{poly}(\lambda, \log m)$

Our construction: $|\sigma_i| = \text{poly}(\lambda)$

A Commit-and-Prove Strategy for Batch Arguments



Let $w_i = (w_{i,1}, \dots, w_{i,m})$ be **vector** of wire labels associated with wire i across the m instances

2 Prover constructs the following proofs:

Input validity

Commitments to the statement wires are correctly computed

Commitments in our scheme are *deterministic*, so verifier can directly check

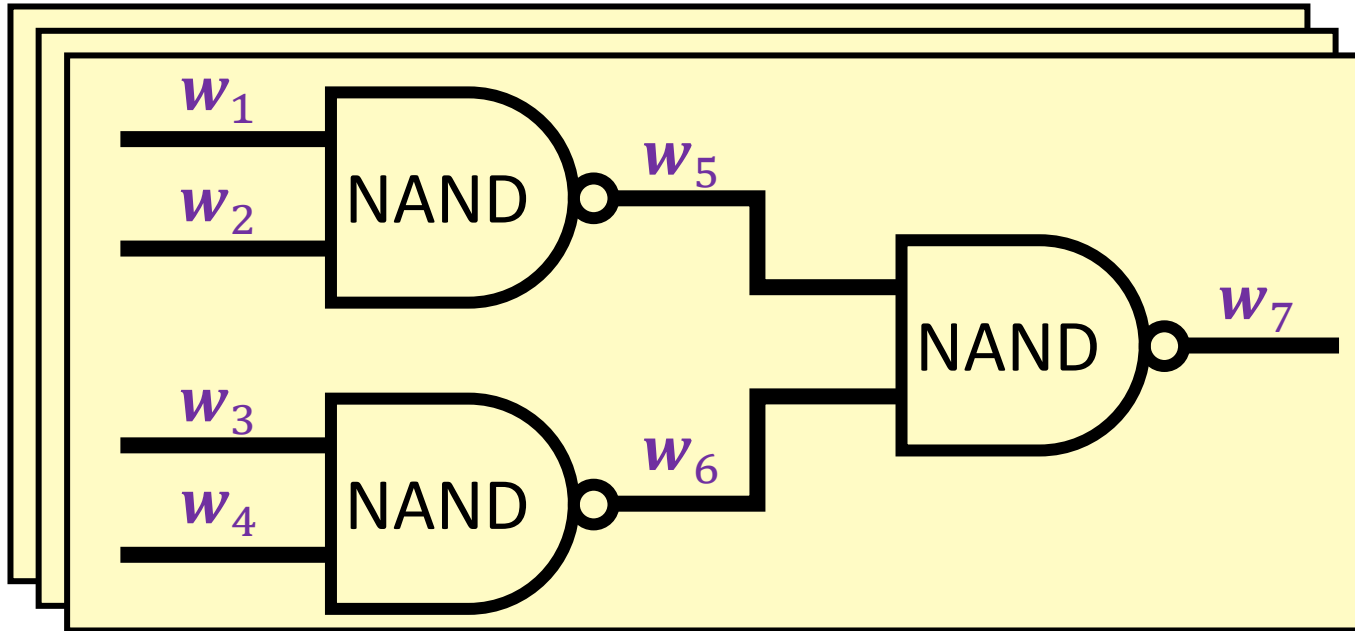
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$$w_i = \begin{matrix} w_{i,1} & w_{i,2} & \dots & w_{i,m} \end{matrix} \longrightarrow \sigma_i$$

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Wire validity

Commitment for each wire is a commitment to a 0/1 vector

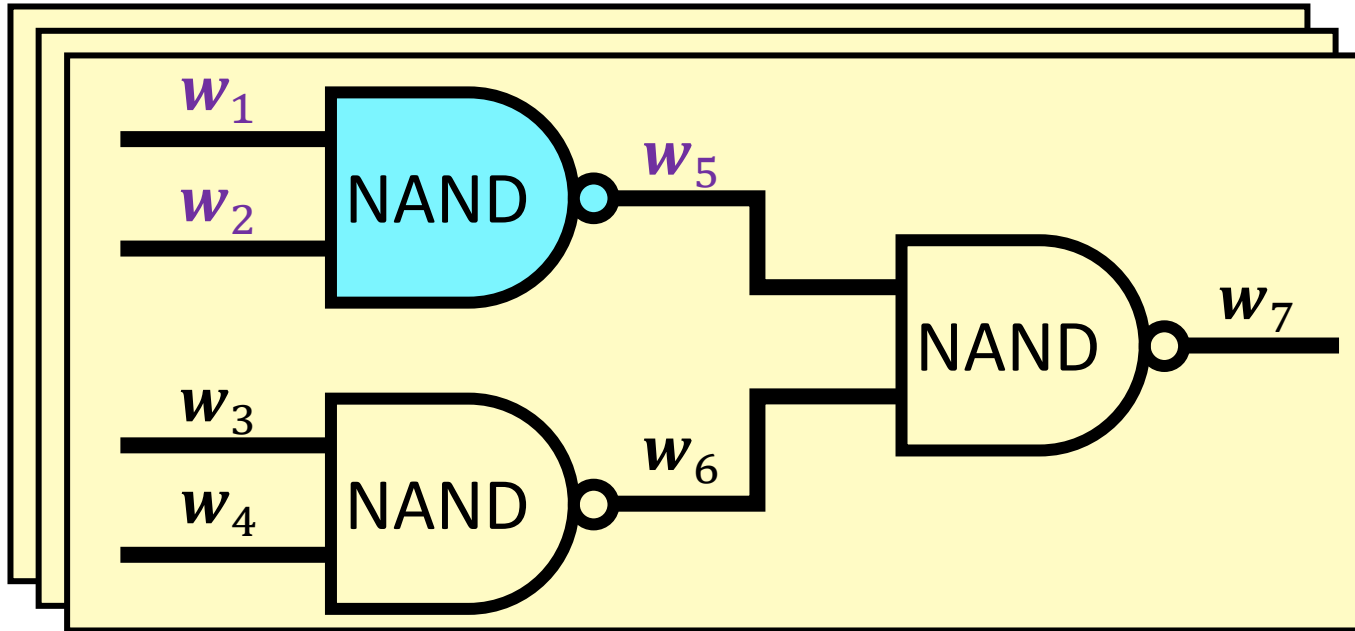
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For each gate, commitment to output wires is consistent with gate operation and commitment to input wires

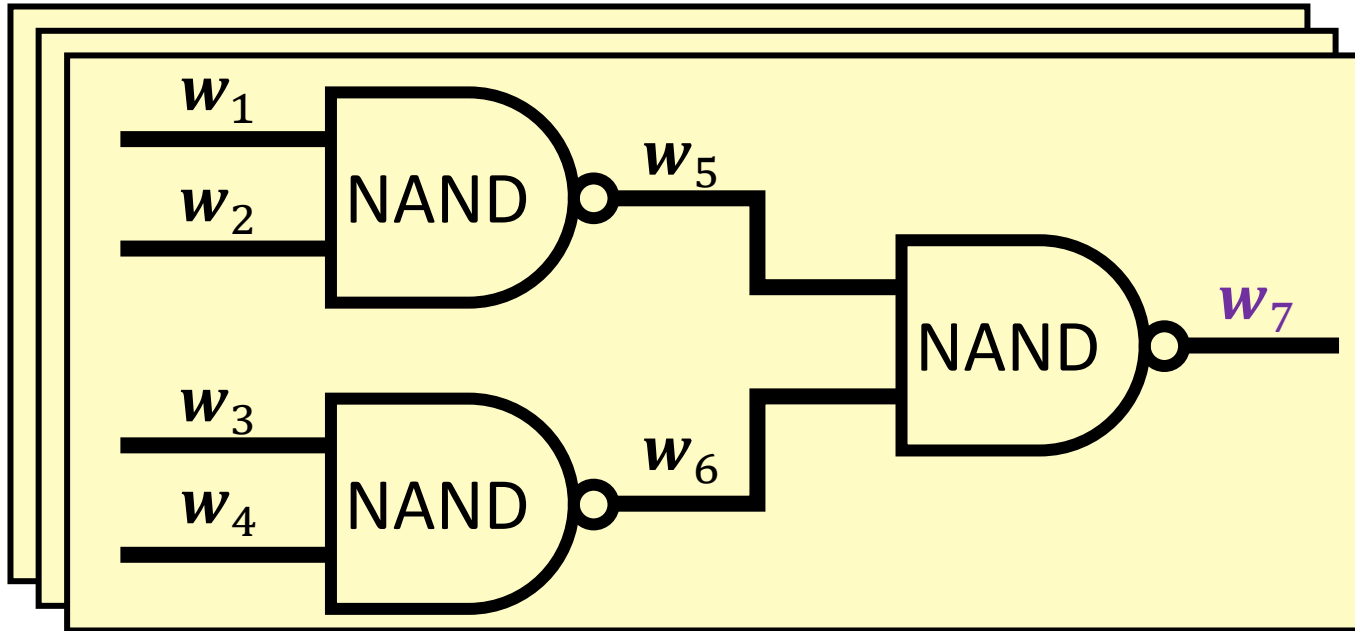
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Let $w_i = (w_{i,1}, \dots, w_{i,m})$ be **vector** of wire labels associated with wire i across the m instances

2 Prover constructs the following proofs:

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Wire validity

Gate validity

Output validity

Commitment to output wire is a commitment to the all-ones vector

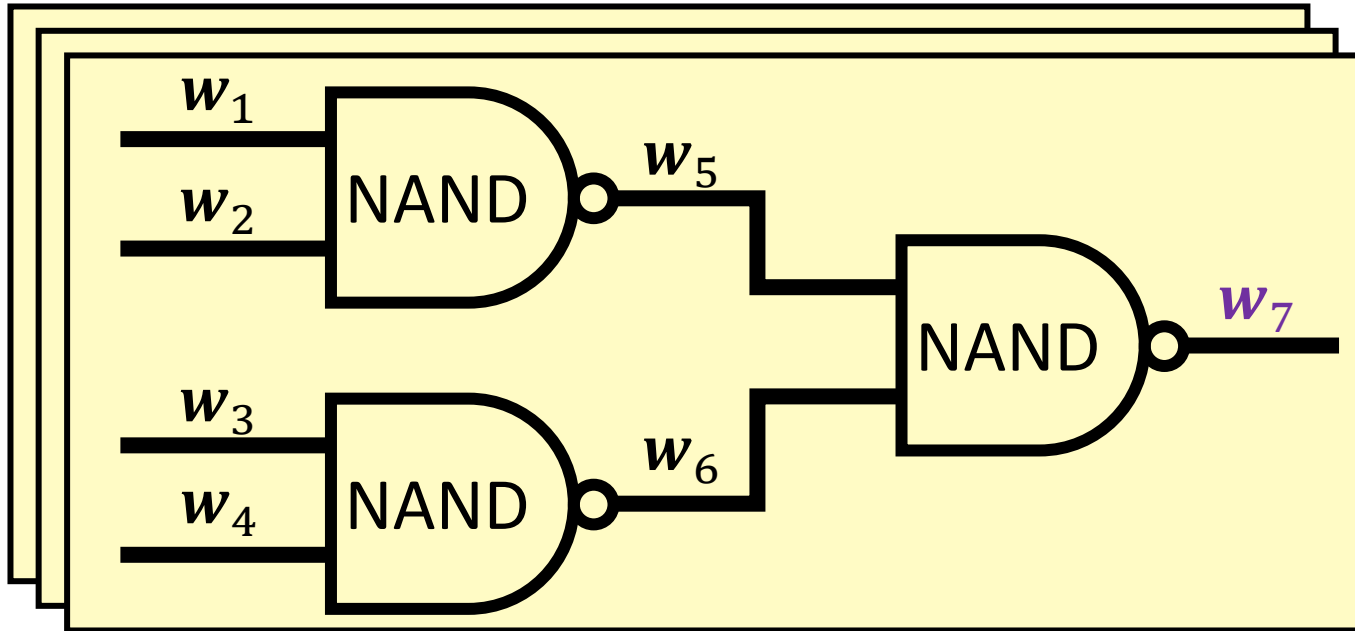
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Requirement: $|\sigma_i| = \text{poly}(\lambda, \log m)$

Our construction: $|\sigma_i| = \text{poly}(\lambda)$

Key idea: Validity checks are quadratic and can be checked in the exponent

Construction from Composite-Order Groups

Pedersen multi-commitments: (without randomness)

Let \mathbb{G} be a group of order $N = pq$ (composite order)

Let $\mathbb{G}_p \subset \mathbb{G}$ be the subgroup of order p and let g_p be a generator of \mathbb{G}_p

crs: sample $\alpha_1, \dots, \alpha_m \leftarrow \mathbb{Z}_N$
output $A_1 \leftarrow g_p^{\alpha_1}, \dots, A_m \leftarrow g_p^{\alpha_m}$

denotes encodings in \mathbb{G}_p

$[\alpha_1]$ $[\alpha_2]$ $[\dots]$ $[\alpha_m]$

commitment to $\mathbf{x} = (x_1, \dots, x_m) \in \{0,1\}^m$:

$$\sigma_{\mathbf{x}} = A_1^{x_1} A_2^{x_2} \dots A_m^{x_m}$$

(subset product of the A_i 's)

$$[\sigma_{\mathbf{x}}] = \left[\sum_{i \in [m]} \alpha_i x_i \right]$$

Proving Relations on Committed Values

Common reference string:

$$[\alpha_1] \quad A_1 = g_p^{\alpha_1}$$

$$[\vdots]$$

$$[\alpha_m] \quad A_m = g_p^{\alpha_m}$$

Commitment to (x_1, \dots, x_m) :

$$[\sum_{i \in [m]} \alpha_i x_i]$$

$$\begin{aligned} \sigma_x &= A_1^{x_1} A_2^{x_2} \dots A_m^{x_m} \\ &= g_p^{\alpha_1 x_1 + \dots + \alpha_m x_m} \end{aligned}$$

Wire validity

Commitment for each wire is a commitment to a 0/1 vector
 $x \in \{0,1\}$ if and only if $x^2 = x$

Key idea: Use pairing to check quadratic relation in the exponent

Recall: pairing is an efficiently-computable bilinear map on \mathbb{G} :

$$e(g^x, g^y) = e(g, g)^{xy}$$

$$e([\![x]\!] , [\![y]\!]) \longrightarrow [\![xy]\!]$$

Multiplies exponents in the target group

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Wire validity

Commitment for each wire is a commitment to a 0/1 vector
 $x \in \{0,1\}$ if and only if $x^2 = x$

Approach: consider the following pairing relations:

$$e(\sigma_x, \sigma_x) \text{ and } e(\sigma_x, \prod_{i \in [m]} A_i)$$

$$A = \prod_{i \in [m]} A_i = g_p^{\sum_{i \in [m]} \alpha_i}$$

(commitment to all-ones vector)

Proving Relations on Committed Values

Common reference string:

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$$e\left([\sum_{i \in [m]} \alpha_i x_i], [\sum_{i \in [m]} \alpha_i x_i]\right)$$

$$= [\sum_{i \in [m]} \alpha_i^2 x_i^2] \times [\sum_{i \neq j} \alpha_i \alpha_j x_i x_j]$$

non-cross terms *cross terms*

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non-cross terms *cross terms*

Proving Relations on Committed Values

If $x_i^2 = x_i$ for all i , then

$$\left[\sum_{i \in [m]} \alpha_i^2 x_i \right]$$

=

$$\left[\sum_{i \in [m]} \alpha_i^2 x_i^2 \right]$$

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Commitment for each wire is a commitment to a 0/1 vector $x \in \{0,1\}$ if and only if $x^2 = x$

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$$= \underbrace{\left[\sum_{i \in [m]} \alpha_i^2 x_i \right]}_{\text{non-cross terms}} \times \underbrace{\left[\sum_{i \neq j} \alpha_i \alpha_j x_i \right]}_{\text{cross terms}}$$

$$e\left(\left[\sum_{i \in [m]} \alpha_i x_i \right], \left[\sum_{i \in [m]} \alpha_i x_i \right]\right)$$

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$$e\left(\left[\sum_{i \in [m]} \alpha_i x_i \right], \left[\sum_{i \in [m]} \alpha_i x_i \right]\right)$$

When $x_i^2 = x_i$, difference between these terms is

$$\left[\sum_{i \neq j} \alpha_i \alpha_j (x_i - x_i x_j) \right]$$

Give prover ability to
eliminate cross-terms *only*

Augment CRS with cross-terms

$$\left[\alpha_i \alpha_j \right] B_{i,j} = g_p^{\alpha_i \alpha_j} \quad \forall i \neq j$$

Proving Relations on Committed Values

Prover now computes additional group component in the *base* group

$$\begin{array}{ccc}
 \boxed{\left[\sum_{i \neq j} \alpha_i \alpha_j (x_i - x_i x_j) \right]} & \xrightarrow{\text{Pair with } g_p} & \boxed{\left[\sum_{i \neq j} \alpha_i \alpha_j (x_i - x_i x_j) \right]} \\
 V = B_{i,j}^{x_i - x_i x_j} & & e(g_p, V)
 \end{array}$$

$$e\left(\boxed{\left[\sum_{i \in [m]} \alpha_i x_i \right]}, \boxed{\left[\sum_{i \in [m]} \alpha_i \right]} \right) \quad e\left(\boxed{\left[\sum_{i \in [m]} \alpha_i x_i \right]}, \boxed{\left[\sum_{i \in [m]} \alpha_i x_i \right]} \right)$$

When $x_i^2 = x_i$, difference between these terms is

Augment CRS with cross-terms

$$\boxed{\left[\sum_{i \neq j} \alpha_i \alpha_j (x_i - x_i x_j) \right]} \xrightarrow{\text{Give prover ability to eliminate cross-terms only}} \boxed{\left[\alpha_i \alpha_j \right]} B_{i,j} = g_p^{\alpha_i \alpha_j} \quad \forall i \neq j$$

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Overall verification relation: $e(\sigma_x, \sigma_x) = e(\sigma_x, A)e(g_p, V)$ $A = \prod_{i \in [m]} A_i$

Proving Relations on Committed Values

Prover now computes additional group component in the *base* group

$$\left[\sum_{i \neq j} \alpha_i \alpha_j (x_i - x_i x_j) \right] \xrightarrow{\text{Pair with } g_p} \left[\sum_{i \neq j} \alpha_i \alpha_j (x_i - x_i x_j) \right]$$
$$V = B_{i,j}^{x_i - x_i x_j} \qquad e(g_p, V)$$

Overall verification relation: $e(\sigma_x, \sigma_x) = e(\sigma_x, A) e(g_p, V)$ $A = \prod_{i \in [m]} A_i$

Non-cross terms ensure that $x_i^2 = x_i$

Proving Relations on Committed Values

Prover now computes additional group component in the *base* group

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Non-cross terms ensure that $x_i^2 = x_i$

Correction factor to correct for cross terms

Proving Relations on Committed Values

Common reference string:

$$[\alpha_1] \quad [\dots] \quad [\alpha_m]$$

$$A_1 = g_p^{\alpha_1}$$

$$A_m = g_p^{\alpha_m}$$

$$[\alpha_1 + \dots + \alpha_m] \quad A = \prod_{i \in [m]} A_i$$

$$[\alpha_i \alpha_j] \quad B_{i,j} = g_p^{\alpha_i \alpha_j} \quad \forall i \neq j$$

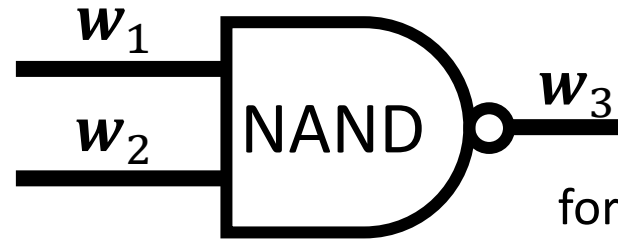
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Gate validity

For each gate, commitment to output wires is consistent with gate operation and commitment to input wires



$$\text{for all } i \in [m]: w_{3,i} = 1 - w_{1,i} w_{2,i}$$

Can leverage same approach as before:

$$e(\sigma_{w_3}, A) = e(g_p, g_p)^{\sum_{i \in [m]} \alpha_i^2 w_{3,i} + \sum_{i \neq j} \alpha_i \alpha_j w_{3,i}}$$

$$e(A, A) = e(g_p, g_p)^{\sum_{i \in [m]} \alpha_i^2 + \sum_{i \neq j} \alpha_i \alpha_j}$$

$$e(\sigma_{w_1}, \sigma_{w_2}) = e(g_p, g_p)^{\sum_{i \in [m]} \alpha_i^2 w_{1,i} w_{2,i} + \sum_{i \neq j} \alpha_i \alpha_j w_{1,i} w_{2,j}}$$

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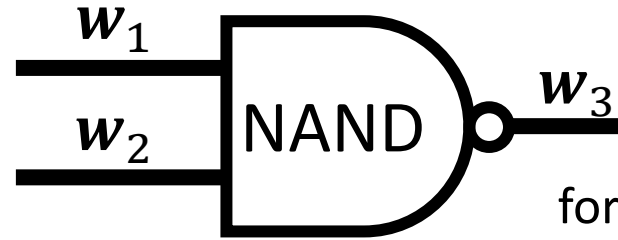
If $w_{3,i} + w_{1,i}w_{2,i} = 1$ for all i , then

$$\frac{e(\sigma_{w_3}, A) e(\sigma_{w_1}, \sigma_{w_2})}{e(A, A)}$$

only consists of **cross terms!**

Gate validity

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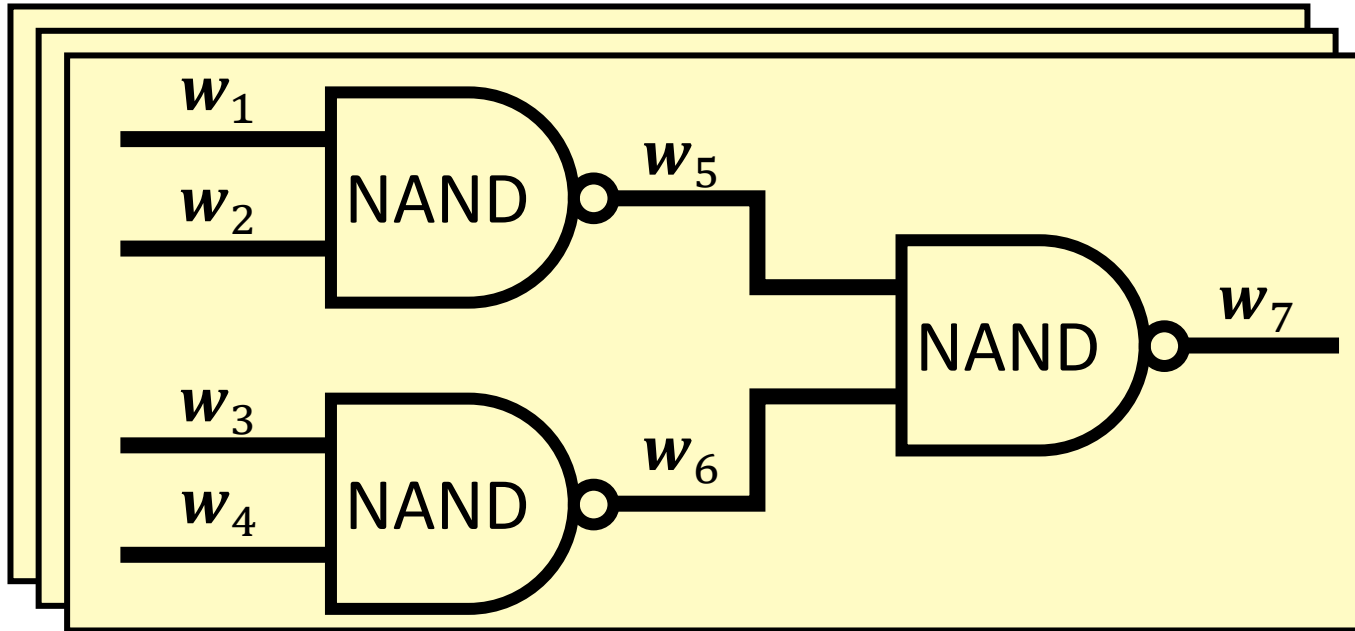
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$$e(A, A) = e(g_p, g_p)^{\sum_{i \in [m]} \alpha_i^2 + \sum_{i \neq j} \alpha_i \alpha_j}$$

$$e(\sigma_{w_1}, \sigma_{w_2}) = e(g_p, g_p)^{\sum_{i \in [m]} \alpha_i^2 w_{1,i} w_{2,i} + \sum_{i \neq j} \alpha_i \alpha_j w_{1,i} w_{2,j}}$$

Proof Size



Let $w_i = (w_{i,1}, \dots, w_{i,m})$ be **vector** of wire labels associated with wire i

2 Prover constructs the following proofs:

Input validity

Wire validity

Gate validity

Output validity

One group element

One group element

1 Prover commits to each vector of wire assignments

$$w_i = \begin{matrix} w_{i,1} & w_{i,2} & \dots & w_{i,m} \end{matrix} \longrightarrow \sigma_i$$

Commitment size: $|\sigma_i| = \text{poly}(\lambda)$

Single group element

Overall proof size (t wires, s gates):

$$(2t + s) \cdot \text{poly}(\lambda) = |C| \cdot \text{poly}(\lambda)$$

Is This Sound?

Common reference string:

$$[\alpha_1] \quad [\dots] \quad [\alpha_m]$$

$$A_1 = g_p^{\alpha_1}$$

$$A_m = g_p^{\alpha_m}$$

$$[\alpha_1 + \dots + \alpha_m] \quad A = \prod_{i \in [m]} A_i$$

$$[\alpha_i \alpha_j] \quad B_{i,j} = g_p^{\alpha_i \alpha_j} \quad \forall i \neq j$$

Commitment to (x_1, \dots, x_m) :

$$[\sum_{i \in [m]} \alpha_i x_i]$$

$$\begin{aligned} \sigma_x &= A_1^{x_1} A_2^{x_2} \dots A_m^{x_m} \\ &= g_p^{\alpha_1 x_1 + \dots + \alpha_m x_m} \end{aligned}$$

Soundness requires some care:

Groth-Ostrovsky-Sahai NIZK based on similar **commit-and-prove** strategy

Soundness in GOS is possible by extracting a witness from the commitment

For a false statement, no witness exists

Our setting: commitments are *succinct* – cannot extract a full witness

Solution: “local extractability” [KPY19] or “somewhere extractability” [CJJ21]

Somewhere Soundness

CRS will have two modes:

Normal mode: used in the real scheme

Extracting on index i : supports witness extraction for instance i (given a trapdoor)

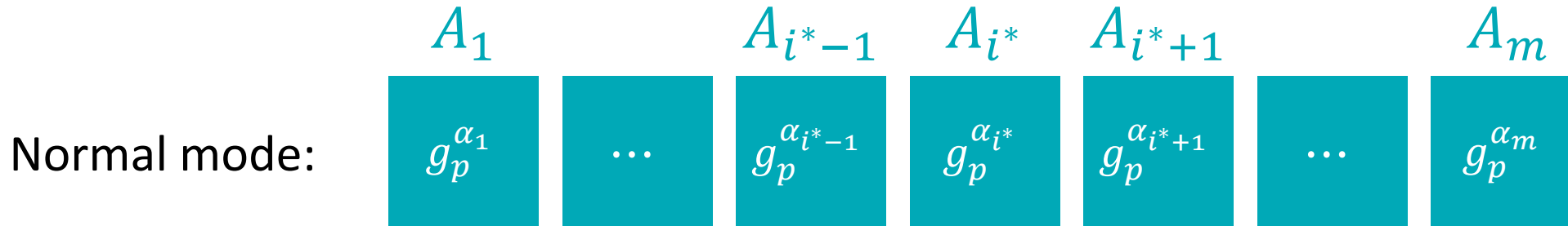
If proof π verifies, then we can extract a witness w_i such that $C(x_i, w_i) = 1$

CRS in the two modes are **computationally indistinguishable**

Similar to “dual-mode” proof systems and somewhere statistically binding hash functions

Implies **non-adaptive** soundness

Local Extraction



Move slot i^* to full group



Subgroup decision assumption [BGN05]:

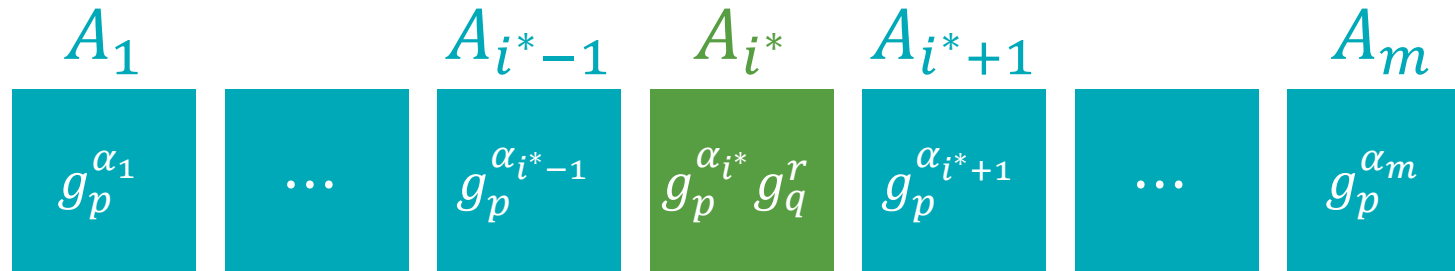
Random element in subgroup (\mathbb{G}_p)

\approx

Random element in full group (\mathbb{G})

Local Extraction

CRS in extraction mode (for index i^*):



Trapdoor: g_q (generator of \mathbb{G}_q)

Can extract by projecting into \mathbb{G}_q

Extracted bit for a commitment σ is 1 if σ has a (non-zero) component in \mathbb{G}_q

Correctness of Extraction

Consider wire validity check:

$$e(\sigma_x, \sigma_x) = e(\sigma_x, A)e(g_p, V)$$

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Adversary chooses commitment σ_x and proof V

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Consider wire validity check:

$$e(\sigma_x, \sigma_x) = e(\sigma_x, A)e(g_p, V)$$

Adversary chooses commitment σ_x and proof V

Generator g_p and aggregated component A part of the CRS (honestly-generated)

If this relation holds, it must hold in **both**
the order- p subgroup **and** the order- q subgroup of \mathbb{G}_T

Key property: $e(g_p, V)$ is **always** in the order- p subgroup; adversary **cannot** influence the verification relation in the order- q subgroup

$$\text{Write } \sigma_x = g_p^s g_q^t$$

$$\text{Write } A = g_p^{\sum_{i \in [m]} \alpha_i} g_q^r$$

In the order- q subgroup, exponents must satisfy:

$$t^2 = tr \pmod q$$

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the order- p subgroup **and** the order- q subgroup of \mathbb{G}

Key property: $e(g_p, V)$ is **always**
verification relation in the order- p subgroup

If wire validity checks pass, then $t = b_i r$ where $b_i \in \{0,1\}$

Observe: $b_i \in \{0,1\}$ is also the extracted bit

$$\text{Write } \sigma_x = g_p^s g_q^t$$

$$\text{Write } A = g_p^{\sum_{i \in [m]} \alpha_i} g_q^r$$

In the order- q subgroup, exponents must satisfy:

$$t^2 = tr \pmod q$$

Correctness of Extraction

Consider gate validity check:

$$e(\sigma_{w_3}, A)e(\sigma_{w_1}, \sigma_{w_2}) = e(A, A)e(g_p, W)$$

Correctness of Extraction

Consider gate validity check:

$$e(\sigma_{w_3}, A)e(\sigma_{w_1}, \sigma_{w_2}) = e(A, A)e(g_p, W)$$

Adversary chooses commitment $\sigma_{w_1}, \sigma_{w_2}, \sigma_{w_3}$ and proof W

Generator g_p and aggregated key A part of the CRS (honestly-generated)

Write

$$\sigma_{w_1} = g_p^{s_1} g_q^{t_1}$$

$$\sigma_{w_2} = g_p^{s_2} g_q^{t_2}$$

$$\sigma_{w_3} = g_p^{s_3} g_q^{t_3}$$

$$\text{Write } A = g_p^{\sum_{i \in [m]} \alpha_i} g_q^r$$

In the order- q subgroup, exponents must satisfy:

$$t_3 r + t_1 t_2 = r^2 \pmod{q}$$

By wire validity checks: $t_i = b_i r$ where $b_i \in \{0,1\}$

$$b_3 r^2 + b_1 b_2 r^2 = r^2 \pmod{q}$$

$$b_3 = 1 - b_1 b_2 = \text{NAND}(b_1, b_2)$$

Correctness of Extraction

Consider gate validity check:

$$e(\sigma_{w_3}, A)e(\sigma_{w_1}, \sigma_{w_2}) = e(A, A)e(g_p, W)$$

Adversary chooses commitment $\sigma_{w_1}, \sigma_{w_2}, \sigma_{w_3}$ and proof W

Generator g_p and aggregated key A part of the CRS (honestly-generated)

Write

$$\sigma_{w_1} = g_p^{s_1} g_q^{t_1}$$

$$\sigma_{w_2} = g_p^{s_2} g_q^{t_2}$$

$$\sigma_{w_3} = g_p^{s_3} g_q^{t_3}$$

$$\text{Write } A = g_p^{\sum_{i \in [m]} \alpha_i} g_q^r$$

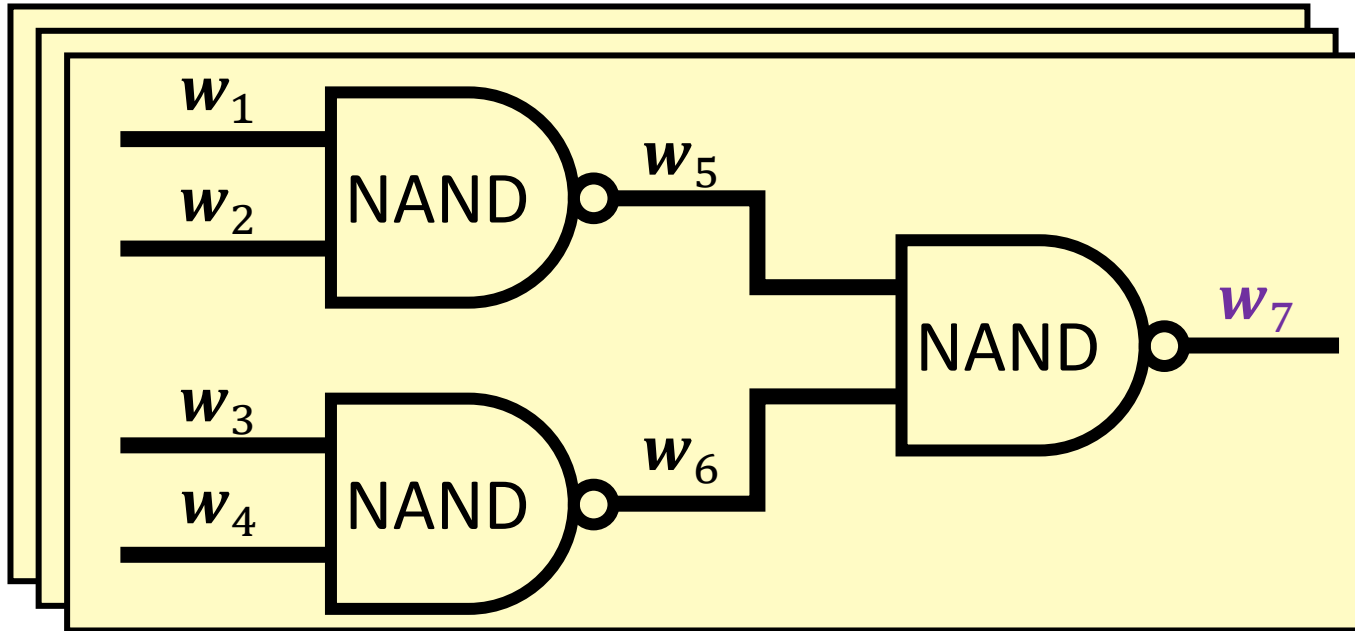
In the order- q subgroup, exponents must satisfy:

$$t_3 r + t_1 t_2 = r^2 \pmod{q}$$

Conclusion: extracted bits are consistent with gate operation

$$b_3 = 1 - b_1 b_2 = \text{NAND}(b_1, b_2)$$

A Commit-and-Prove Strategy for Batch Arguments



1 Prover commits to each vector of wire assignments

$$w_i = [w_{i,1} \quad w_{i,2} \quad \dots \quad w_{i,m}] \rightarrow \sigma_i$$

Let $w_i = (w_{i,1}, \dots, w_{i,m})$ be **vector** of wire labels associated with wire i across the m instances

2 Prover constructs the following proofs:

Input validity

Wire validity

Gate validity

Output validity

Key idea: Validity checks are quadratic and can be checked in the exponent

From Composite-Order to Prime-Order

Batch arguments for NP from standard assumptions over bilinear maps

Subgroup decision assumption in **composite-order** bilinear groups

$$\mathbb{G} \cong \mathbb{G}_p \times \mathbb{G}_q$$

composite-order group

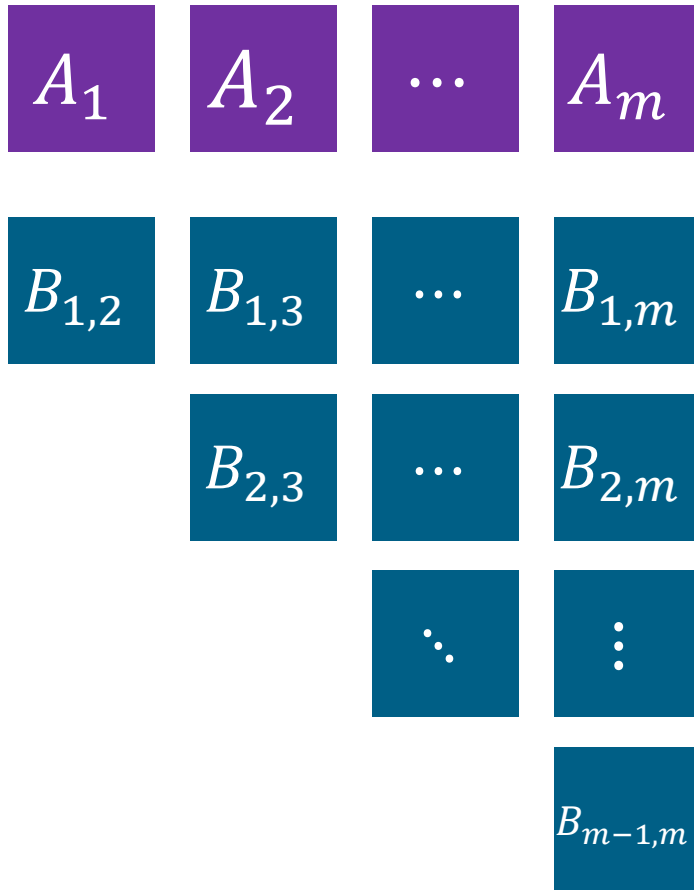
Simulate **subgroups**
with **subspaces**

Yields a batch argument from

k -Linear assumption (for any $k \geq 1$) in **prime-order** asymmetric bilinear groups

Reducing CRS Size

Common reference string:



Size of CRS is $m^2 \cdot \text{poly}(\lambda)$

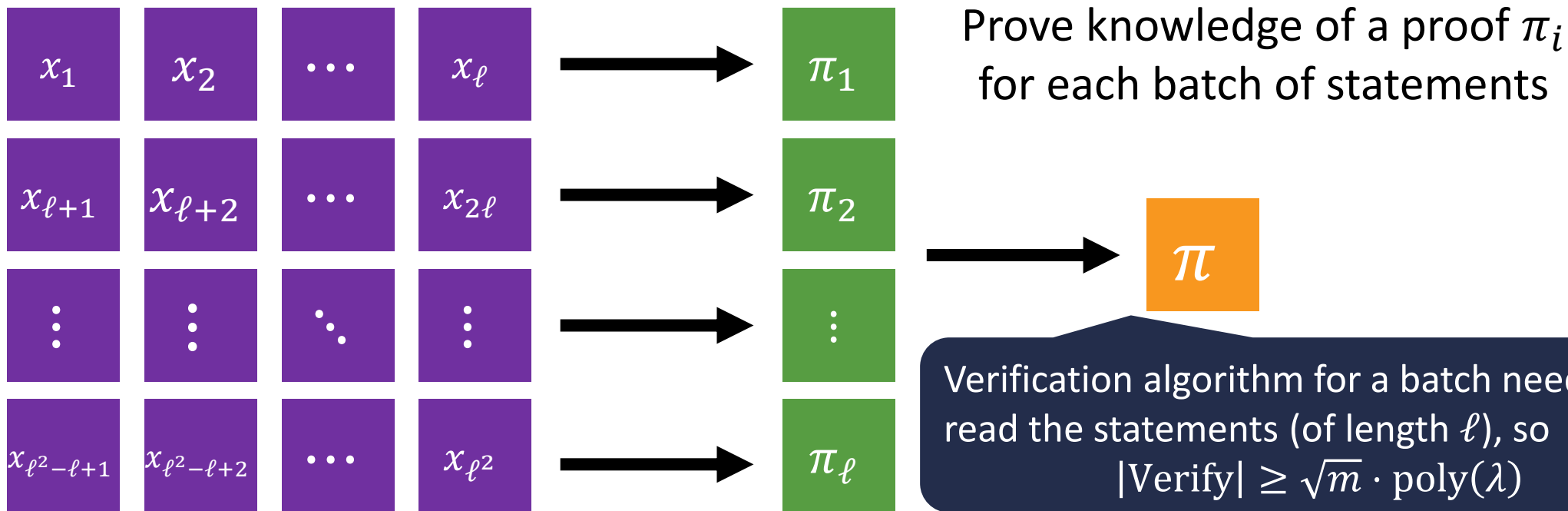
Can rely on recursive composition to reduce CRS size:

$$m^2 \cdot \text{poly}(\lambda) \rightarrow m^\varepsilon \cdot \text{poly}(\lambda)$$

for any constant $\varepsilon > 0$

Similar approach as [KPY19]

The Base Case



$$\ell = \sqrt{m}$$

Use batch argument on $\ell = \sqrt{m}$ instances to prove each batch

Both batch arguments are on $\ell = \sqrt{m}$ statements

Batch Arguments with Split Verification

$\text{Verify}(\text{crs}, \mathcal{C}, (\mathbf{x}_1, \dots, \mathbf{x}_m), \pi)$

$\text{GenVK}(\text{crs}, (\mathbf{x}_1, \dots, \mathbf{x}_m)) \rightarrow \text{vk}$

Runs in time $\text{poly}(\lambda, m, n)$

$|\text{vk}| = \text{poly}(\lambda, \log m, n)$

Preprocesses statements into a
short verification key

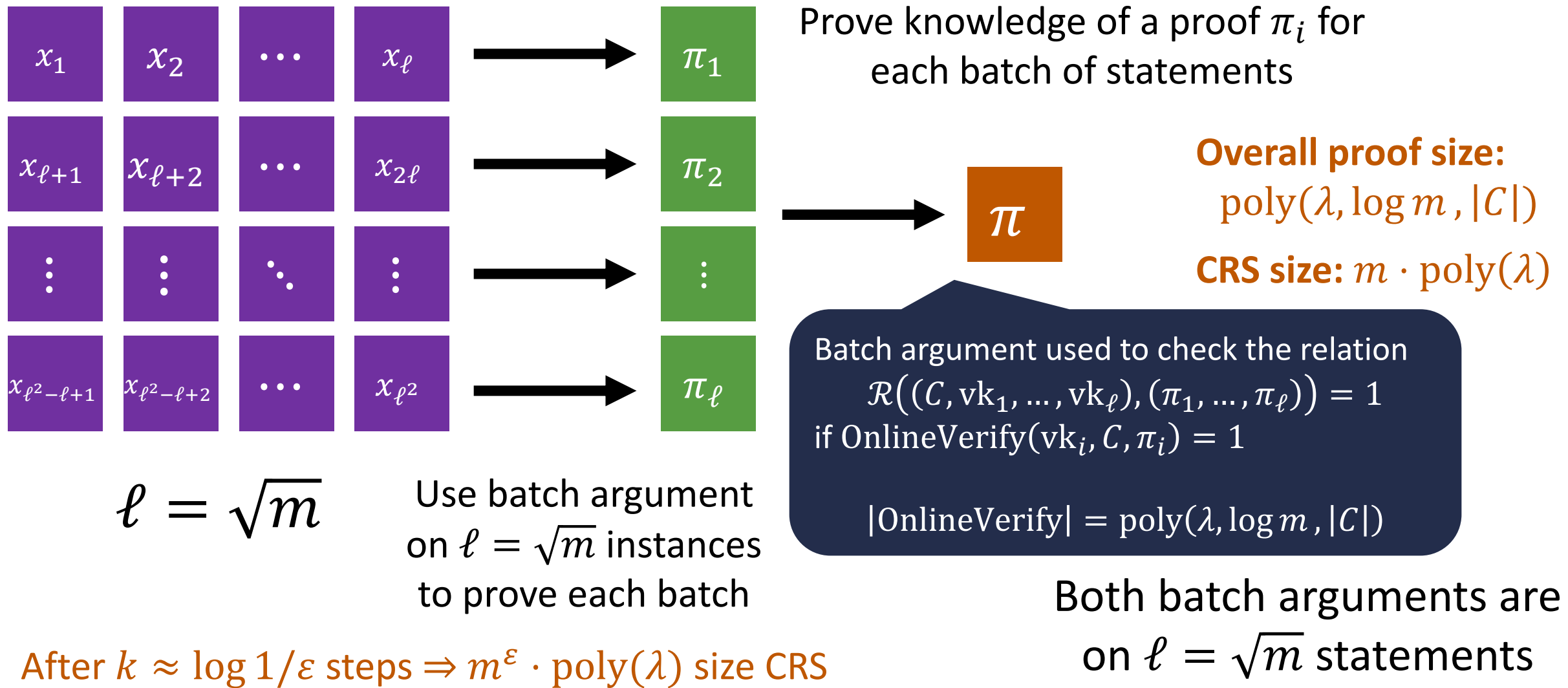
$\text{OnlineVerify}(\text{vk}, \mathcal{C}, \pi)$

Runs in time $\text{poly}(\lambda, \log m, |\mathcal{C}|)$

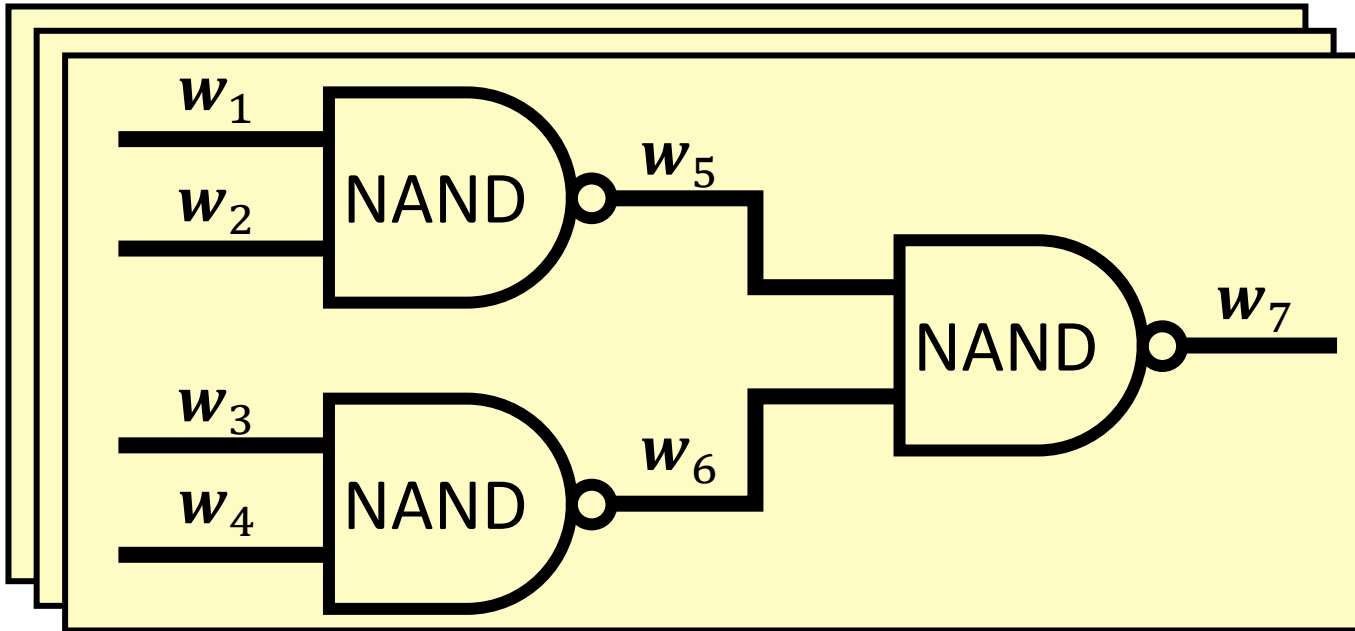
Fast online verification

(Similar property from [CJJ21])

Recursive Bootstrapping



Batch Arguments with Split Verification



In online phase, verifier uses commitments $(\sigma_1, \dots, \sigma_n)$ for the bits of input wires

(no more input validity checks)

Verifier checks the following

Input validity	}	$nm \cdot \text{poly}(\lambda)$
Wire validity		
Gate validity	}	$ C \cdot \text{poly}(\lambda)$
Output validity		
		constant number of group operations per wire/gate

Only depends on the statement!

Given $(x_1, \dots, x_m) \in (\{0,1\}^n)^m$, verifier computes commitments to bits of the statement

$$\forall j \in [n] : \sigma_j \leftarrow \prod_{i \in [m]} A_i^{x_{i,j}}$$

$$\text{GenVK}(\text{crs}, (x_1, \dots, x_m)) \rightarrow (\sigma_1, \dots, \sigma_n)$$

Batch Arguments with Short CRS

Corollary: Batch arguments for NP from **standard assumptions** over bilinear maps

k -Linear assumption (for any $k \geq 1$) in prime-order bilinear groups

Subgroup decision assumption in composite-order bilinear groups

For a proof on m instances of length n :

- **CRS size:** $|\text{crs}| = m^\varepsilon \cdot \text{poly}(\lambda)$ for any constant $\varepsilon > 0$
- **Proof size:** $|\pi| = \text{poly}(\lambda, |C|)$
- **Verification time:** $|\text{Verify}| = \text{poly}(\lambda, n, m) + \text{poly}(\lambda, |C|)$

Application to RAM Delegation (“SNARGs for P”)

Choudhuri et al. [CJJ21] showed:



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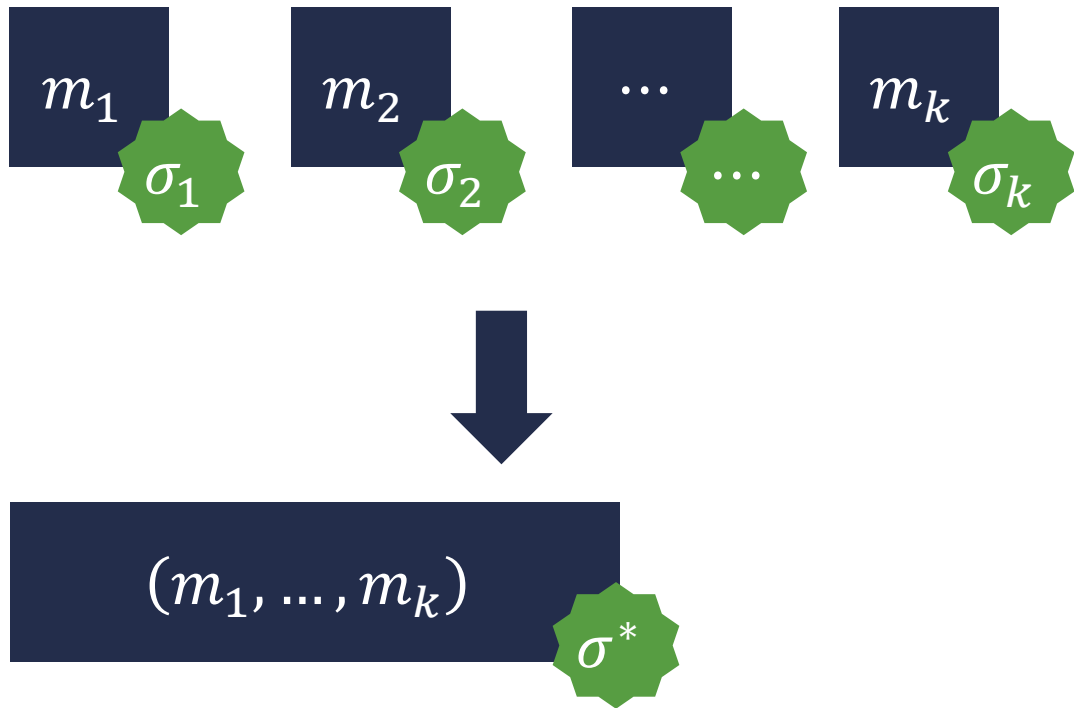
Corollary. RAM delegation from SXDH on prime-order pairing groups

To verify a time- T RAM computation:

- **CRS size:** $|\text{crs}| = T^\varepsilon \cdot \text{poly}(\lambda)$ for any constant $\varepsilon > 0$
- **Proof size:** $|\pi| = \text{poly}(\lambda, \log T)$
- **Verification time:** $|\text{Verify}| = \text{poly}(\lambda, \log T)$

Previous pairing constructions: non-standard assumptions [KPY19] or quadratic CRS [GZ21]

Application to Aggregate Signatures



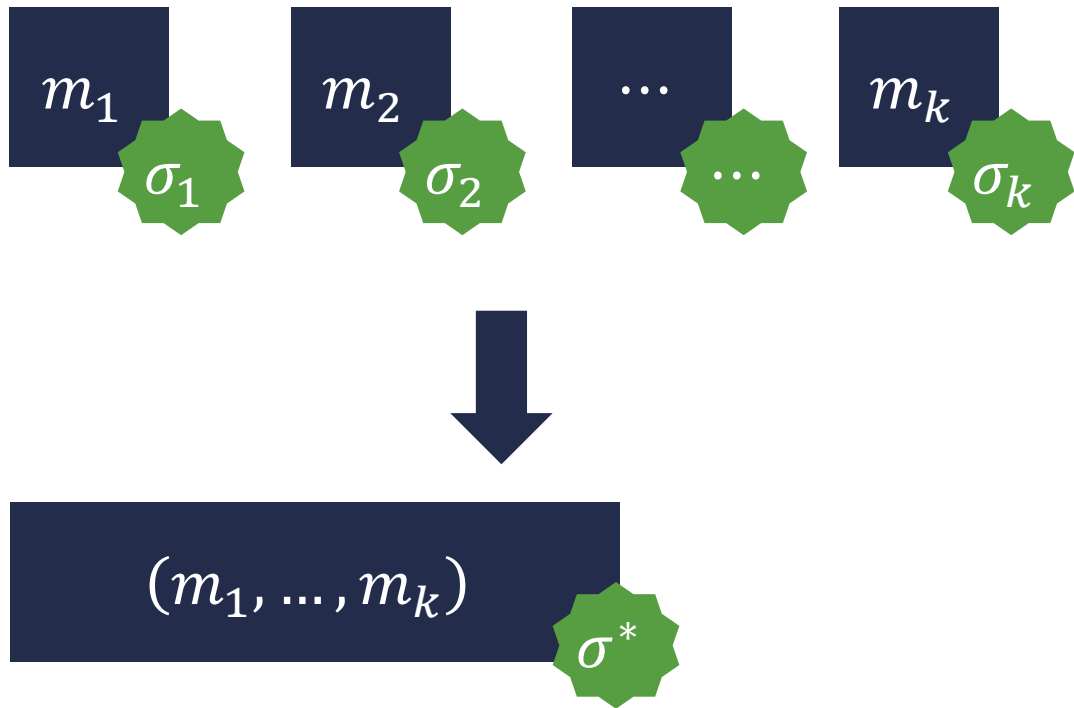
Given k message-signature pairs (m_i, σ_i)

Short signature σ^* on (m_1, \dots, m_k) :
 $|\sigma^*| = \text{poly}(\lambda, \log k)$

Folklore construction from succinct arguments for NP (SNARKs for NP):

prove knowledge of $\sigma_1, \dots, \sigma_k$ such that $\text{Verify}(\text{vk}, m_i, \sigma_i) = 1$

Application to Aggregate Signatures



Given k message-signature pairs (m_i, σ_i)

Short signature σ^* on (m_1, \dots, m_k) :
 $|\sigma^*| = \text{poly}(\lambda, \log k)$

Can replace SNARKs for NP with a batch argument for NP:

prove knowledge of $\sigma_1, \dots, \sigma_k$ such that $\text{Verify}(\text{vk}, m_i, \sigma_i) = 1$

Application to Aggregate Signatures

Can replace SNARKs for NP with a batch argument for NP:

prove knowledge of $\sigma_1, \dots, \sigma_k$ such that $\text{Verify}(\text{vk}, m_i, \sigma_i) = 1$

This work: Batch argument for bounded number of instances

Corollary. Aggregate signature supporting bounded aggregation from bilinear maps

First aggregate signature with **bounded aggregation** from standard pairing-based assumptions (i.e., k -Lin) in the **plain model**

Previous pairing constructions: **unbounded aggregation** from standard pairing-based assumptions in the **random oracle model** [BGLS03]

Summary

Batch arguments for NP from **standard assumptions** over bilinear maps

Key feature: Construction is “**low-tech**”

Direct “commit-and-prove” approach like classic pairing-based proof systems

Corollary: RAM delegation (i.e., “SNARG for P”) with sublinear CRS

Corollary: Aggregate signature with bounded aggregation

<https://eprint.iacr.org/2022/336>

Thank you!