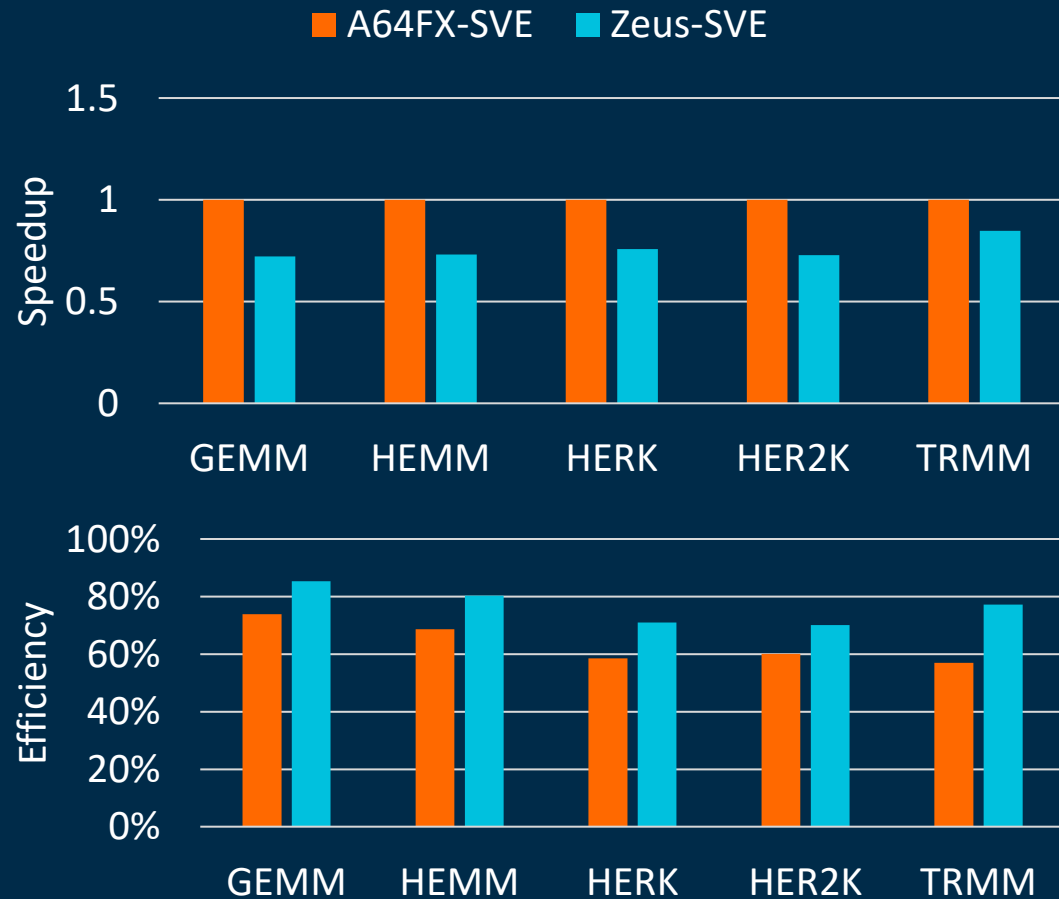


In Conclusion...



- BLIS SVE kernels “port forward” to Neoverse V1
 - Just recompile with appropriate architecture spec
- BLIS on V1 shows great efficiency and good performance for small problem sizes
 - A study of larger problems requires SoC modeling
 - Best done by silicon providers via their own tools
- LLVM and GNU compilers seem to be well prepared for Neoverse V1
 - No performance or correctness issues detected