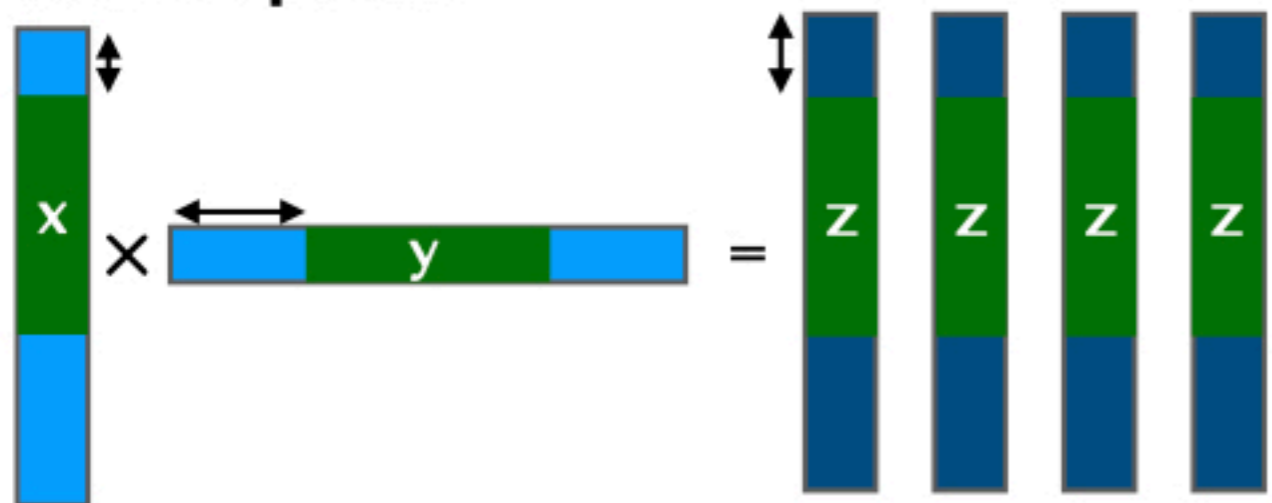


BLIS&TBLIS for Apple M1's Matrix Coprocessor

rank-1 update:



```
mov    x0, x16 ; operand in x0 bitfields
.long  0x2010A0 ; instruction amxfma x0
                ; encoded as machine code
```

