### CALL FOR PAPERS

# International Conference on Formal Methods in Computer-Aided Design (FMCAD'98)

 $\begin{array}{c} {\rm Palo~Alto,~CA,~USA} \\ {\rm 4-6~November~1998} \\ {\rm In~cooperation~with~IFIP~WG~10.5} \end{array}$ 

### Focus and Objectives:

The International Conference on Formal Methods in Computer-Aided Design '98 (FMCAD'98) is a forum for presenting state-of-the-art tools and techniques based on formalized reasoning for computer-aided design of hardware, and comes two years after the previous (and first) event in the FMCAD series, which received overwhelming response. The location of the conference provides a unique opportunity for researchers in the field to interact with engineers from the Silicon Valley semiconductor and CAD companies. In addition, FMCAD'98 will be held immediately prior to ICCAD'98 for the convenience of people wanting to attend both conferences. The objective is to cover all relevant formal aspects of work in computer-aided system design including verification, synthesis and testing. The conference will cover original research in this area, as well as case studies, technology transfer, and other practical experiments. A special focus will be on tool demos and embedded tutorials. It is intended to publish the Proceedings in time for distribution at the conference in the Springer LNCS series. Specific technical areas of FMCAD'98 include, but are not limited to:

- Hardware verification techniques based on model checking, theorem proving, and related or hybrid methods,
- Correct by construction approaches to hardware design, such as synthesis and transformation,
- Hybrid approaches that integrate synthesis and verification or different verification techniques,
- Integration of formal methods with CAD tools, such as for synthesis, simulation, design exploration, and testing,
- Formalized reasoning techniques supporting hardware- and system-level description languages,
- Case studies and application of formal methods in industry.

## **Invited Speakers:**

- Ken McMillan, Cadence Berkeley Labs, USA
- Carl Seger, Intel Corporation, USA
- Richard Platek, Cornell University, and Consultant, ORA Corp., USA
- Amir Pnueli, Weizmann Institute, Israel

**Papers:** 18-page, 12-point font for evaluation, with abstract. Simultaneous submission to other conferences or journals and submission of previously published material are not allowed.

**Tutorials and Tool Demos:** 7-page, 12-point font abstract for evaluation. We encourage presentations of tools on a suitable set of completely worked out examples.

**Submit** your papers via the FMCAD web-page http://lal.cs.byu.edu/fmcad/. Email to fmcad@lal.cs.byu.edu for paper and program inquiries.

#### **Important Dates:**

Submission deadline (firm) April 20, 1998
Notification of acceptance June 20, 1998
Camera ready copy of accepted papers Aug 1, 1998

# General Chairs: Ganesh Gopalakrishnan (Univ. of Utah) and Phillip Windley (Brigham Young Univ.) Local Arrangements: Jens Skakkebæk (Stanford University)

#### **Program Committee:**

M. Aagaard (Intel, USA) M. Fujita (Fujitsu, USA) R. Bryant (CMU, USA) S. German (IBM, USA) J. Burch (Cadence, USA) T. Henzinger (Berkeley, USA) A. Camilleri (HP, USA) R. Hojati (HDAC and Berkeley, USA) E. Cerny (U. Montreal, Canada) A. Hu (UBC, Canada) S.-K. Chin (Syracuse U., USA) W. Hunt (IBM Austin, USA) C.-T. Chou (Intel, USA) S. Johnson (Indiana U., USA) F. Corella (HP, USA) C. Delgado Kloos (U. Carlos III, Spain) L. Fix (Intel, Israel) T. Kropf (U. Karlsruhe, Germany)

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T. Shiple (Synopsys, USA) J. Skakkebæk (Stanford U.) M. Srivas (SRI International, USA) R. Vemuri (U. Cinncinnati, USA) J. Van Tassel (Texas Instruments, USA)