

FP in HOL

The story so far

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Higher Order Logic

- Logic built on top of typed lambda calculus
- Originally due to Church (1940s)
- First implemented by Gordon (early 1980s), by adapting LCF implementation of Milner and colleagues
- Now we have HOL-4, HOL-Light, ProofPower, and Isabelle/HOL, all vital systems
- Basically a kind of typed set theory that builds in functions
- Not clear that HOL has anything to do with FP.

- 1980s
- Gordon's initial work developed some basic types (numbers, pairs, lists) sufficient to do hardware verification examples.
- Melham (thesis) implemented a package for definition of inductive datatypes
- Each such definition provided induction and a so-called *primitive recursion* principle

Theorem (Primitive Recursion Theorem for lists)

```
|- !e f. ?!fn. (fn [] = e) /\  
                (!n. fn (h::t) = f h t (fn t))
```

- Illustrates standard methodology for HOL developments : do not extend logic with new axioms, but instead derive tools on top that use inference to mechanize general theorems
- For example,

Definition (Prim. Rec. FLAT)

```
|- (FLAT [] = []) /\  
   (FLAT(h::t) = h ++ FLAT t)
```

is introduced by constructing appropriate e and f instances for the P.R. theorem and then deriving the specified equations

- Time consuming and possibly slow,
- *BUT* cuts down on soundness bugs, and gives a nice assurance story
- Reasoning about prim. rec. functions over inductive datatypes supported by custom induction principles.

- Good start
- BUT, from the point of view of FP'ers this is an impoverished setting in which to write programs:
 - Only very simple patterns
 - Only very restricted kinds of recursion
 - Numerous other irritations
- In 1990s systems emerged that dealt with some of these problems
- Fourman's **LAMBDA** system (defunct)
- **TFL**
 - Complex patterns
 - Arbitrary recursion (termination proofs required)
 - Per-function induction principles (following Boyer and Moore)

- Based on

Theorem (Wellfounded Recursion theorem)

$$\vdash \mathbf{WF} R \wedge (f = \mathbf{WFREC} R M) \Rightarrow \exists f. \forall x. f(x) = M(f \upharpoonright_{Rx}) x$$

- Works by instantiating and manipulating WF Rec. thm (proved in OL)
- A parameterized implementation, instantiated to HOL-4 and Isabelle/HOL
- Handles deep patterns, e.g. Okasaki-style Red-Black trees
- Deals well with mutually recursive functions
- Deals with nested recursive functions, but not well (since improved by Matthews and Krstic, and recently by Krauss)

The following version of **FLAT** has more complex patterns and also needs a termination proof in order to be admitted.

Definition (FLAT)

```
|- (FLAT [] = []) /\
   (FLAT ([]::rst) = FLAT rst) /\
   (FLAT ((h::t)::rst) = h :: FLAT (t::rst))
```

Theorem (FLAT induction)

```
|- !P. P [] /\
    (!rst. P rst ==> P ([]::rst)) /\
    (!h t rst. P (t::rst) ==> P((h::t)::rst))
==>
!list. P list
```

Typical exercise

Depth first *fold* with graph represented as a function of type

$$\alpha \rightarrow \alpha \text{ list}$$

which takes a node and delivers the children of the node.

$$\mathbf{DFS\!p} : (\alpha \rightarrow \alpha \text{ list}) \rightarrow (\alpha \rightarrow \beta \rightarrow \beta) \rightarrow \alpha \text{ list} \rightarrow \alpha \text{ list} \rightarrow \beta \rightarrow \beta$$

$$\mathbf{DFS\!p} \ G \ f \ \text{seen} \ [] \ \text{acc} = \text{acc}$$

$$\mathbf{DFS\!p} \ G \ f \ \text{seen} \ (h :: t) \ \text{acc} =$$

if **mem** h *seen*

then **DFS $\!p$** G f *seen* t acc

else **DFS $\!p$** G f $(h :: \text{seen})$
 $(G \ h \ ++ \ t)$
 $(f \ h \ \text{acc})$

Depth-first search

- Folds function f over directed, possibly cyclic graph
- Applies f to each node and accumulating parameter acc
- By instantiating f can get map, search, max, filter, *etc* functions for such graphs
- Perfectly acceptable functional program

Depth-first search

- The functional representation of the graph allows infinite graphs
- Makes the function partial (if graph has an infinite number of reachable nodes)
- For example $\lambda x.[x + 1]$
- HOL only supports total functions so **DFS_p** wouldn't be admitted
- How to repair (totalize)?

Depth-first search repaired

The fold will always terminate given a finite set of reachable nodes. How to define reachability?

Definition (Reachability)

$$\mathbf{R}_G x y \equiv \mathbf{mem} y (G x)$$
$$\mathbf{reach}_G \equiv \mathbf{RTC} R_G$$
$$\mathbf{reachlist}_G nodes y \equiv \exists x. \mathbf{mem} x nodes \wedge \mathbf{reach}_G x y$$

Thus, we want to constrain **DFSp** by finiteness of nodes reachable from root nodes of graph.

DFS : $(\alpha \rightarrow \alpha \text{ list}) \rightarrow (\alpha \rightarrow \beta \rightarrow \beta) \rightarrow \alpha \text{ list} \rightarrow \alpha \text{ list} \rightarrow \beta \rightarrow \beta$

```
DFS G f seen to_visit acc =  
  if Finite (reachlistG to_visit)  
    then case to_visit  
      of []  $\Rightarrow$  acc  
       | (h :: t)  $\Rightarrow$   
         if mem h seen  
           then DFS G f seen t acc  
           else DFS G f (h :: seen)  
                (G h ++ t)  
                (f h acc)  
    else ARB
```

$\vdash \mathbf{DFS} \ G \ f \ \text{seen} \ [] \ \text{acc} = \text{acc}$

Finite ($\text{reachlist}_G \ (h :: t)$)

$\vdash \mathbf{DFS} \ G \ f \ \text{seen} \ (h :: t) \ \text{acc} =$
if **mem** $h \ \text{seen}$
then **DFS** $G \ f \ \text{seen} \ t \ \text{acc}$
else **DFS** $G \ f \ (h :: \text{seen})$
 $(G \ h \ ++ \ t)$
 $(f \ h \ \text{acc})$

Termination

- In first recursive call, list of seen nodes doesn't change, but nodes still to visit shrinks
- In second recursive call, seen nodes gets bigger and nodes to visit can increase in size
- So simple measures don't work.
- Recall that the set of reachable nodes is finite.
- **Idea.** Let \prec be the lexicographic combination of the number of reachable nodes not yet seen and the number of nodes in *to_visit*.

Definition

$$\begin{aligned} & (G, f, \text{seen}', \text{to_visit}', \text{acc}') \prec (G, f, \text{seen}, \text{to_visit}, \text{acc}) \\ & \text{iff} \\ & (\|\mathbf{reachlist}_G \text{to_visit}' \setminus \mathbf{ListToSet} \text{seen}'\|, \mathbf{length} \text{to_visit}') <_{\text{lex}} \\ & (\|\mathbf{reachlist}_G \text{to_visit} \setminus \mathbf{ListToSet} \text{seen}\|, \mathbf{length} \text{to_visit}) \end{aligned}$$

- In first recursive call h has been previously seen, so the set of unseen reachable nodes does not change, and t is smaller than $h :: t$.
- In the second call, h is added to the seen list, and all of the nodes reachable from the children of h are also reachable from h itself. Thus the set of reachable nodes gets no additions, and since the addition to the seen list was previously reachable the size of the calculated set decreases.

$\forall P.$

$$\left(\begin{array}{l} \forall G f s h t a. \\ P G f s [] a \wedge \\ \left(\begin{array}{l} (\text{Finite } (\text{reachlist}_G (h :: t)) \wedge \text{mem } h s \Rightarrow P G f s t a) \wedge \\ (\text{Finite } (\text{reachlist}_G (h :: t)) \wedge \neg \text{mem } h s \\ \Rightarrow P G f (h :: s) (G h ++ t) (f h a)) \\ \Rightarrow P G f s (h :: t) a \end{array} \right) \end{array} \right)$$

\Rightarrow

$$\forall v v_1 v_2 v_3 v_4. P v v_1 v_2 v_3 v_4$$

What does it mean for this fold on graphs to be correct?

- All reachable nodes are visited
- No unreachable nodes are visited
- No reachable node is visited twice

How, though, do we capture the notion of *visits*?

- We capture this notion by using **cons** as the folding function given to **DFS**, so that the returned list is just the visited nodes.

DFS with folding function f is equal to gathering all the visited nodes and then folding f over the resulting list.

Theorem (DFS Fold)

$$\begin{aligned} \text{Finite } (\text{reachlist}_G \text{ to_visit}) &\Rightarrow \\ \text{DFS } G \ f \ \text{seen} \ \text{to_visit} \ \text{acc} & \\ = & \\ \text{foldr } f \ \text{acc} \ (\text{DFS } G \ \text{cons} \ \text{seen} \ \text{to_visit} \ []) & \end{aligned}$$

With this understanding, it suffices to prove that the invocation **DFS** G **cons** *seen* to_visit $[\]$ contains no duplicate entries, contains each node reachable from to_visit , and contains no nodes not so reachable. The first property is

Theorem (DFS Distinct)

Finite ($reachlist_G\ to_visit$)
 \Rightarrow **all_distinct** (**DFS** G **cons** *seen* to_visit $[\]$)

and the other two are phrased as

Theorem (DFS Reach)

Finite ($reachlist_G\ to_visit$) \Rightarrow
 $\forall x. reachlist_G\ to_visit\ x$
 \Leftrightarrow
mem x (**DFS** G **cons** $[\]\ to_visit\ [\]$)

- Possible to go on and instantiate the various parameters of DFS to get various simplifications
 - Simpler constraint assuring (but not characterizing) termination : finite number of parent nodes in graph
 - DFS with adjacency lists

Adjacency lists

- An adjacency list

$A : (\alpha \times \alpha \text{ list}) \text{ list}$

gives a listing of nodes alongside their children.

- **toGraph** converts an adjacency list into a graph.

Definition (**toGraph**)

```
toGraph al n =  
  case filter ( $\lambda(x, \_). (x = n)$ ) al  
  of []  $\rightarrow$  []  
   | ( $\_, x$ ) :: t  $\rightarrow$  x
```

- Can then prove that DFS terminates when called on any graph derived from an adjacency list

```
⊢ DFS (toGraph A) f seen [] acc = acc
  DFS (toGraph A) f seen (h :: t) acc =
    if mem h seen
      then DFS (toGraph A) f seen t acc
      else DFS (toGraph A) f (h :: seen)
              ((toGraph A) h ++ t)
              (f h acc)
```

- Fun tutorial study
- Formalization challenges (partiality, termination, *visits*, ...)
- Need to do math in order to work with such programs (e.g., reachability)
- Possibly of future use; could be added to a library

Current state of affairs

- Programming total functions over inductive datatypes is pretty well handled in most proof systems
- Could always be improved, of course
- Isabelle/HOL has a nice development of domain theory for applications that need it.
- But domains make life more complicated (lifting)
- Support for lazy datatypes and functions over them, not using domains, was pioneered by John Matthews, but is still not mechanized well in any HOL implementation.
- HOL systems have only simple types, and there doesn't seem to be much momentum for supporting more expressive type systems.

Critique and a Response

- Functions essentially trapped inside the formal system (“Case studies are boring”)
- Sterile environment?
- But ACL2 community has shown that breaking free of the proof system is possible
- Emitting formal programs into outside world has many benefits
- Other systems have followed: PVS, Isabelle/HOL, HOL-4 all provide export for formal programs
- Coq has a variety of solutions, both internal and external
- Other systems (e.g., Matlab) also export programs and/or hardware

What then?

- Observation: programs are exported to the metalanguage
- Lisp for ACL2, ML for Isabelle/HOL and HOL
- Nice research project: export to mainstream languages like Java or even C
- However, current program export facilities exploit the fact that the conceptual gap between the formal program and the host PL is small
- But what if we want to export to Java, C, or even hardware?
- End up re-capitulating phases of compilation
- But then the small gap gets ever wider ...

- Our current research investigates ways to
 - specify functional programs as mathematics
 - prove correctness properties at the mathematics level
 - translate to assembly or hardware
 - translation **done by proof**, so result is guaranteed to return the correct answers.
- Amounts to *compilation of logic functions*, inside the logic
- Two approaches to providing this:
 - Verified compiler. This is what is done traditionally
 - Translation validation. Recent alternative proposed by Pnueli

- Have built two prototype TV compilers for a very simple functional language
 - hardware (with Mike Gordon)
 - ARM assembly (with Owens, Li, Tuerk)
- Work is still very much in progress
- Target example: Elliptic Curve Crypto (relatively efficient replacement for RSA)
 - Formal theory of elliptic curves (on top of finite field theory)
 - Define recursive functions that implement, e.g., addition of points on elliptic curves
 - Compile these to ARM assembly
 - formal ARM model in HOL-4

One approach

- Try to do as much as possible by source-to-source translations.
- Start by translating to combinator form, then to ANF (administrative normal form)
- These end up being *semantic* versions of the standard syntax bashing done in CPS translation
- Register allocation done by standard graph-colouring algorithm. Used to deliver an α -convertible version (nice trick from Jason Hickey)
- Maintenance of equality, by proof, from starting program
- That's the front end

\vdash **Rounds** $(n, (y, z), (k_0, k_1, k_2, k_3), s) =$
 if $n = 0w$ then $((y, z), (k_0, k_1, k_2, k_3), s)$
 else **Rounds** $(n - 1w,$
 let $s' = s + 2654435769w$ in
 let $y' = y + \mathbf{ShiftXor}(z, s', k_0, k_1)$
 in $((y', z + \mathbf{ShiftXor}(y', s', k_2, k_3)), (k_0, k_1, k_2, k_3), s')$

After Front-end processing

```
⊢ Rounds( $r_0, (r_8, r_5), (r_4, r_3, r_2, r_6), r_7$ ) =  
  let  $v_9 = (\text{op } =) (r_0, 0w)$   
  in if  $v_9$  then  $((r_8, r_5), (r_4, r_3, r_2, r_6), r_7)$   
    else let  $m_2 = (\text{op } -) (r_0, 1w)$  in  
      let  $m_4 = (\text{op } +) (r_7, 2654435769w)$  in  
        let  $r_1 = \text{ShiftXor } (r_5, m_4, r_4, r_3)$  in  
          let  $r_9 = (\text{op } +) (r_8, r_1)$  in  
            let  $r_1 = \text{ShiftXor } (r_9, m_4, r_2, r_6)$  in  
              let  $r_1 = (\text{op } +) (r_5, r_1)$  in  
                let  $((m_5, m_3), (m_1, m_0, m_6, r_1), r_0) =$   
                  Rounds ( $m_2, (r_9, r_1), (r_4, r_3, r_2, r_6), m_4$ )  
                in  $((m_5, m_3), (m_1, m_0, m_6, r_1), r_0)$ 
```

- Back end proof *synthesizes* a counterpart function to the front end function. Then a tactic is executed to show the two are equal.
- In general, the backend is pretty conventional compiler verification technology
- We *synthesize* the IL semantics from the ARM semantics, rather than relating two operational semantics
- Main difficulty is dealing with memory
- In particular, function call is hard.

- To be done in near future: defunctionalization to support higher order
- Not mentioned: work on translating FP by proof to hardware
- Early days in this area, but I think it's quite exciting

- **TFL** supports *recursion schemes*, by allowing free variables in rhs, for example

Definition (While-loops)

While $s = \text{if } B \text{ s then } \mathbf{While} (C \text{ s}) \text{ else } s.$

- **While** can also be defined directly
- Connection with FP: Lewis, Shields, Meijer, Launchbury, *Implicit parameters: Dynamic scoping with static types*

- Polytypism (type-indexed functions) is becoming a basic FP tool
- Applications in logic:
 - Termination proofs
 - Normalization by Evaluation
 - Translation between representations (mapping to binary format, to SAT, to LISP, *etc*)
- In HOL systems, two ways to support it:
 - A polytypic function f is represented by a meta-level function parameterized by a P.R. theorem (HOL-4).
 - Explicit definitions over type structure (Isabelle/HOL).

- Actually, not so recent ...
- Used extensively in Isabelle/HOL, but not the other HOL systems
- Supports some abstract algebra and number theory hierarchies
- Recent work from CMU translates type classes to ML functors, offering a way to map formal developments from Isabelle/HOL to HOL-Light

THE END