

Mechanically-Verified Validation of Satisfiability Solvers

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The University of Texas at Austin

Dissertation Proposal
October 18, 2013

Outline

- Motivation and Proposal
- Satisfiability and Proofs
- Task 1: Designing a Proof Format
- Task 2: Developing an Efficient Checker
- Task 3: Proving Correctness
- Timeline and Conclusion

Motivation

Satisfiability solvers are used in amazing ways...

- Hardware verification: Centaur x86 verification
- Combinatorial problems:
 - van der Waerden numbers
[Dransfield, Marek, and Truszczynski, 2004]
 - Gardens of Eden in Conway's Game of Life
[Hartman, Heule, Kwekkeboom, and Noels, 2013; Kouril and Paul, 2008]
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..., but satisfiability solvers have errors.

- Documented bugs in SAT, SMT, and QBF solvers
[Brummayer and Biere, 2009; Brummayer et al., 2010]
- Competition winners have contradictory results
(HWMCC winners from 2011 and 2012)
- Implementation errors often imply conceptual errors

Solutions

Verify SAT solvers

- Requires verification of all crucial search techniques
- Delicate balance between efficiency and ease of verification
- Verification proofs are specific to each solver
- New developments in solving require additional proof effort

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Validate SAT solver output

- Emit “proof” of unsatisfiability from SAT solver
- A single proof checker can validate results from many state-of-the-art solvers
- Proof checker uses limited number of techniques and can be mechanically verified

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For my dissertation, I will develop a fast mechanically-verified satisfiability proof checker using ACL2.

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This project has three tasks:

1. Design a suitable proof format,
2. Implement an efficient proof checker for the format, and
3. Demonstrate a proof of correctness for the proof checker.

Proof Properties

Easy to Emit

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■ Resolution Proofs

[Zhang and Malik, 2003]

[Van Gelder, 2008]

[Biere, 2008]

■ ■ with Verified Checker

[Weber, 2006, and Amjad, 2009] (Isabelle/HOL)

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DRUP (DRUP-Trim)

[Heule, Hunt, Jr., and **Wetzler**, STVR 201X]

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RAT Proofs

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Proposed Work

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- Motivation and Proposal
- **Satisfiability and Proofs**
- Task 1: Designing a Proof Format
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Satisfiability

Is there an assignment of values to variables such that a given Boolean formula evaluates to TRUE?


- $(x_1 \vee x_2 \vee \neg x_3) \wedge$
- $(\neg x_1 \vee \neg x_2 \vee x_3) \wedge$
- $(x_2 \vee x_3 \vee \neg x_4) \wedge$
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Determining unsatisfiability is more difficult.


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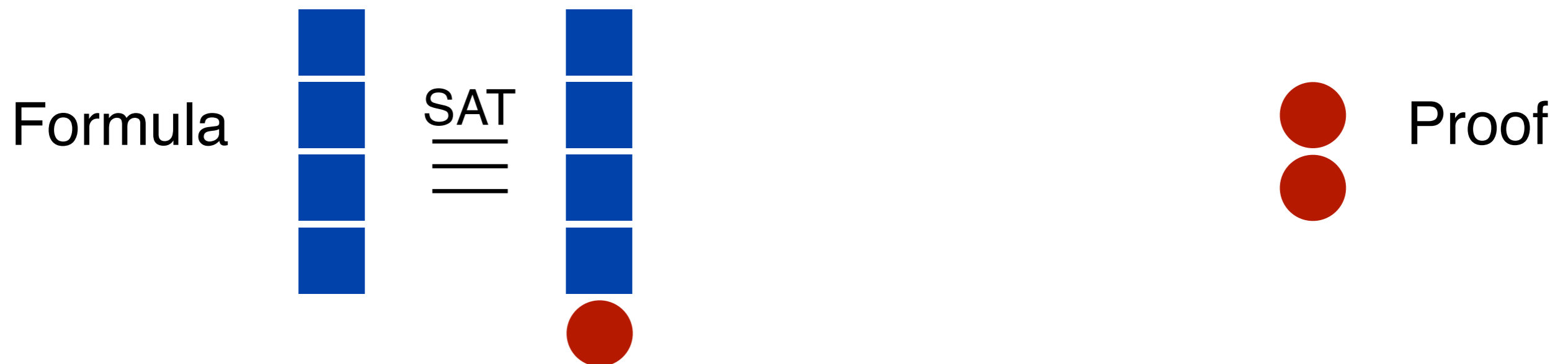
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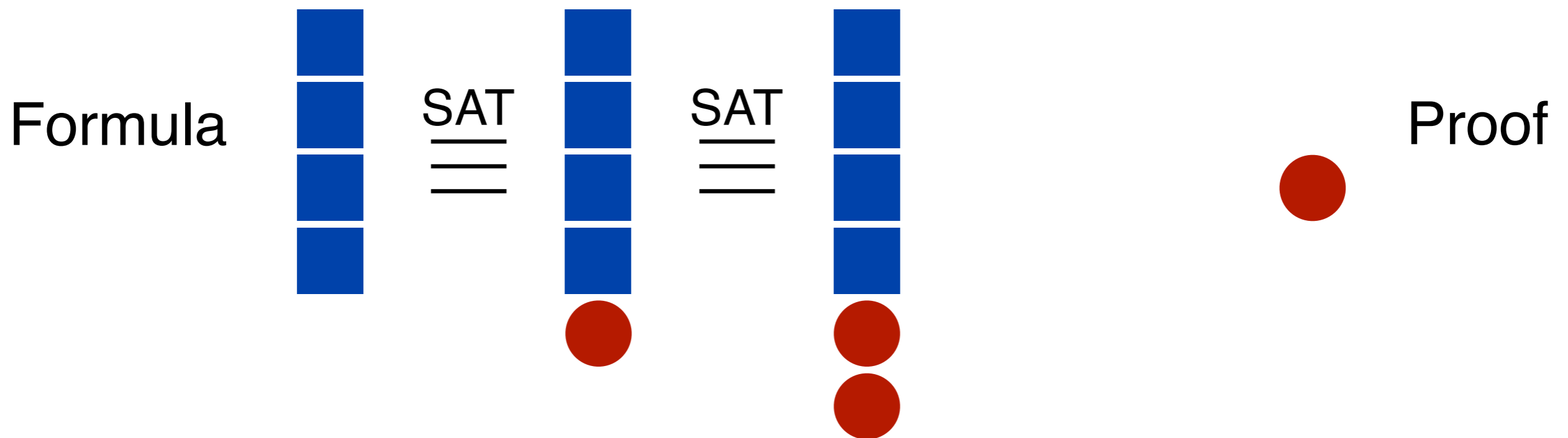
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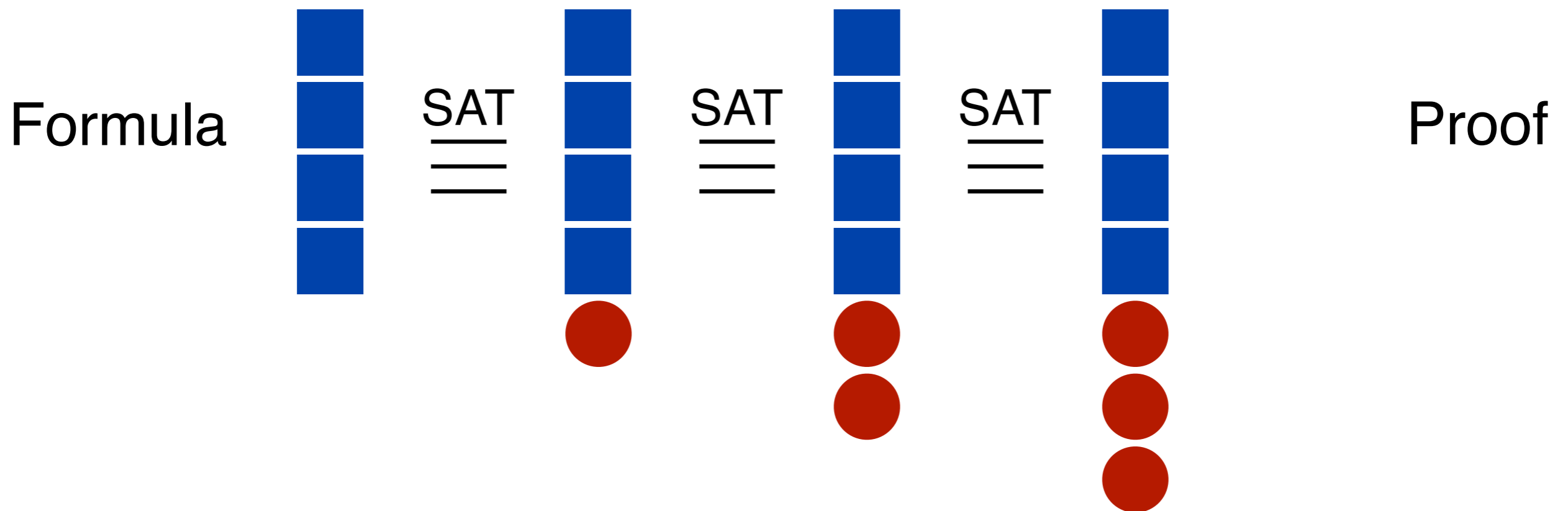
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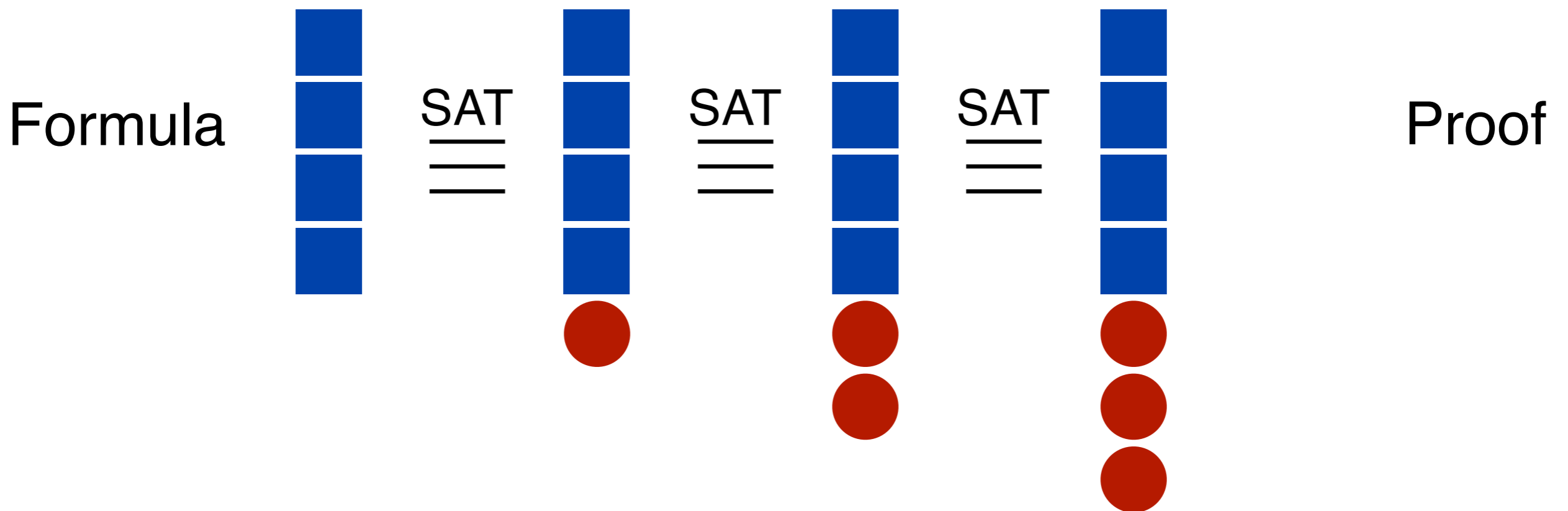
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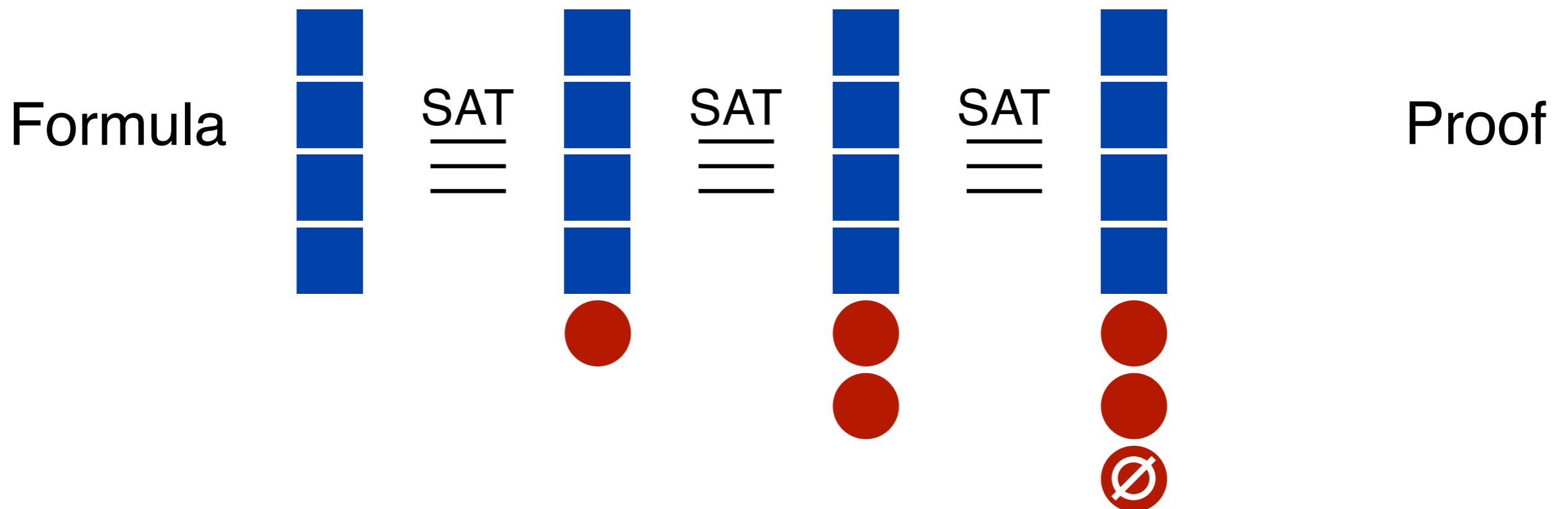


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Use clausal proofs as a foundation with two extensions:

- Add deletion information
- Extend equivalence from logical to satisfiability

Extension 1: Deletion Information























Proofs can be extended with clause deletion information.

- Solvers remove clauses during search
- Remove unnecessary clauses during validation
- Emit learning and deletion information
- New format called DRUP (**D**eletion **RUP**)

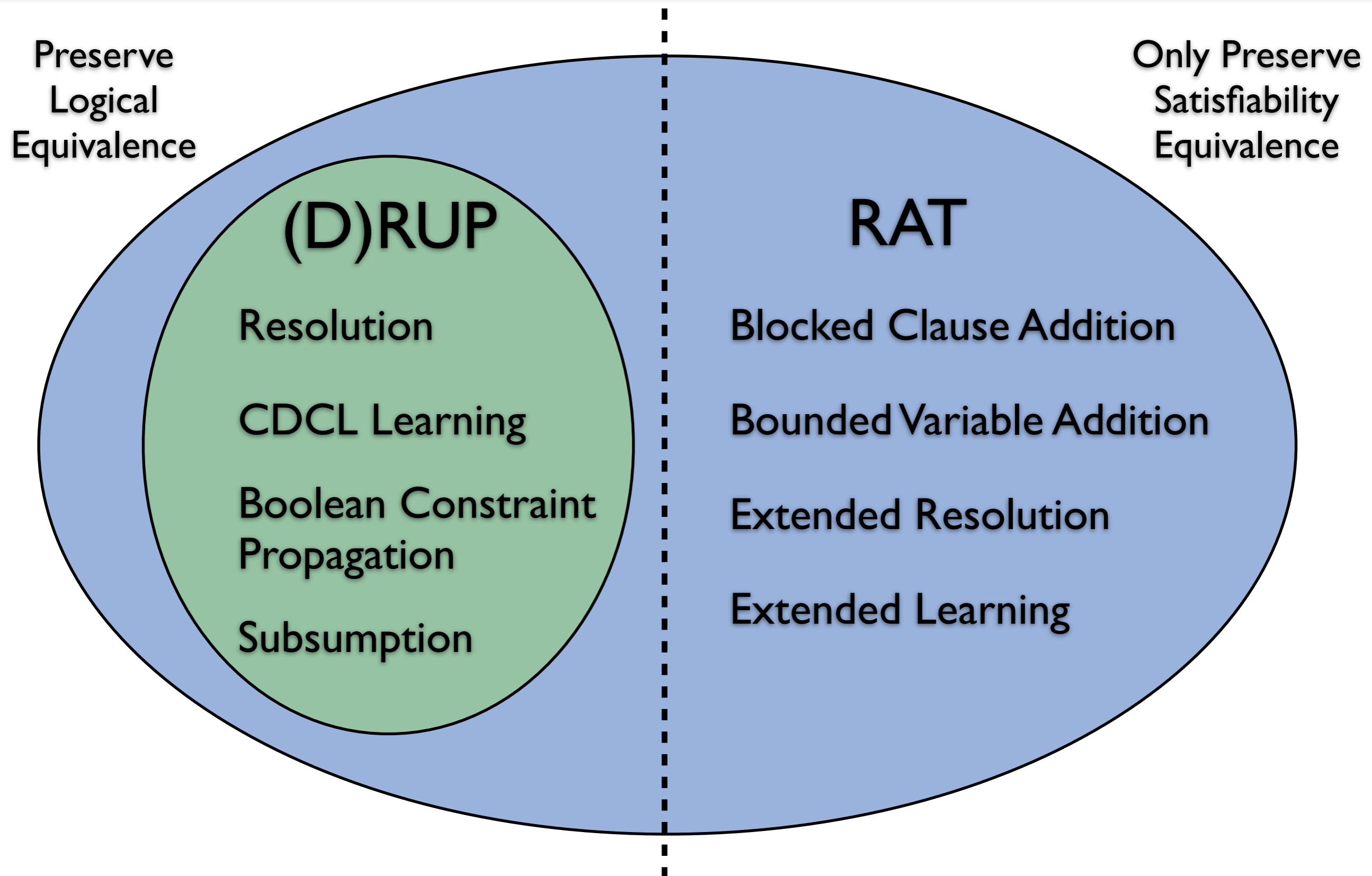
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		-1	-2	3	0			-1	0				d	-1	2	4	0
		2	3	-4	0			2	0				-1			0	
		-2	-3	4	0				0				d	-1	-2	3	0
		1	3	4	0								d	-1	-3	-4	0
		-1	-3	-4	0								d	-1	2		0
		1	-2	-4	0									2			0
		-1	2	4	0								d	1	2	-3	0
													d	2	3	-4	0
																	0



























Extension 2: Expressiveness



DRAT Format

The DRUP and RAT proof formats can be combined.

- How will the two formats interact?
- With what frequency are RAT clauses produced?
- Will the addition of RAT clauses lead to more deletions?

CNF		1	2	-3	0	DRUP		-1	2	0	DRAT		-1	0				
		1	2	-3	0			-1	2	0			-1	0				
		-1	-2	3	0			d	-1	2	4	0		d	-1	2	4	0
		2	3	-4	0			-1		0			d	-1	-2	3	0	
		-2	-3	4	0			d	-1	-2	3	0		d	-1	-3	-4	0
		1	3	4	0			d	-1	-3	-4	0		2			0	
		-1	-3	-4	0			d	-1	2		0		d	1	2	-3	0
		1	-2	-4	0			2		0			d	2	3	-4	0	
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- Validate proofs in a time similar to solving
- Performance without full range of solver techniques

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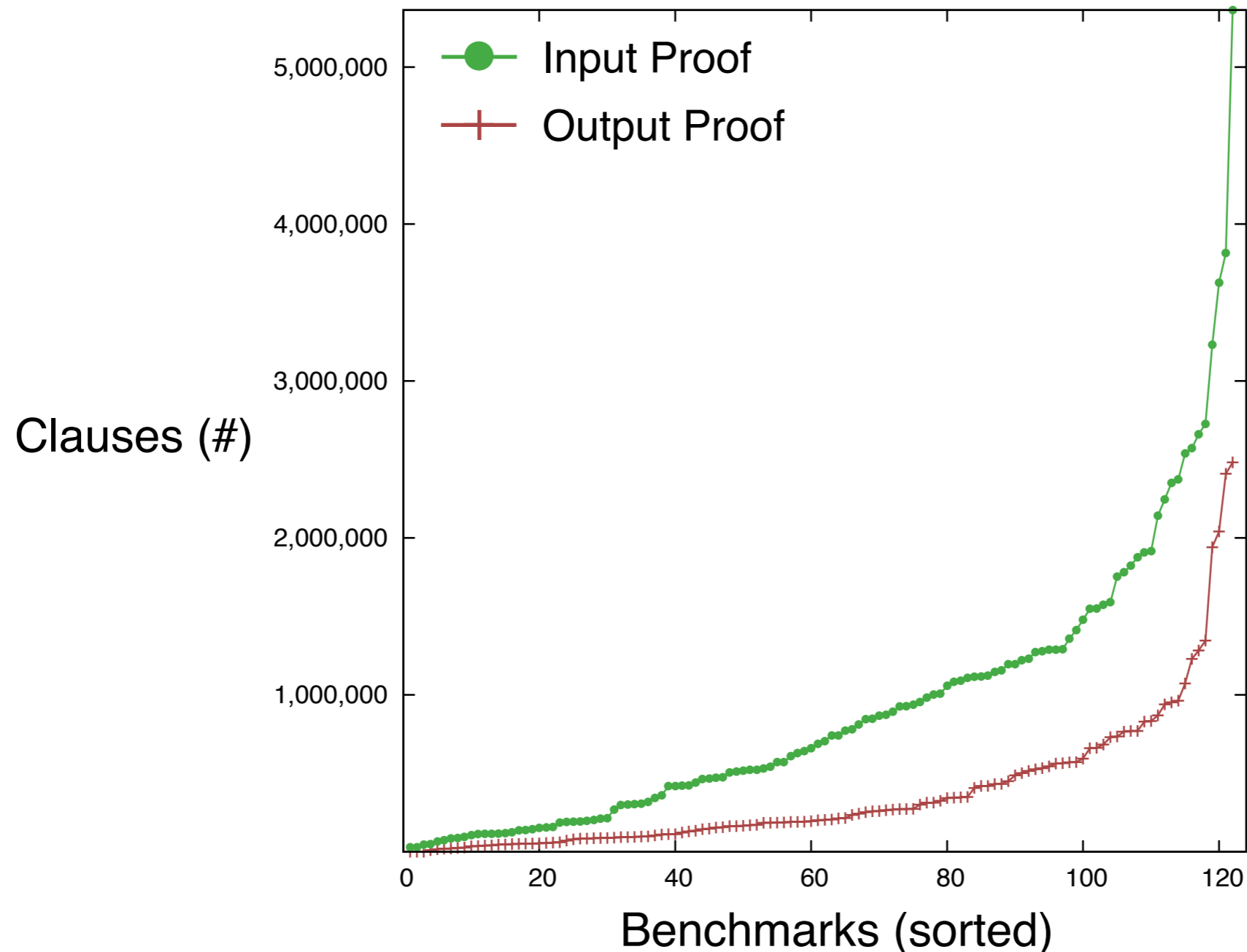
- Validate proofs in a time similar to solving
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Several techniques to gain performance.

- Proofs can be trimmed before validated
- Efficient Boolean constraint propagation
- Constant-time, indexed memory access and update

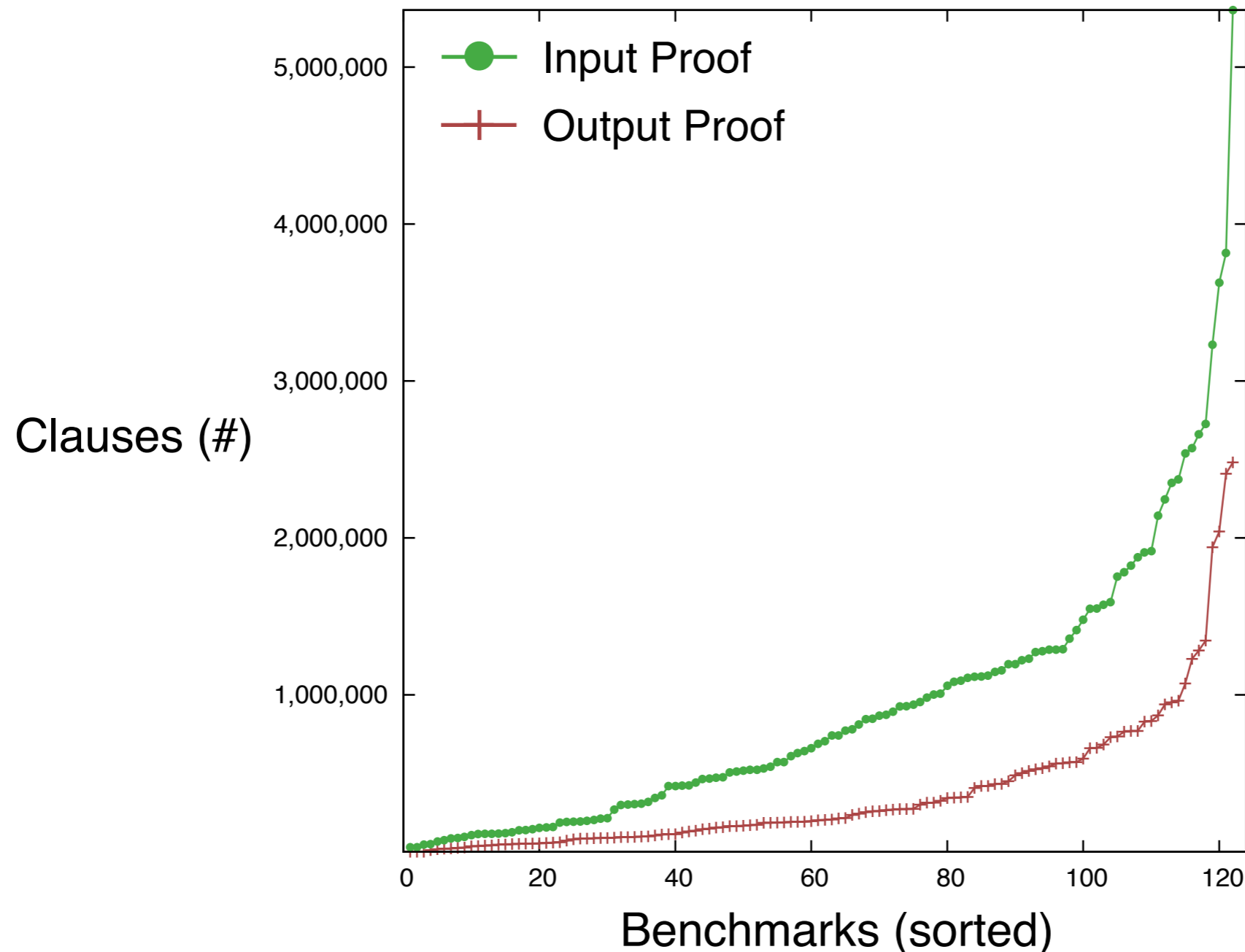
Proof Trimming

Proofs often contain clauses that are unnecessary.
Our DRUP-trim tool trims (and checks) proofs.



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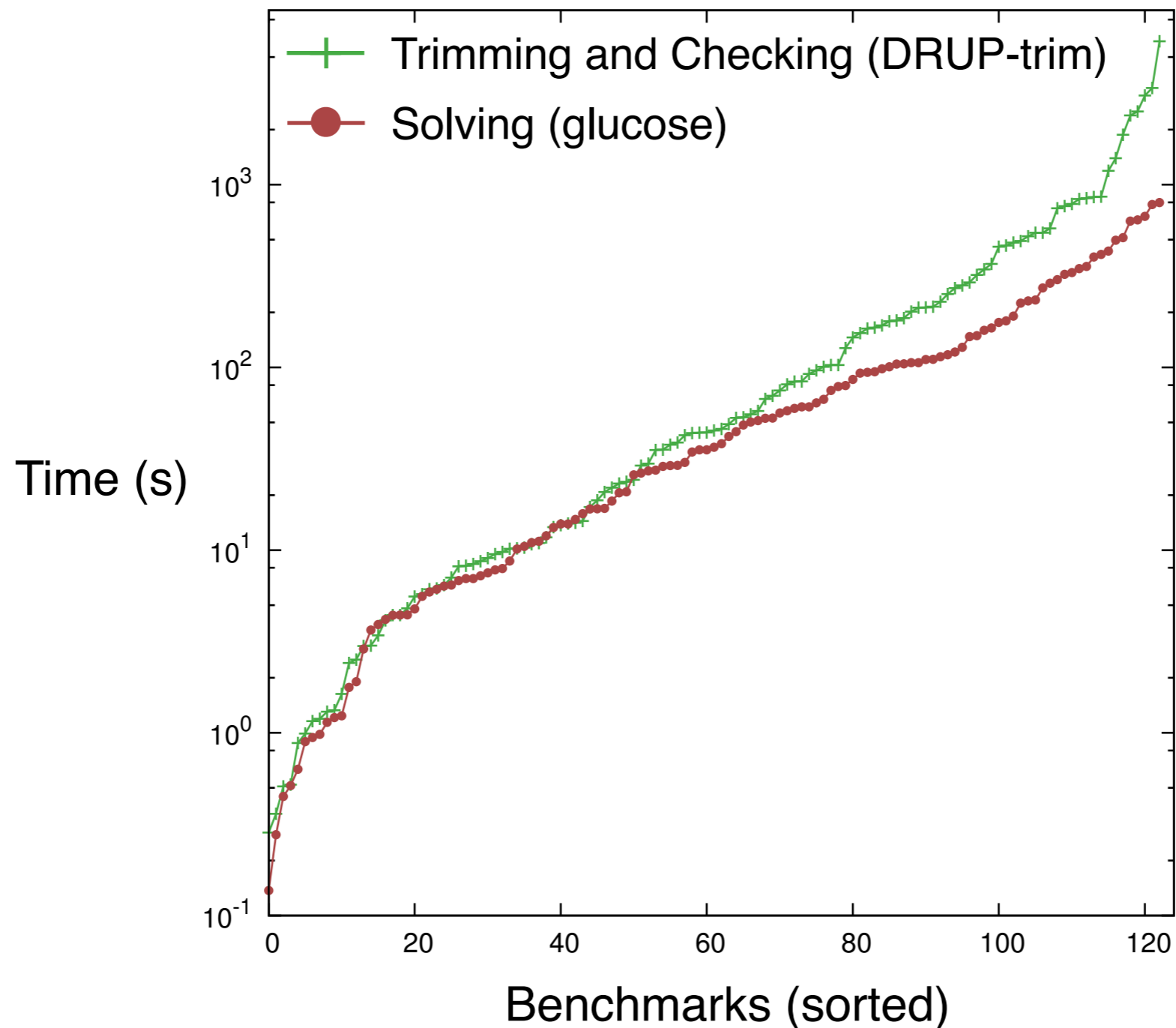
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Adds optimal deletion information.

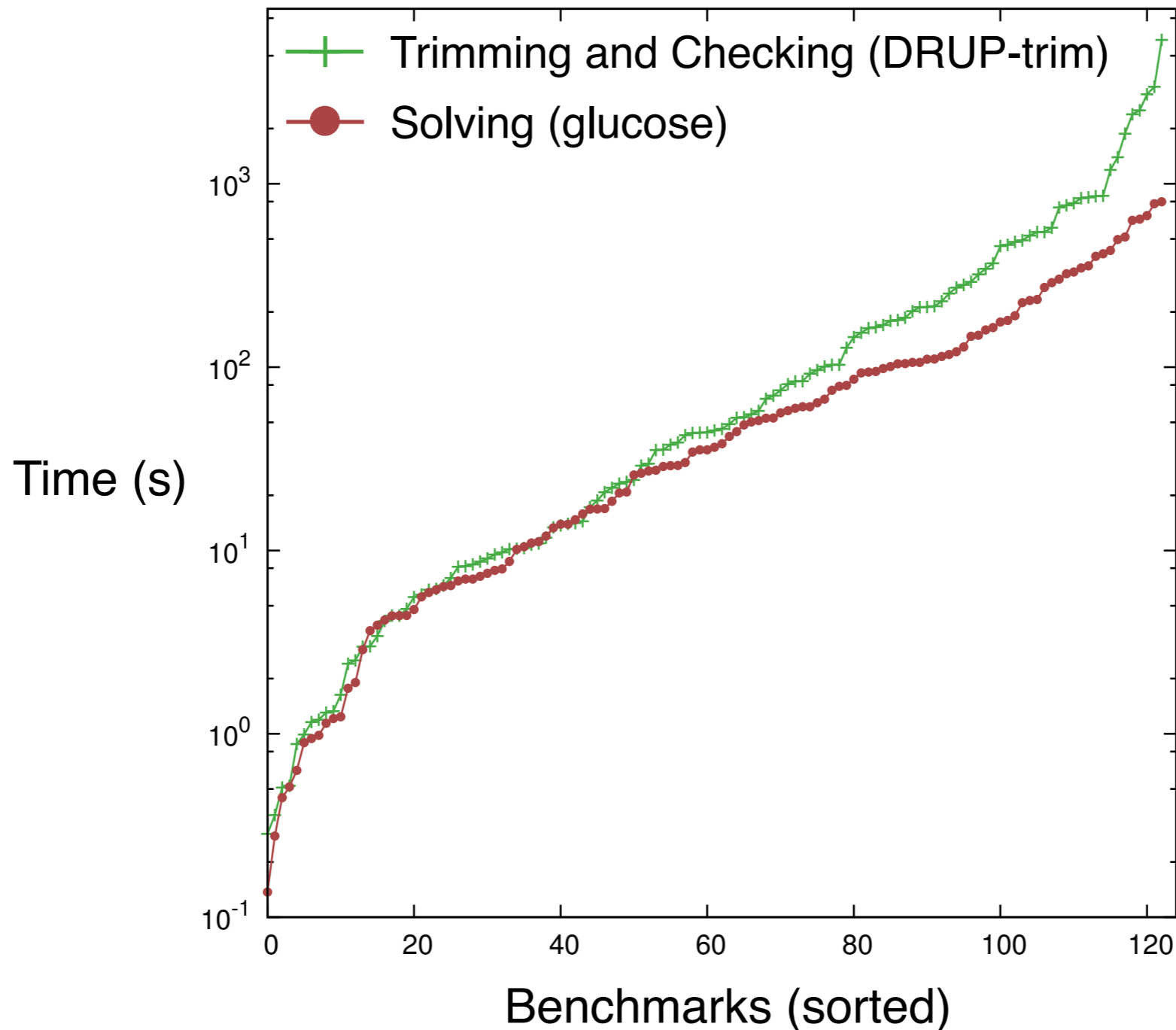
Fast Proof Checking

DRUP-trim is able to closely match solving time.



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Used to check unsatisfiability results from SAT Competition 2013.

Fast Boolean Constraint Propagation

Clausal proof checkers spend around 95% of their time performing Boolean Constraint Propagation.

- Core technique in solvers
- Often implemented using a watched-literal data structure

Watched-Literal Invariant:

All clauses are satisfied or contain at least two unassigned literals.

This is just one of many implementation techniques that must be verified.

Efficiency of ACL2 Code

Typical ACL2 list-only data structures are not efficient.

- Access and update are linear time operations

Instead, one can:

- Mimic array-like structures using STOBJS
- Disassemble key functions to compare compiled code to a highly optimized version

We have implemented a basic RUP proof checker in ACL2 that achieves roughly 60% of a similar proof checker written in C.

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Task 3: Proving Correctness

Interactive theorem provers assist with verification.

- ACL2 combines a programming language, first-order logic, and theorem prover
- Proof checker is modeled in ACL2
- Specification for termination and soundness (but not completeness) are formalized
- Efficient execution by way of Common LISP compilers

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Incremental approach to proof process:

- Prove correctness of proof checkers for different formats
- Refine code to resemble C-equivalent

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Verified SAT solvers and proof checkers using ACL2.

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Litany of transformations and refinements eventually resulting in code that corresponds to our C code.

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Timeline

October 2013

May 2014

Fast RAT
Checker in ACL2

Verified “Flat” RAT
Checker in ACL2

Connect Fast and
“Flat” RAT Checkers

Design and Testing
of DRAT Format

Fast DRAT
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Proof Format



Efficient Proof Checker



Proving Correctness

Conclusion

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Compact

Checked Efficiently

Verified Checker

Expressive

Our goal is to increase confidence in **all** satisfiability solvers by efficiently checking proofs with a mechanically-verified proof checker.

Recent Work

Bridging the Gap Between Easy Generation and Efficient Verification of Unsatisfiability Proofs

Marijn J.H. Heule, Warren A. Hunt, Jr., and Nathan Wetzler

Accepted: Software Testing, Verification, and Reliability (STVR 201X)

Verifying Refutations with Extended Resolution

Marijn J.H. Heule, Warren A. Hunt, Jr., and Nathan Wetzler

Published: Conference on Automated Deduction (CADE 2013)

Mechanical Verification of SAT Refutations with Extended Resolution

Nathan Wetzler, Marijn J.H. Heule, and Warren A. Hunt, Jr.

Published: Interactive Theorem Proving (ITP 2013)

Trimming while Checking Clausal Proofs

Marijn J.H. Heule, Warren A. Hunt, Jr., and Nathan Wetzler

Published: Formal Methods in Computer-Aided Design (FMCAD 2013)

Thank you for your attention! Questions?

Redundancy

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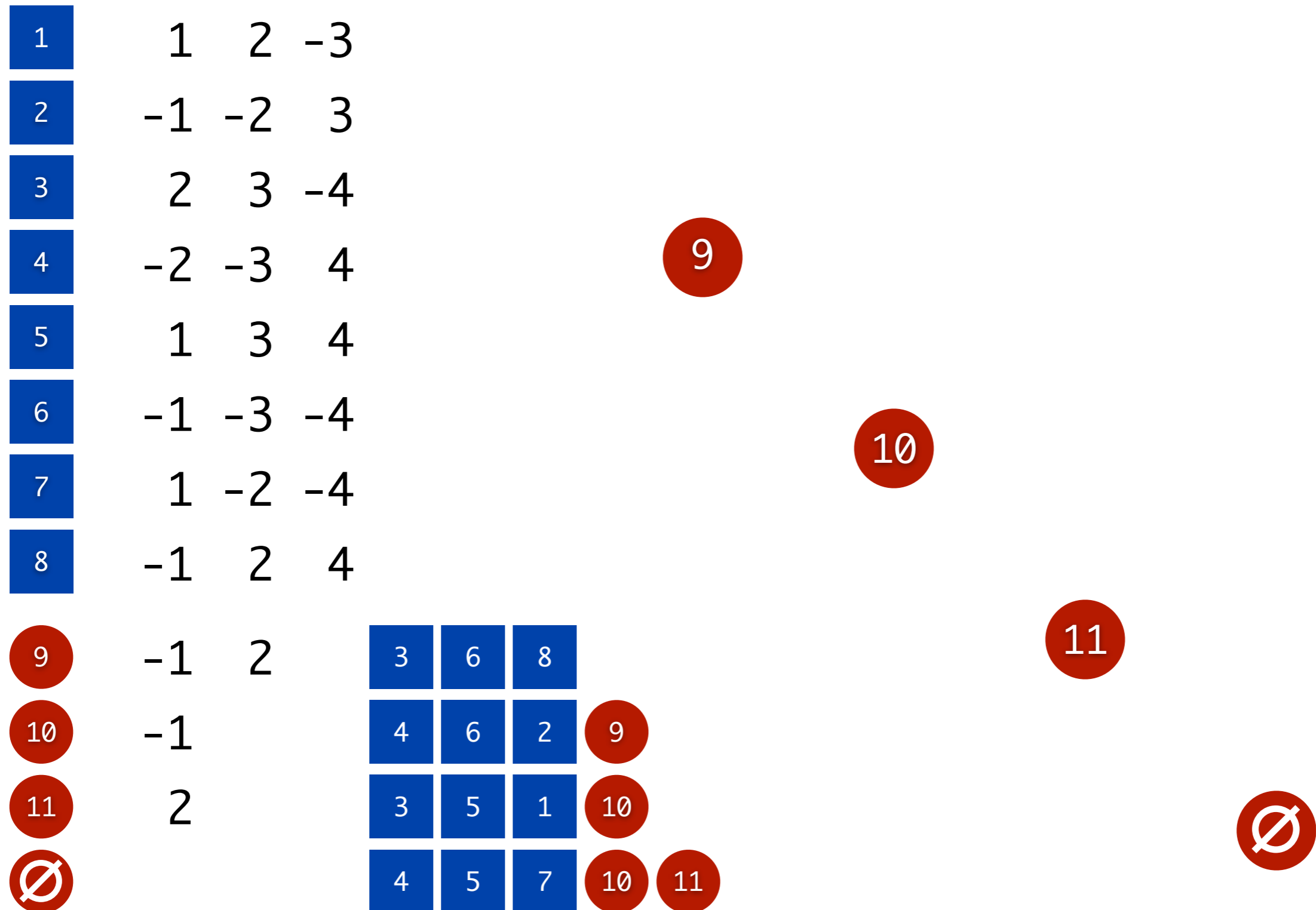
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$$(F \wedge C) \stackrel{\text{SAT}}{\equiv} F$$

Resolution Proof

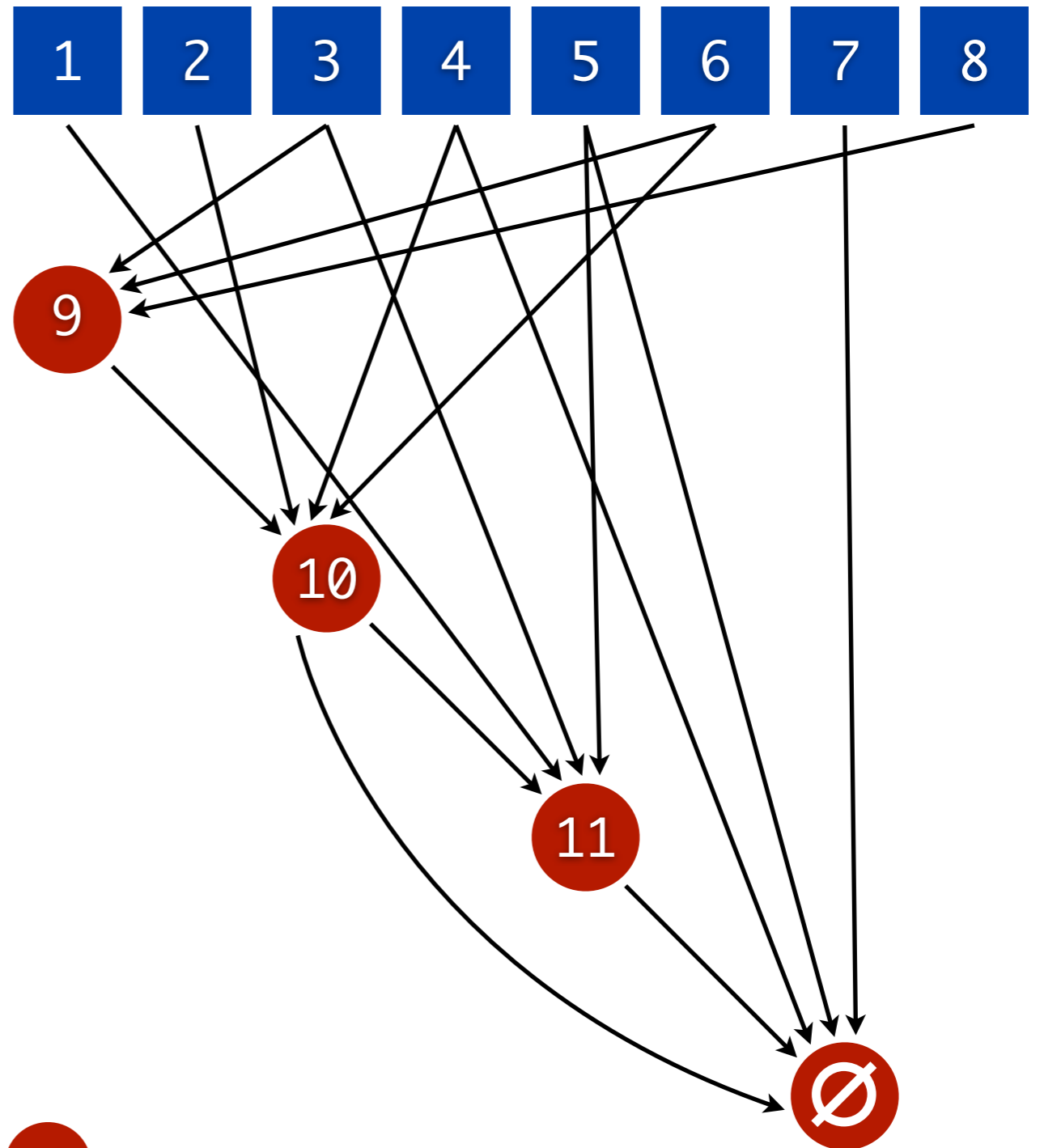


Resolution Proof

1	1	2	-3
2	-1	-2	3
3	2	3	-4
4	-2	-3	4
5	1	3	4
6	-1	-3	-4
7	1	-2	-4
8	-1	2	4

9	-1	2
10	-1	
11	2	
\emptyset		

3	6	8		
4	6	2	9	
3	5	1	10	
4	5	7	10	11

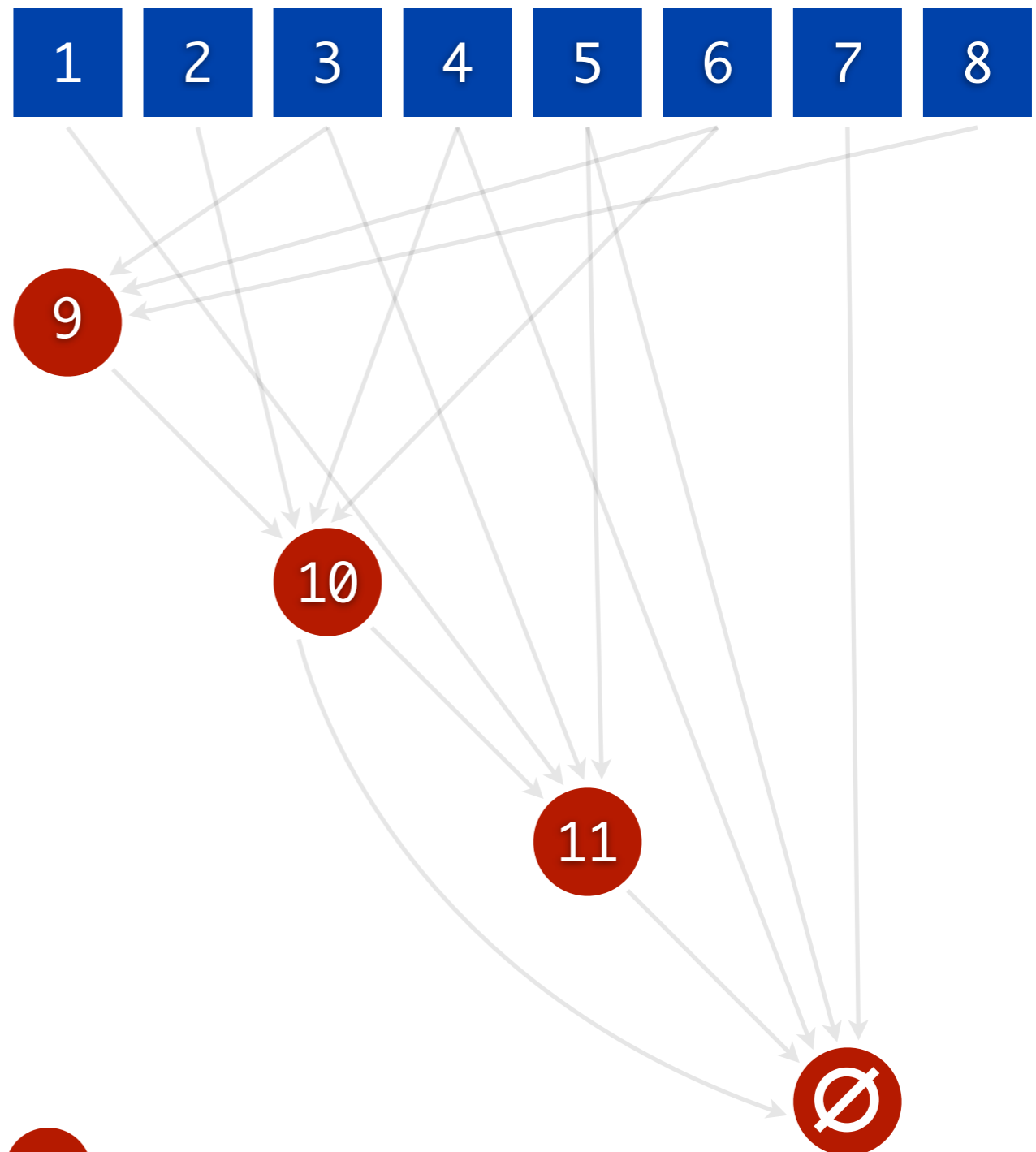


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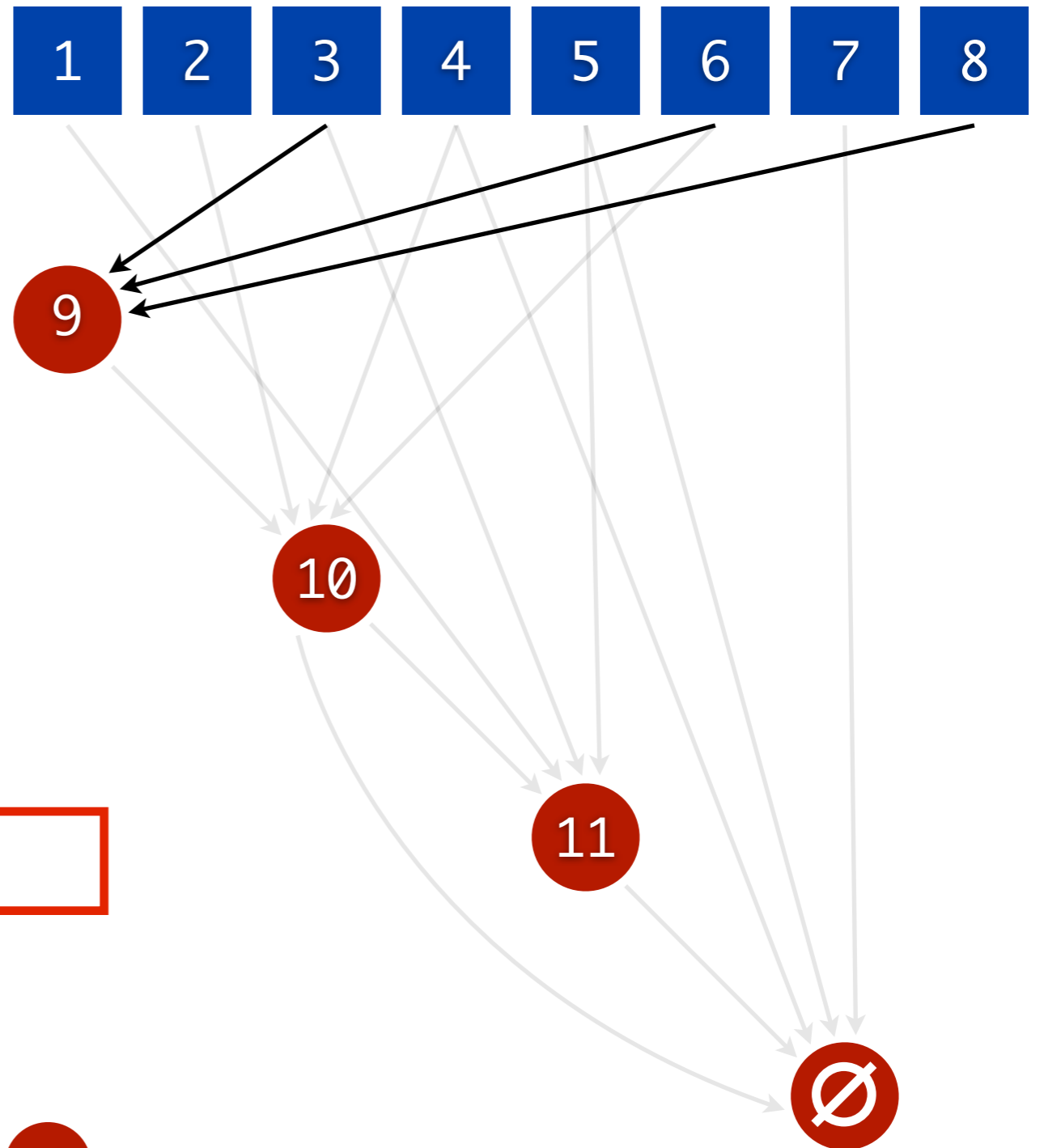
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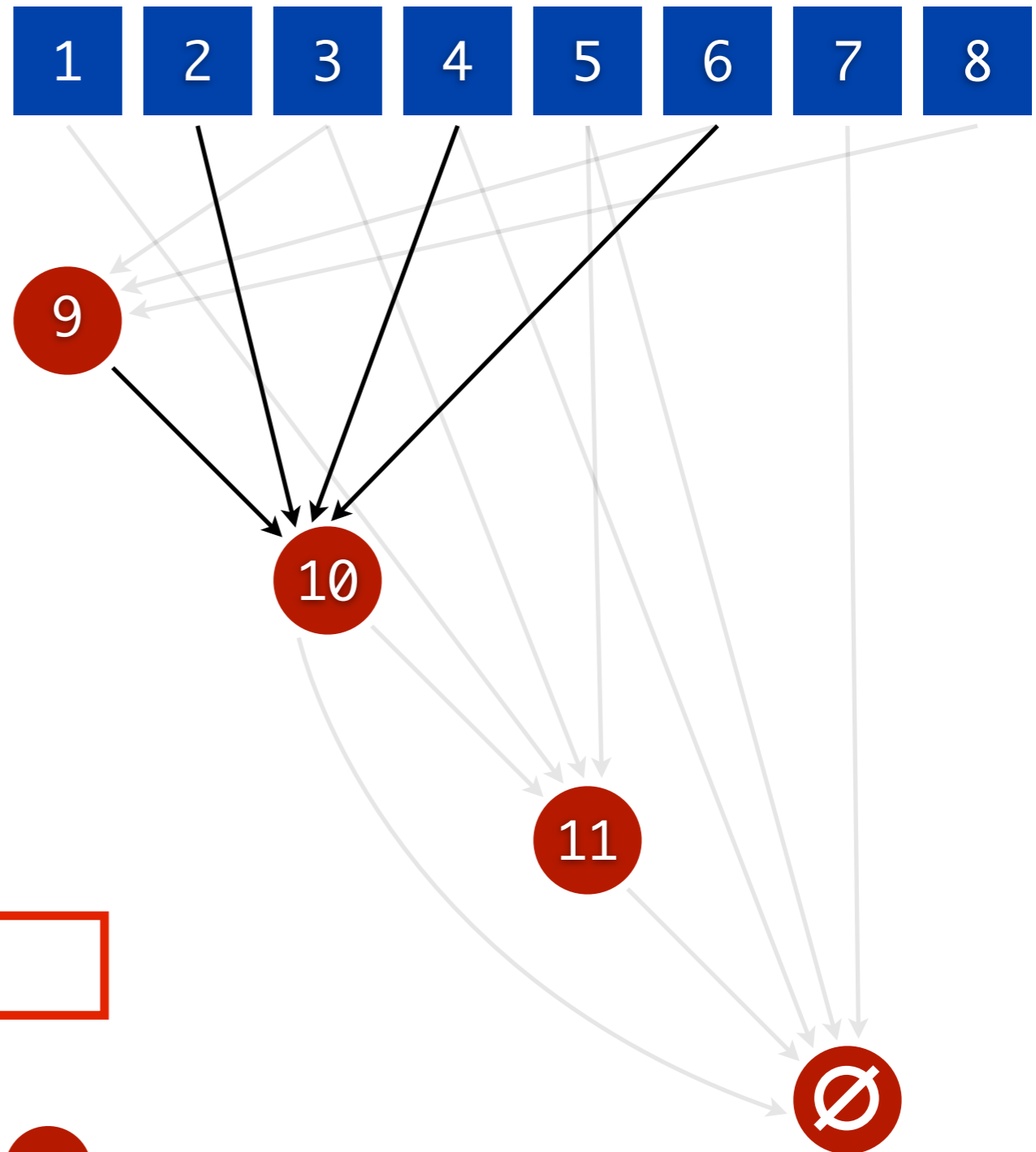
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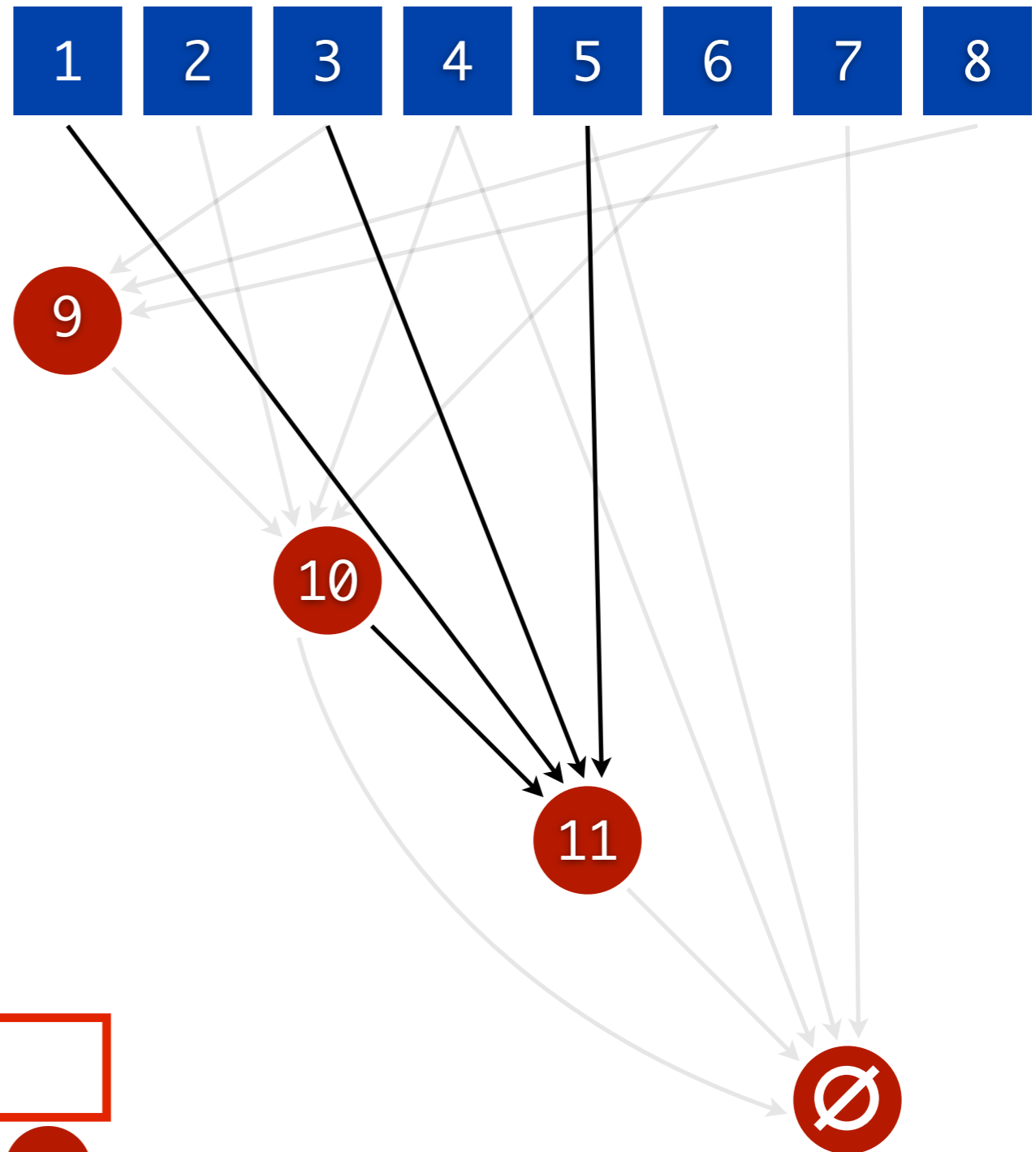
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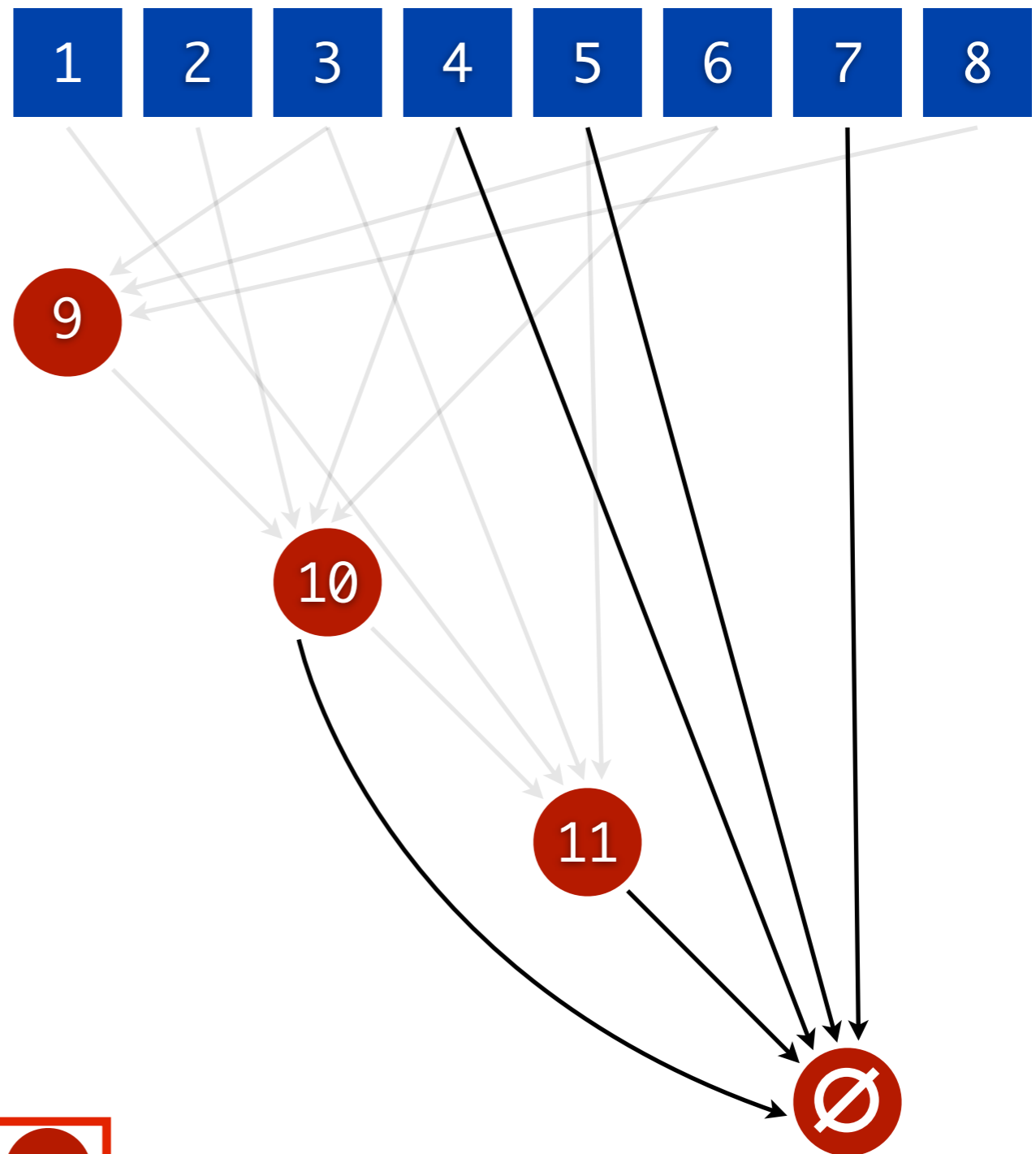
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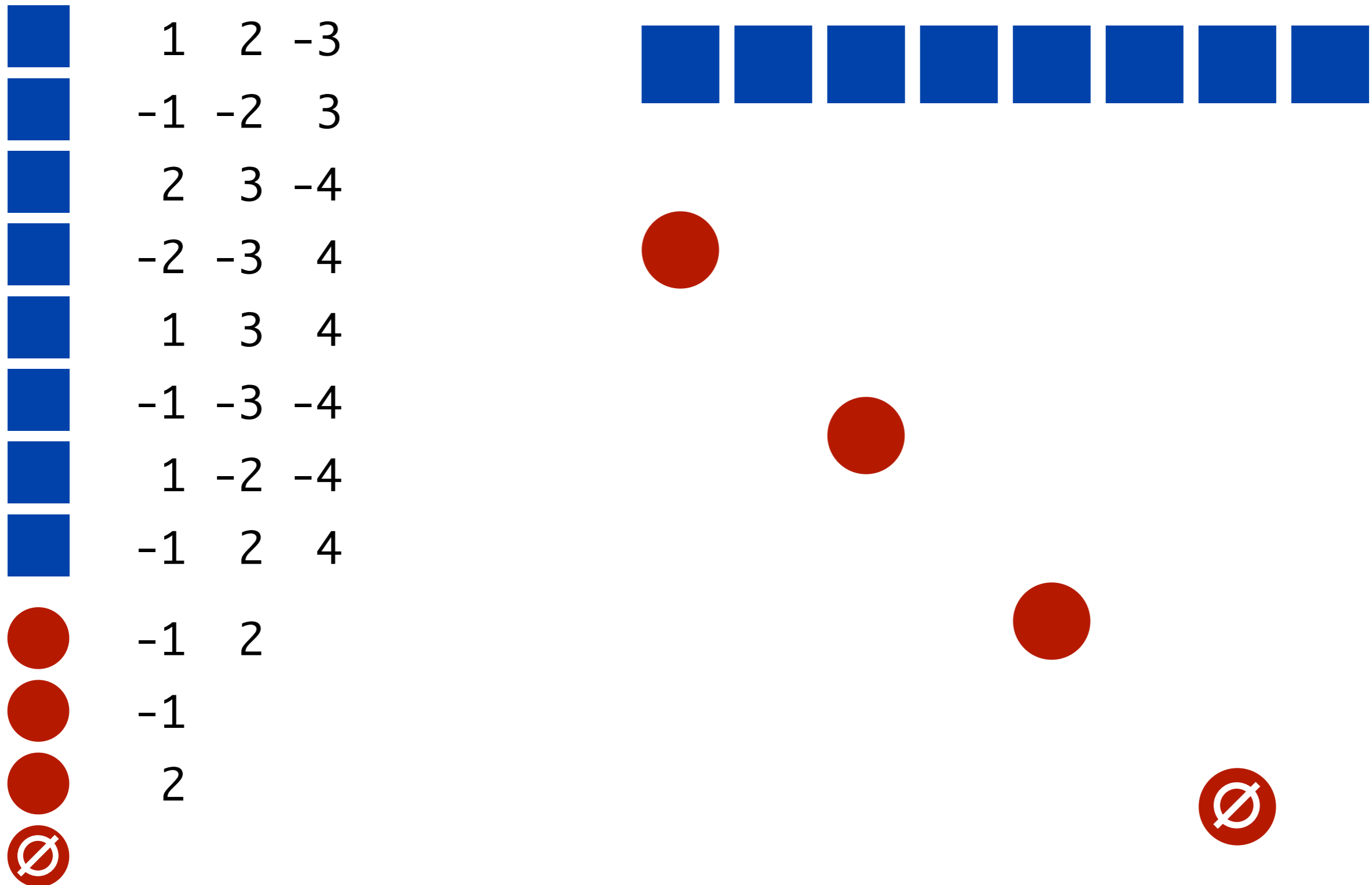
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∅			4	5	7	10	11

Resolution Proof

1	1	2	-3				
2	-1	-2	3				
3	2	3	-4				
4	-2	-3	4				
5	1	3	4				
6	-1	-3	-4				
7	1	-2	-4				
8	-1	2	4				
9	-1	2		3	6	8	
10	-1			4	6	2	9
11	2			3	5	1	10
\emptyset				4	5	7	10 11



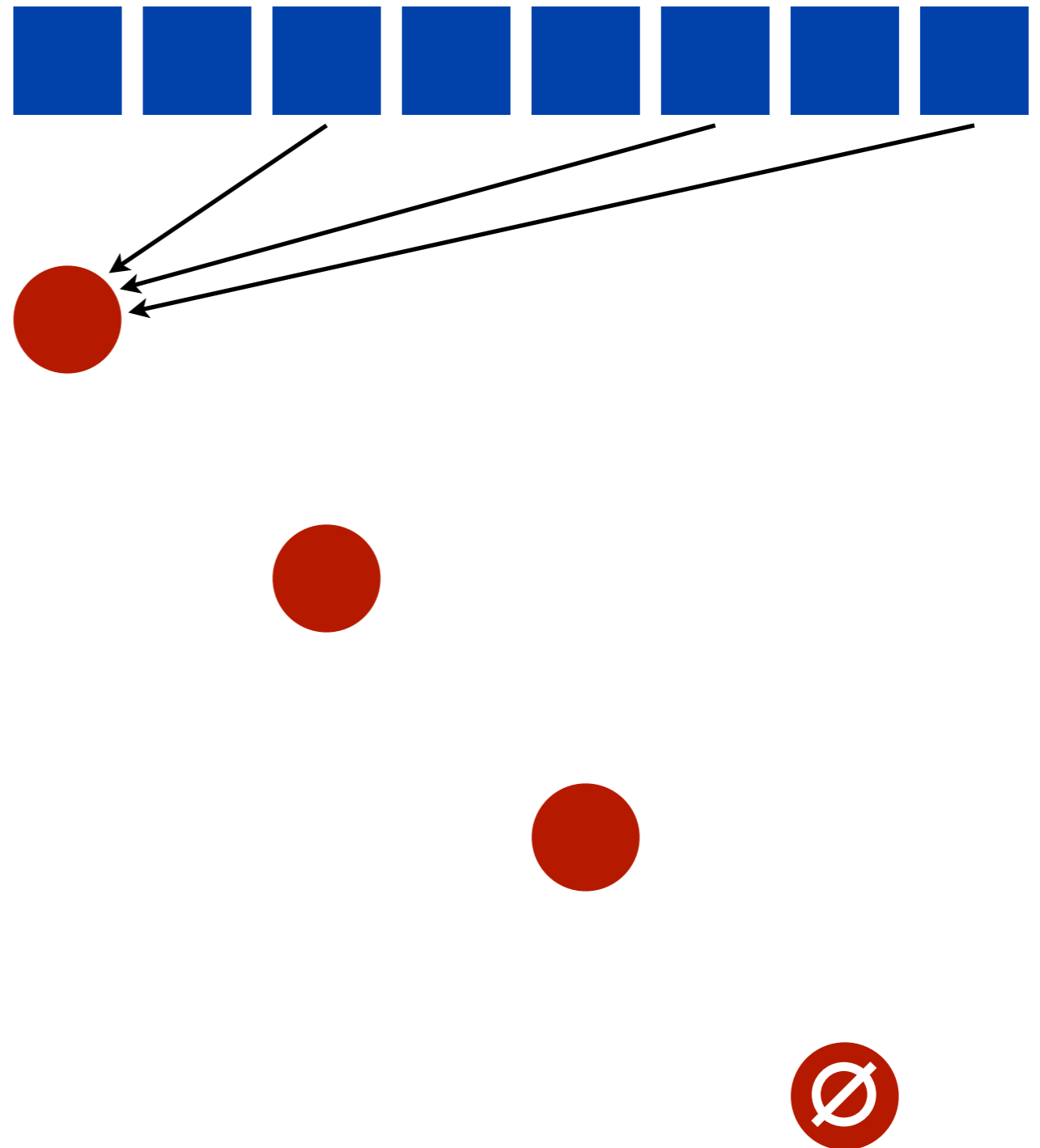
RUP Proof



RUP Proof

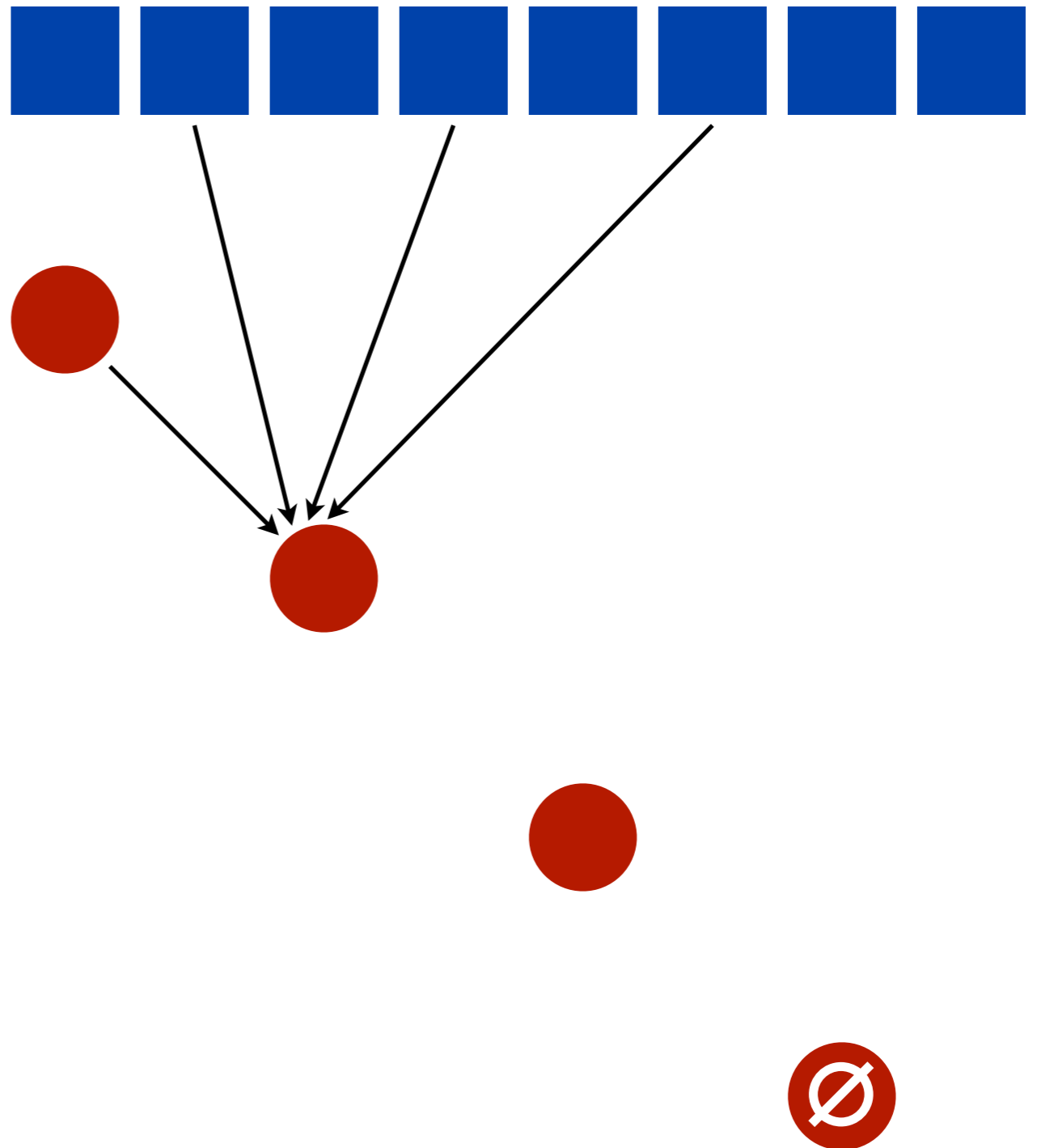
■	1	2	-3
■	-1	-2	3
■	2	3	-4
■	-2	-3	4
■	1	3	4
■	-1	-3	-4
■	1	-2	-4
■	-1	2	4

●	-1	2
●	-1	
●	2	
⊘		

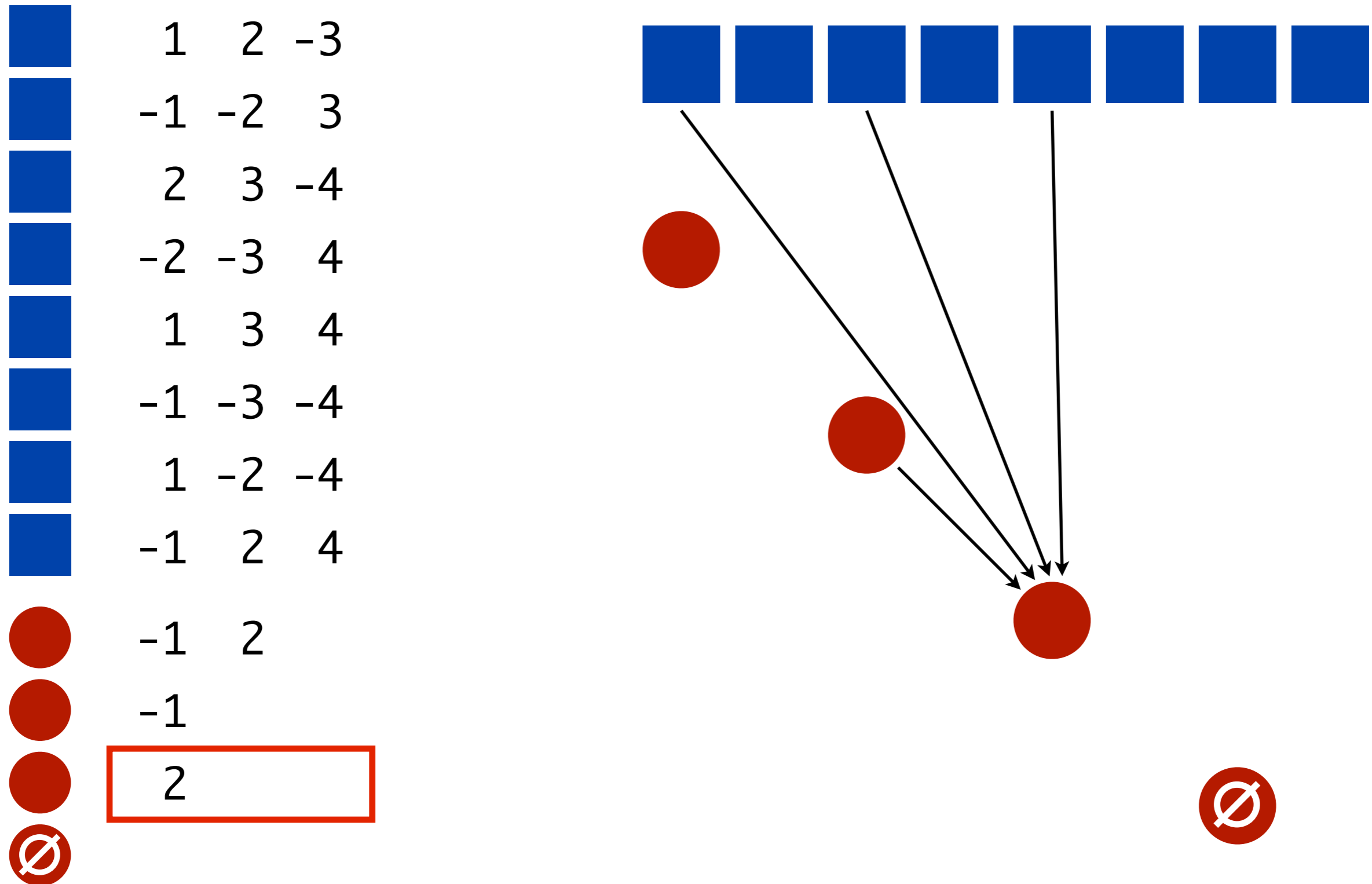


RUP Proof

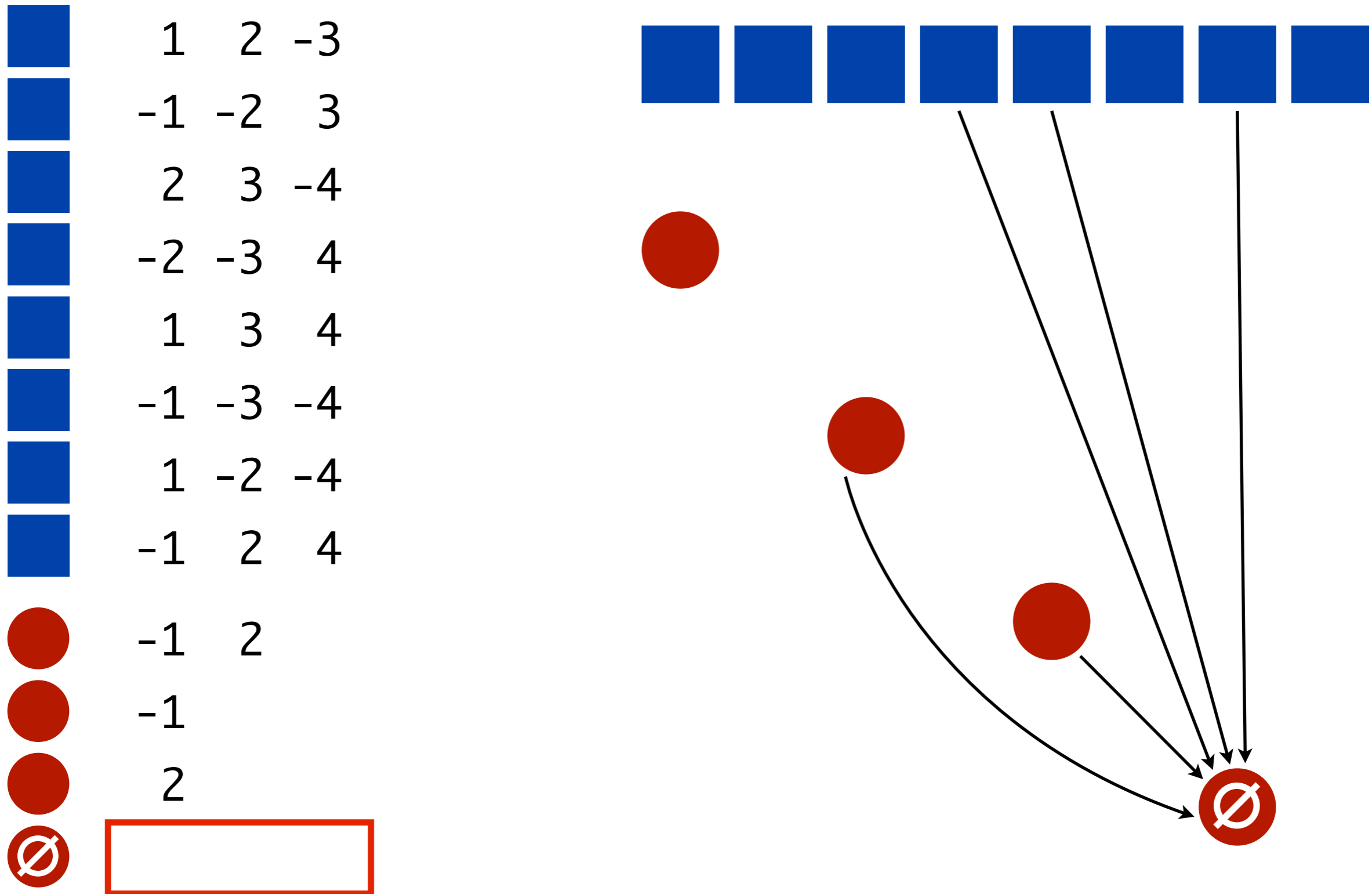
■	1	2	-3
■	-1	-2	3
■	2	3	-4
■	-2	-3	4
■	1	3	4
■	-1	-3	-4
■	1	-2	-4
■	-1	2	4
●	-1	2	
●	-1		
●	2		
⊘			



RUP Proof





















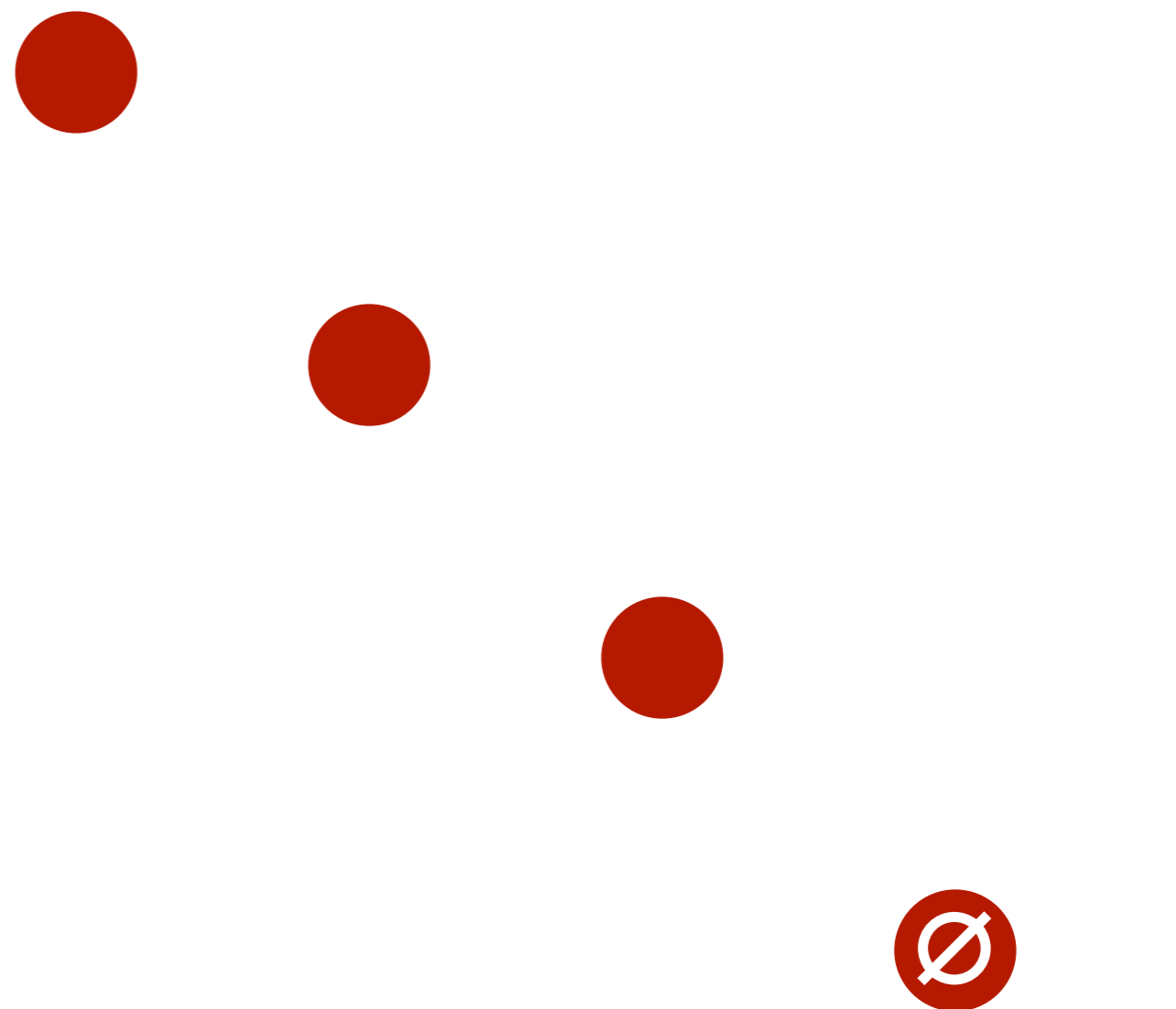
RUP Proof



DRUP Proof

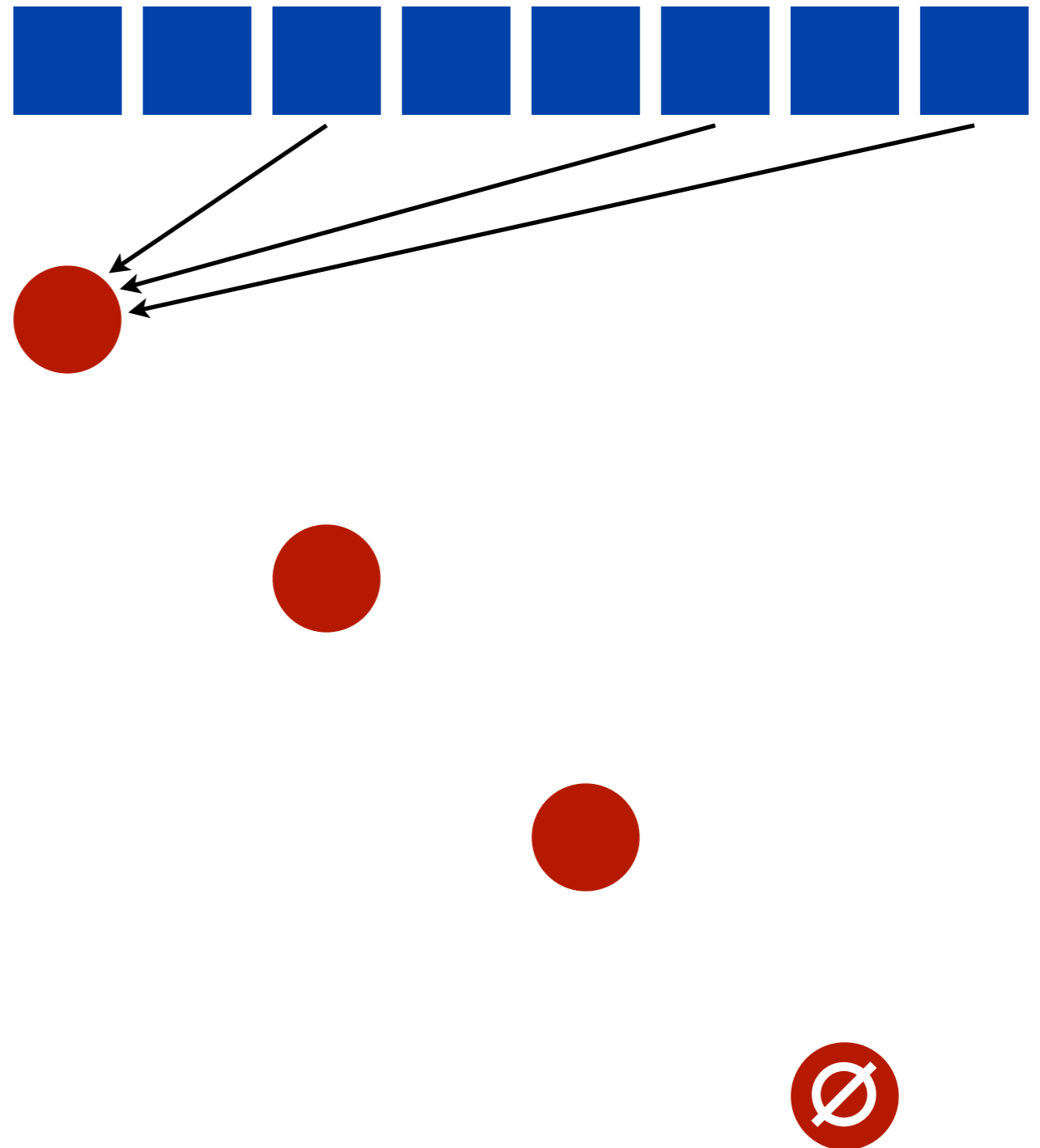


	1	2	-3		-1	2		
	-1	-2	3		d	-1	2	4
	2	3	-4		-1			
	-2	-3	4		d	-1	-2	3
	1	3	4		d	-1	-3	-4
	-1	-3	-4		d	-1	2	
	1	-2	-4		2			
	-1	2	4		d	1	2	-3
					d	2	3	-4
								





















DRUP Proof

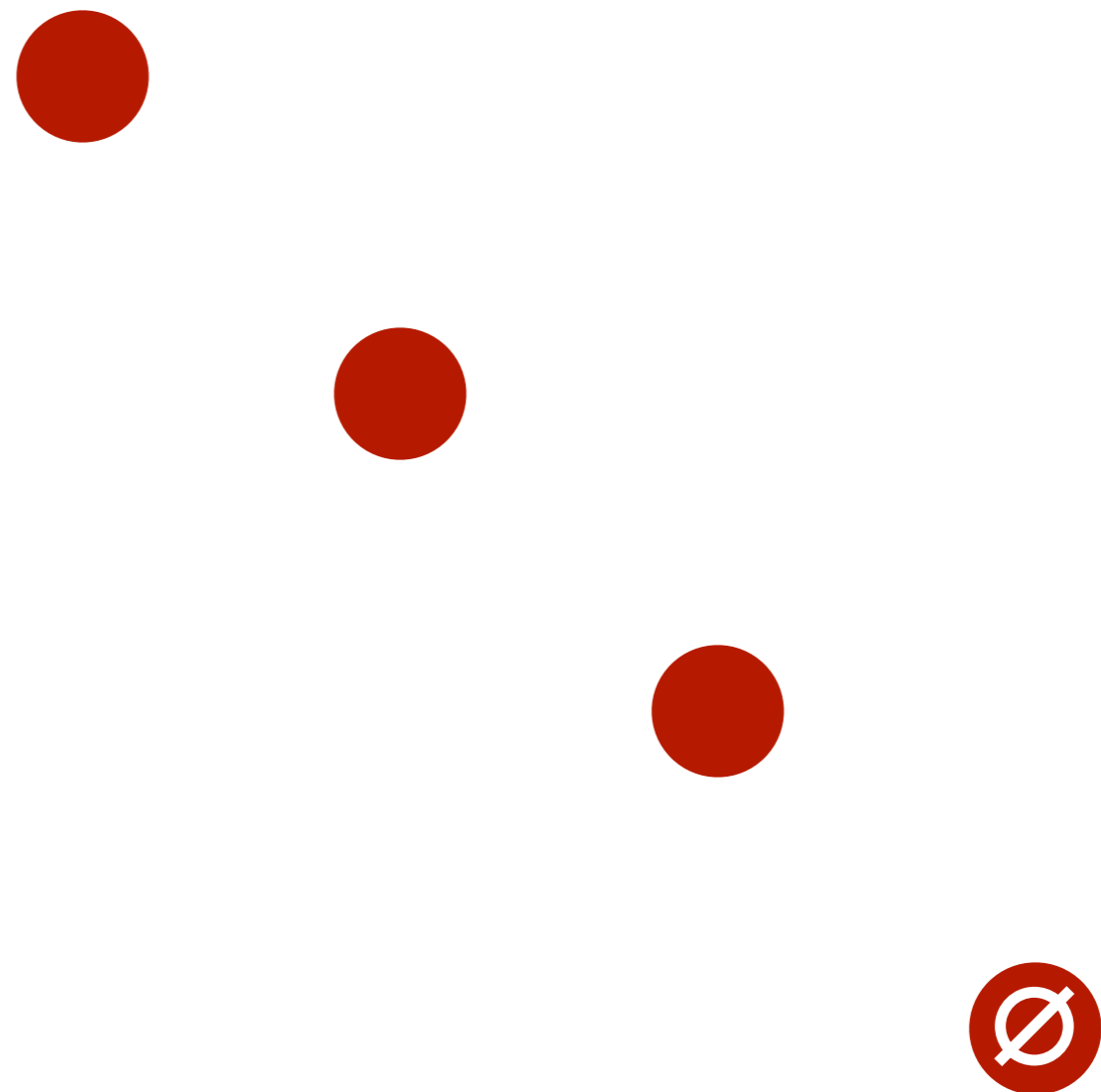
■	1	2	-3	●	-1	2		
■	-1	-2	3	■	d	-1	2	4
■	2	3	-4	●	-1			
■	-2	-3	4	■	d	-1	-2	3
■	1	3	4	■	d	-1	-3	-4
■	-1	-3	-4	●	d	-1	2	
■	1	-2	-4	●	2			
■	-1	2	4	■	d	1	2	-3
				■	d	2	3	-4
				⊘				



DRUP Proof

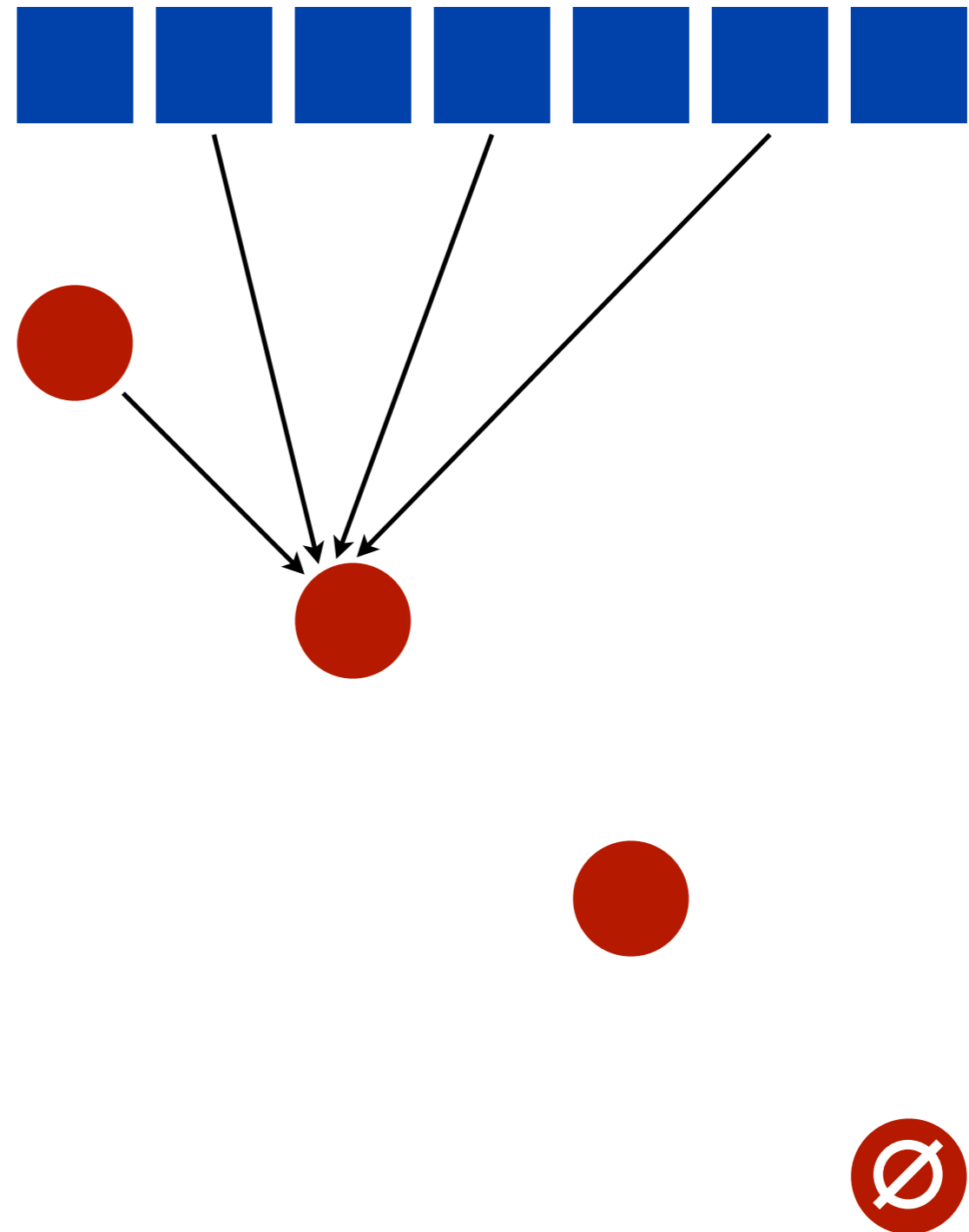


	1	2	-3		-1	2
	-1	-2	3		d	-1 2 4
	2	3	-4		-1	
	-2	-3	4		d	-1 -2 3
	1	3	4		d	-1 -3 -4
	-1	-3	-4		d	-1 2
	1	-2	-4		2	
	-1	2	4		d	1 2 -3
					d	2 3 -4
					\emptyset	





















DRUP Proof

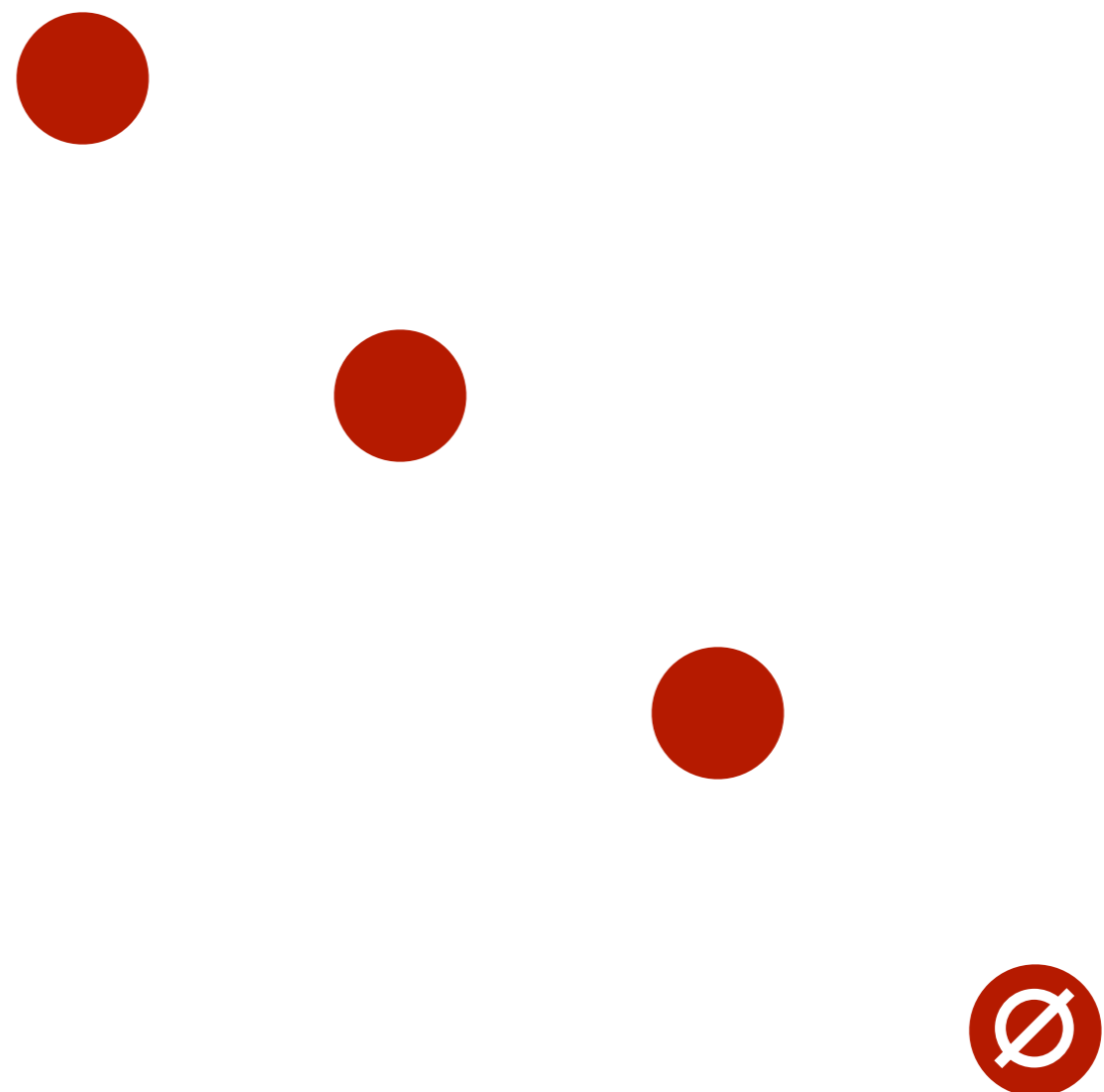
■	1	2	-3	●	-1	2		
■	-1	-2	3	■	d	-1	2	4
■	2	3	-4	●	-1			
■	-2	-3	4	■	d	-1	-2	3
■	1	3	4	■	d	-1	-3	-4
■	-1	-3	-4	●	d	-1	2	
■	1	-2	-4	●	2			
■	-1	2	4	■	d	1	2	-3
				■	d	2	3	-4
				⊘				



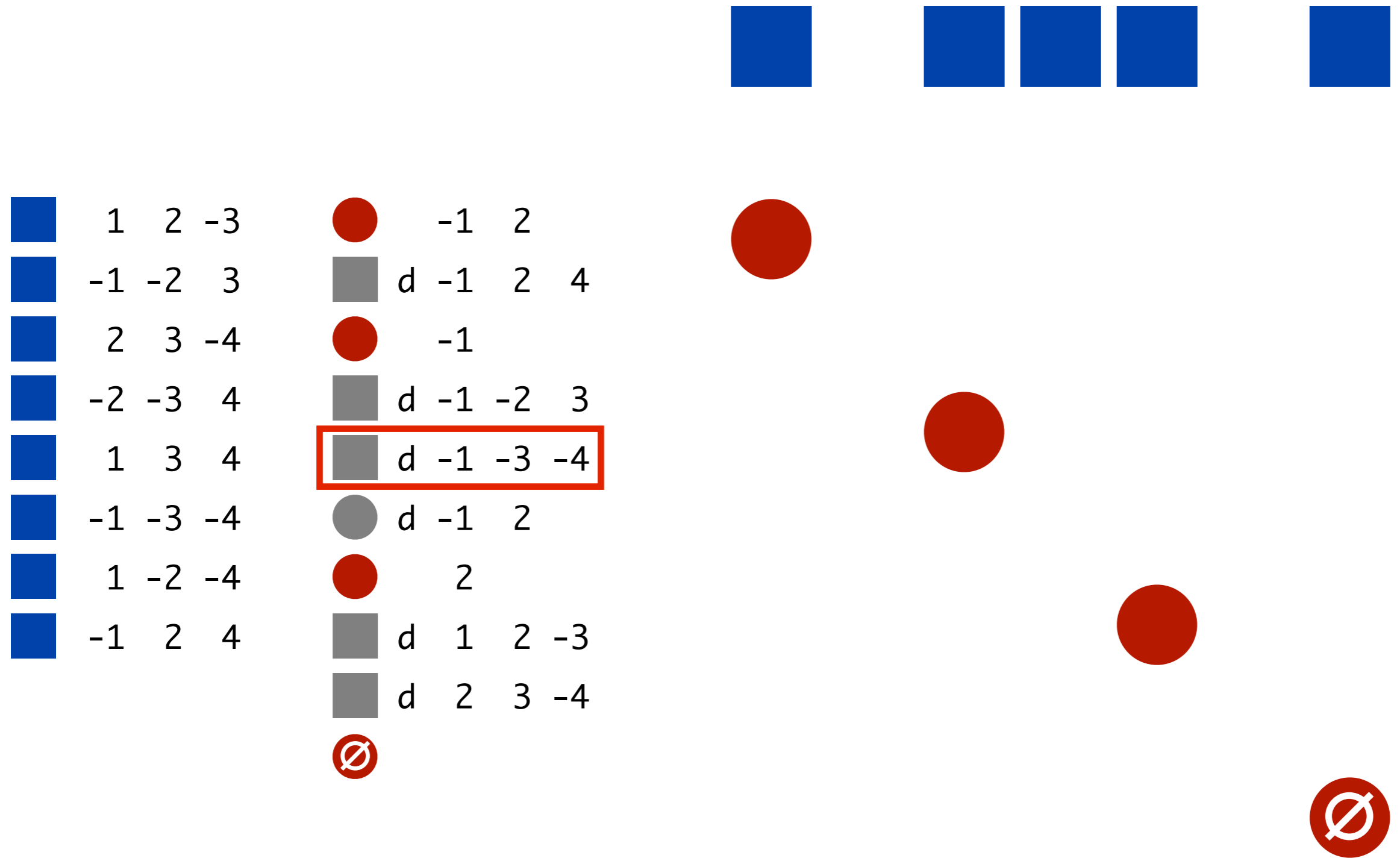
DRUP Proof



	1	2	-3		-1	2
	-1	-2	3		d	-1 2 4
	2	3	-4		-1	
	-2	-3	4		d	-1 -2 3
	1	3	4		d	-1 -3 -4
	-1	-3	-4		d	-1 2
	1	-2	-4		2	
	-1	2	4		d	1 2 -3
					d	2 3 -4
					\emptyset	





















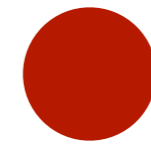
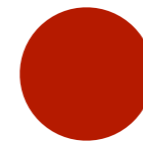
DRUP Proof





















DRUP Proof

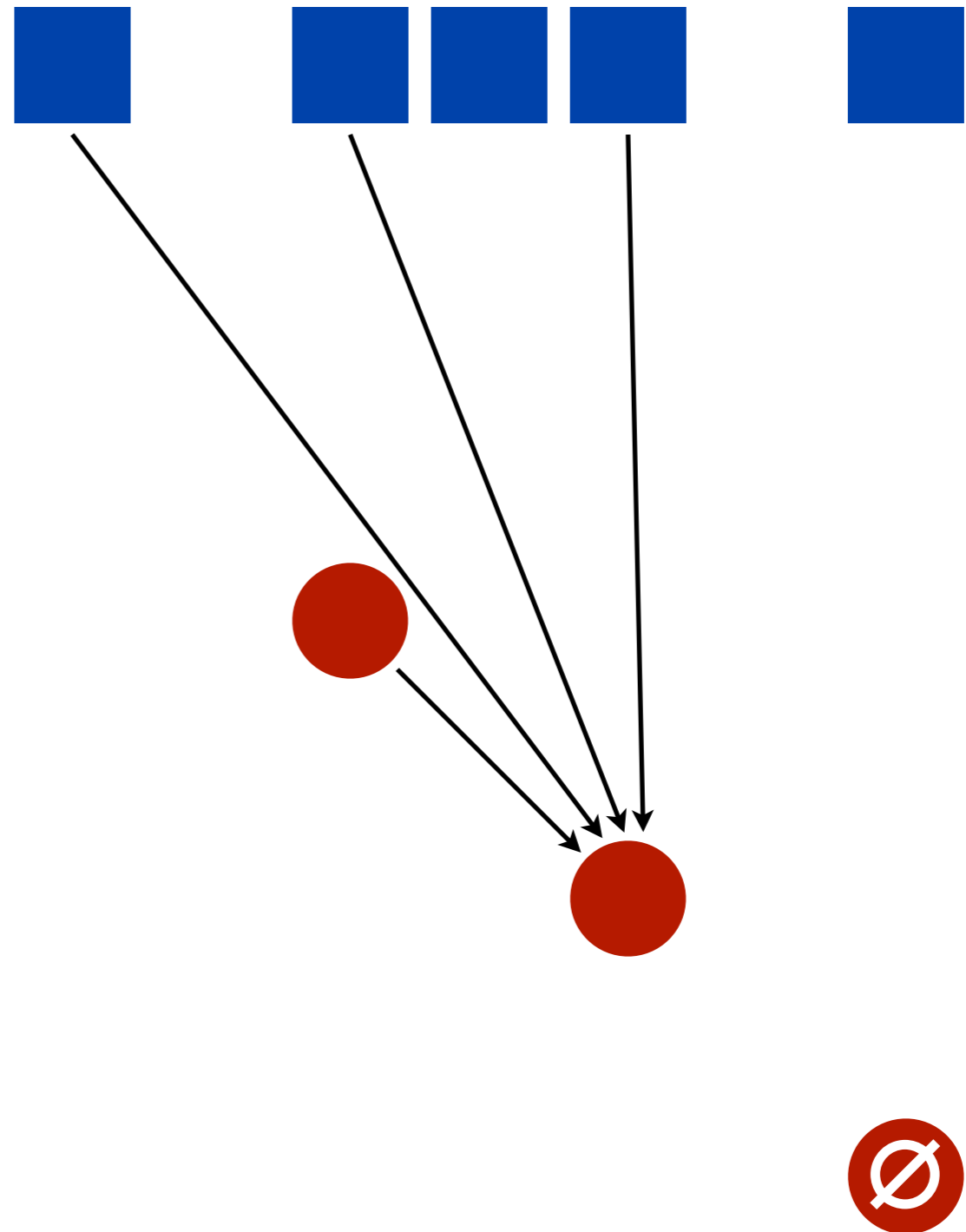


	1	2	-3		-1	2		
	-1	-2	3		d	-1	2	4
	2	3	-4		-1			
	-2	-3	4		d	-1	-2	3
	1	3	4		d	-1	-3	-4
	-1	-3	-4		d	-1	2	
	1	-2	-4		2			
	-1	2	4		d	1	2	-3
					d	2	3	-4
								





















DRUP Proof

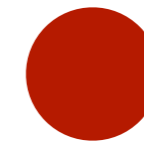
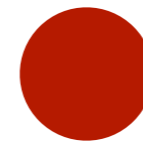
	1	2	-3		-1	2
	-1	-2	3		d	-1 2 4
	2	3	-4		-1	
	-2	-3	4		d	-1 -2 3
	1	3	4		d	-1 -3 -4
	-1	-3	-4		d	-1 2
	1	-2	-4		2	
	-1	2	4		d	1 2 -3
					d	2 3 -4
						



DRUP Proof





















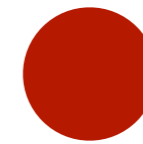
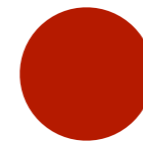
	1	2	-3		-1	2		
	-1	-2	3		d	-1	2	4
	2	3	-4		-1			
	-2	-3	4		d	-1	-2	3
	1	3	4		d	-1	-3	-4
	-1	-3	-4		d	-1	2	
	1	-2	-4		2			
	-1	2	4		d	1	2	-3
					d	2	3	-4
								



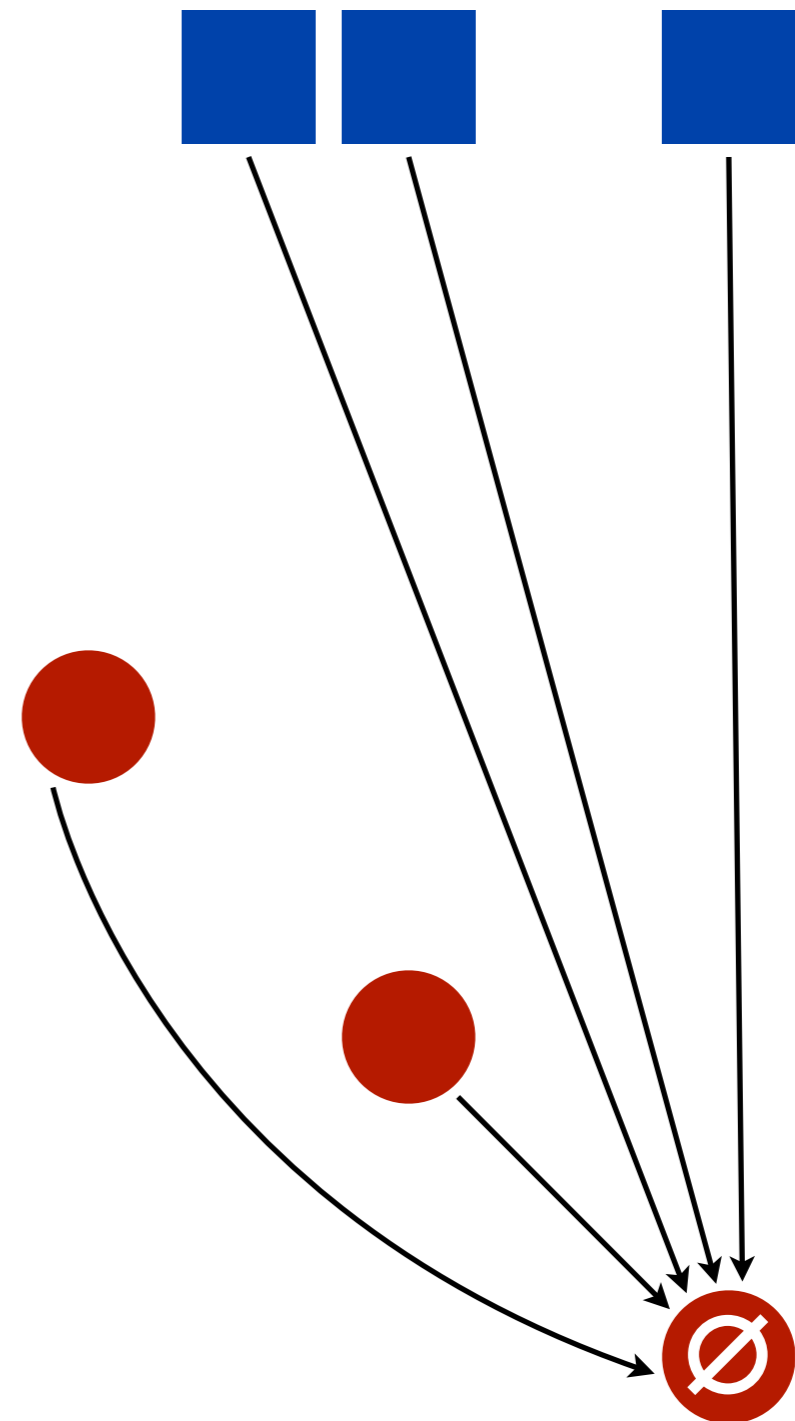
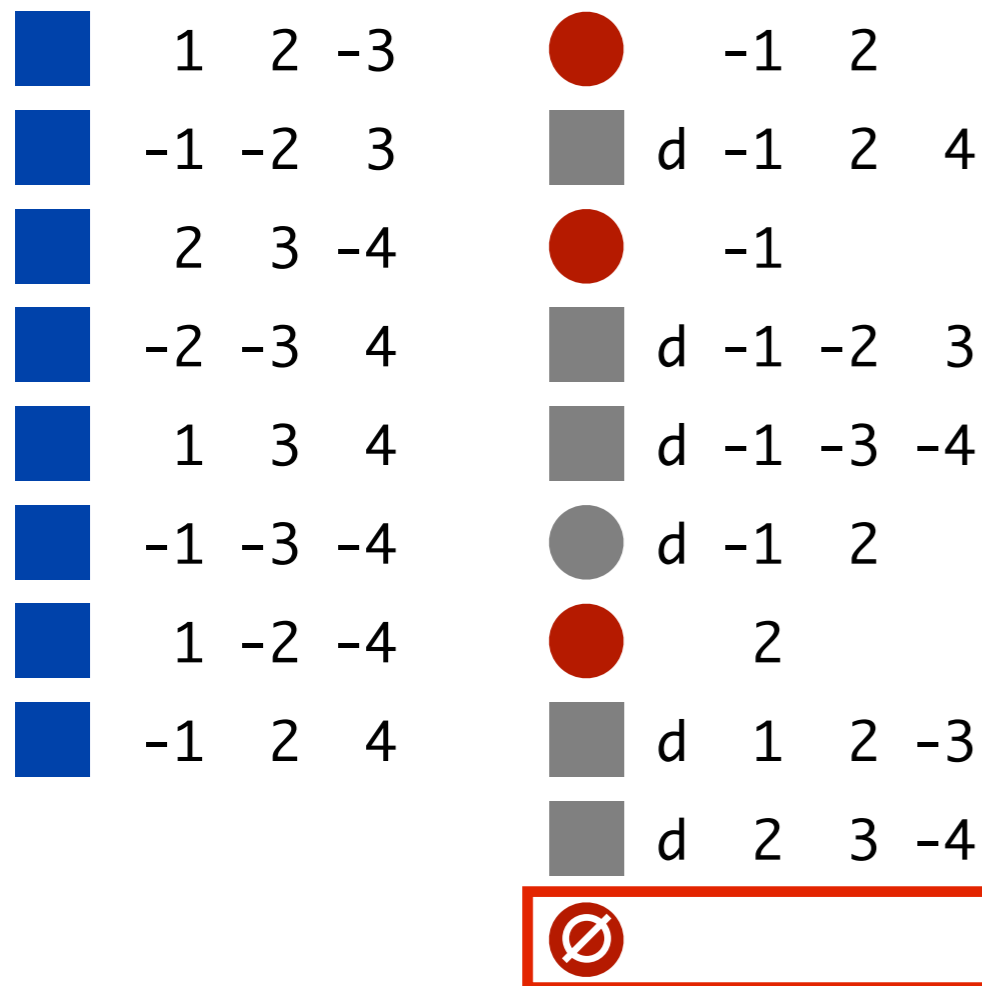
DRUP Proof



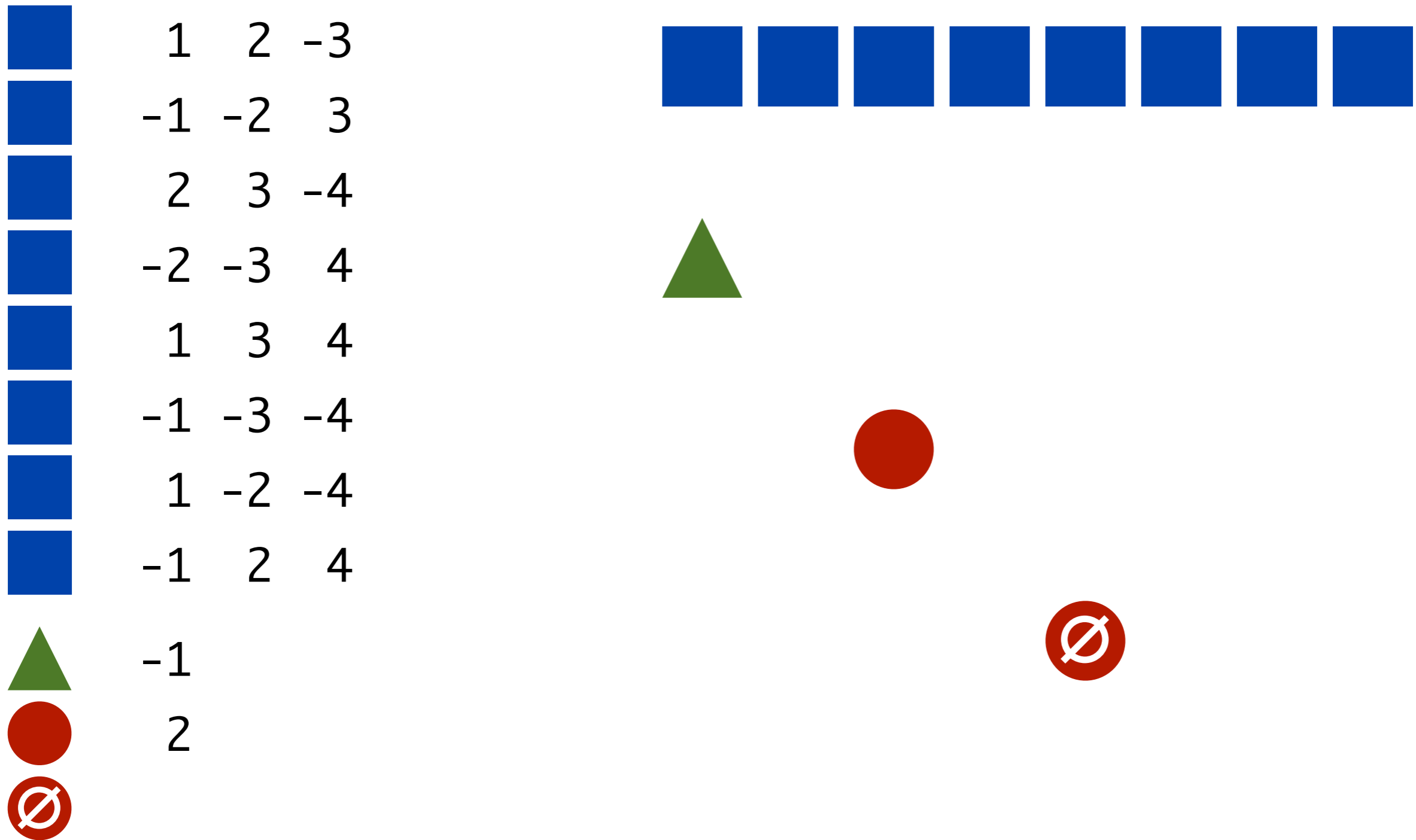
	1	2	-3		-1	2
	-1	-2	3		d	-1 2 4
	2	3	-4		-1	
	-2	-3	4		d	-1 -2 3
	1	3	4		d	-1 -3 -4
	-1	-3	-4		d	-1 2
	1	-2	-4		2	
	-1	2	4		d	1 2 -3
					d	2 3 -4
					\emptyset	



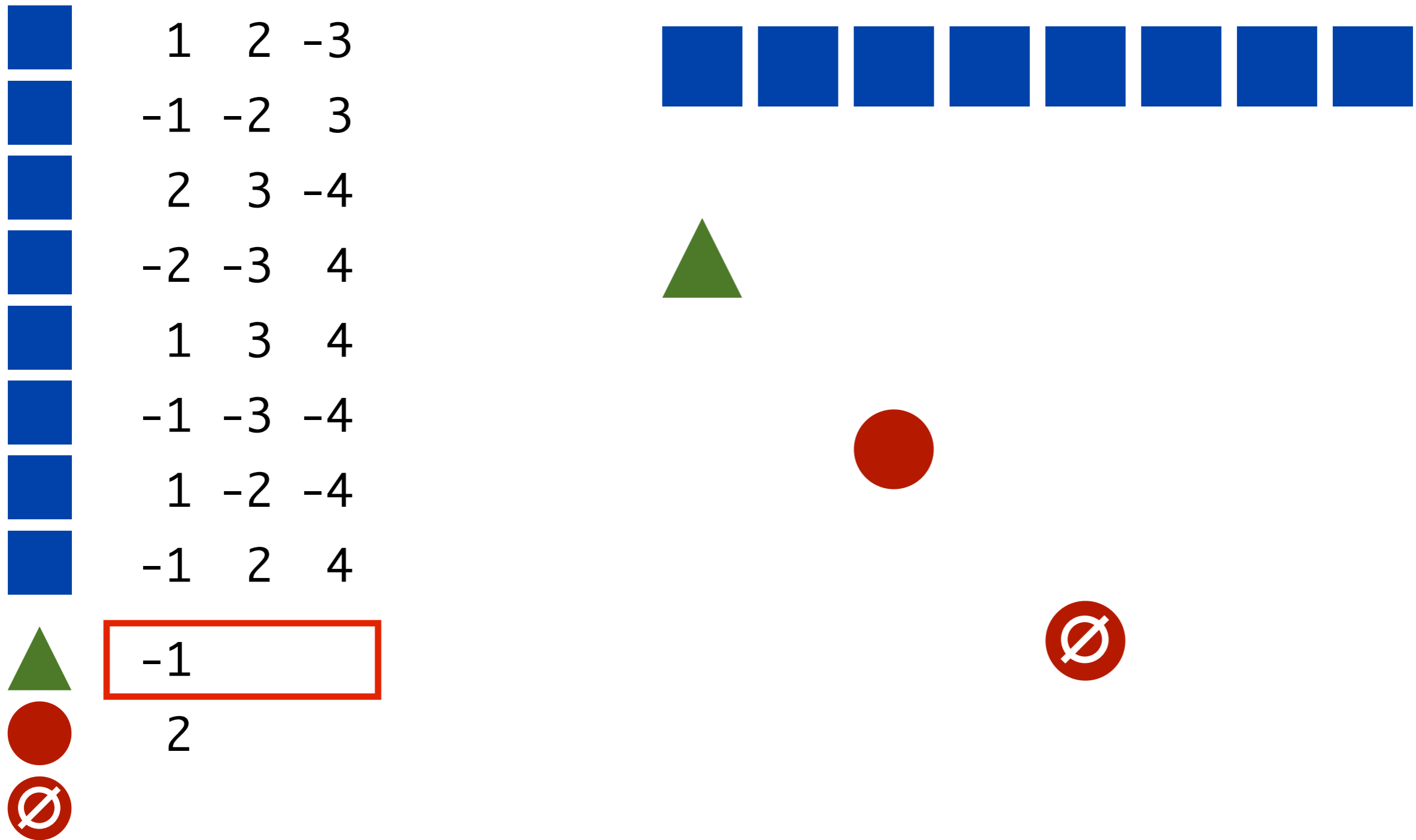
DRUP Proof



RAT Proof



RAT Proof

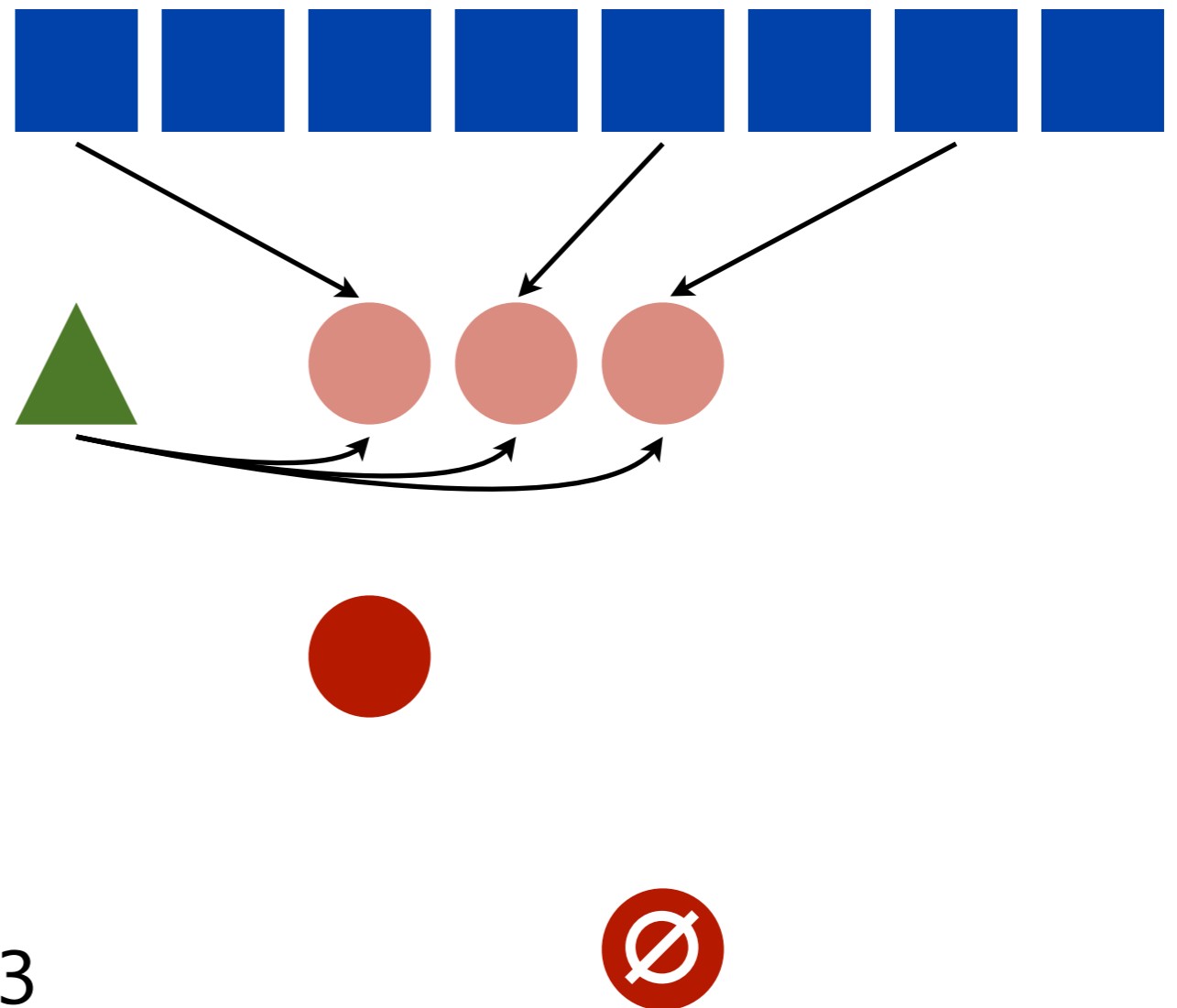


RAT Proof

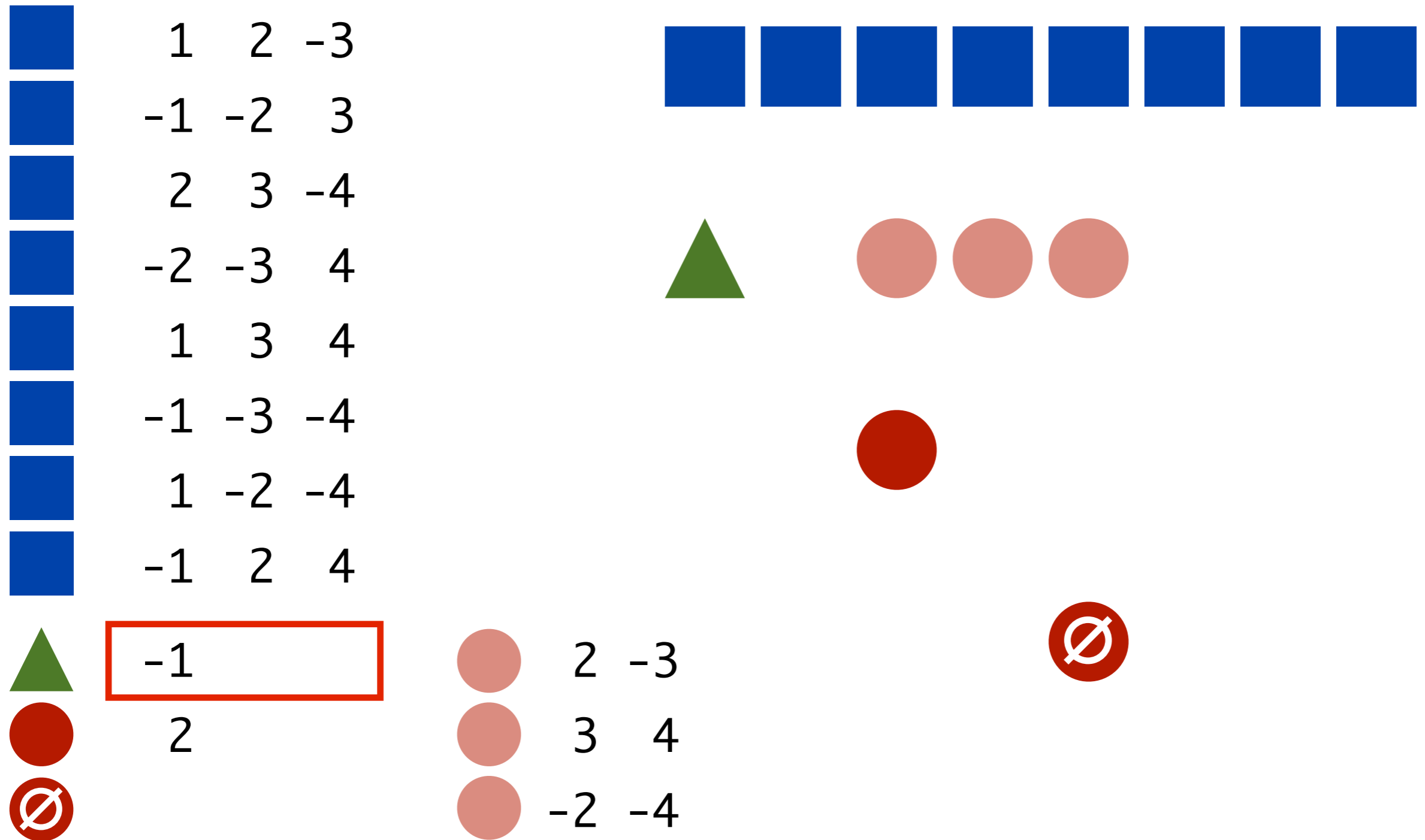
■	1	2	-3
■	-1	-2	3
■	2	3	-4
■	-2	-3	4
■	1	3	4
■	-1	-3	-4
■	1	-2	-4
■	-1	2	4

▲	-1
●	2
⊘	

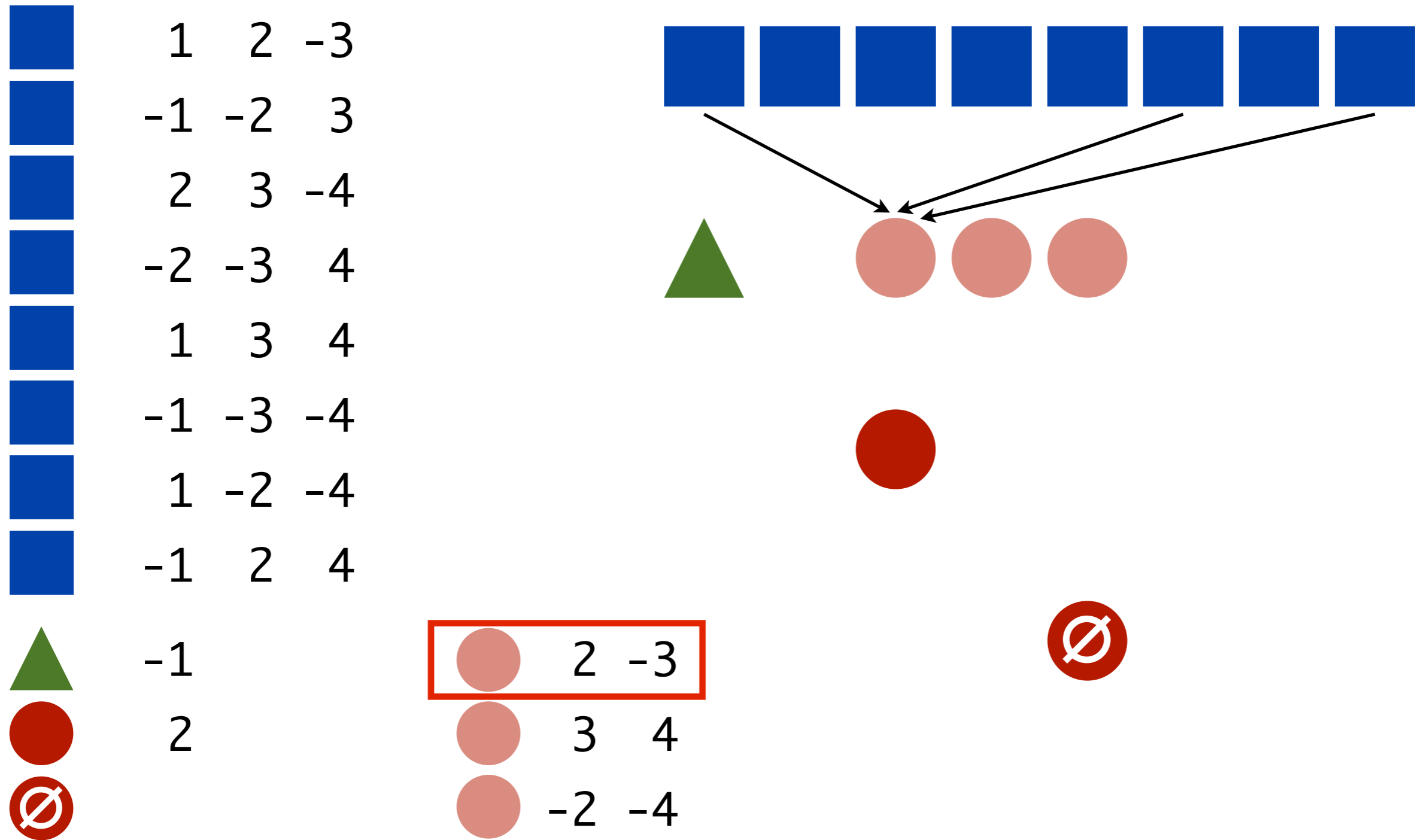
●	2	-3
●	3	4
●	-2	-4



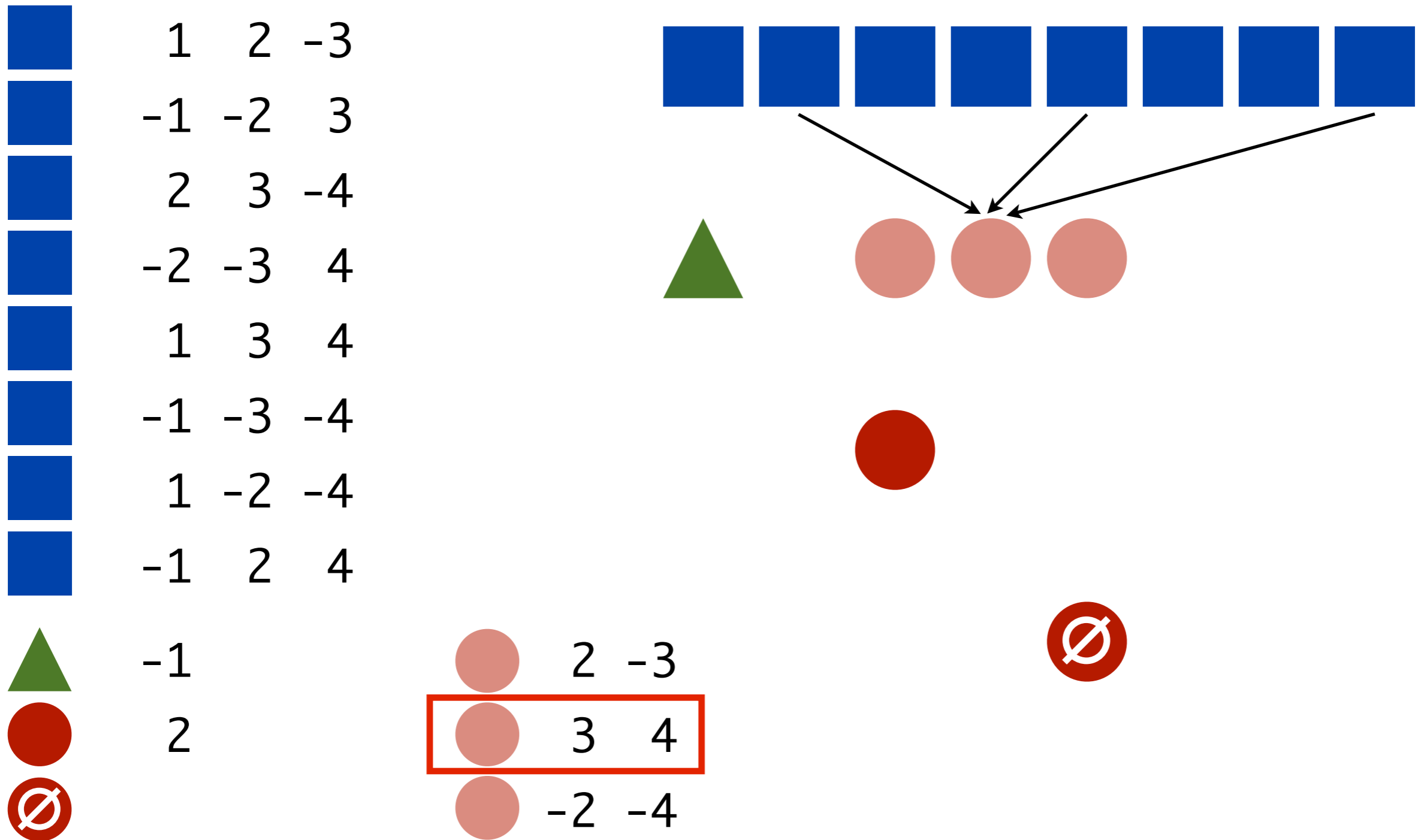
RAT Proof



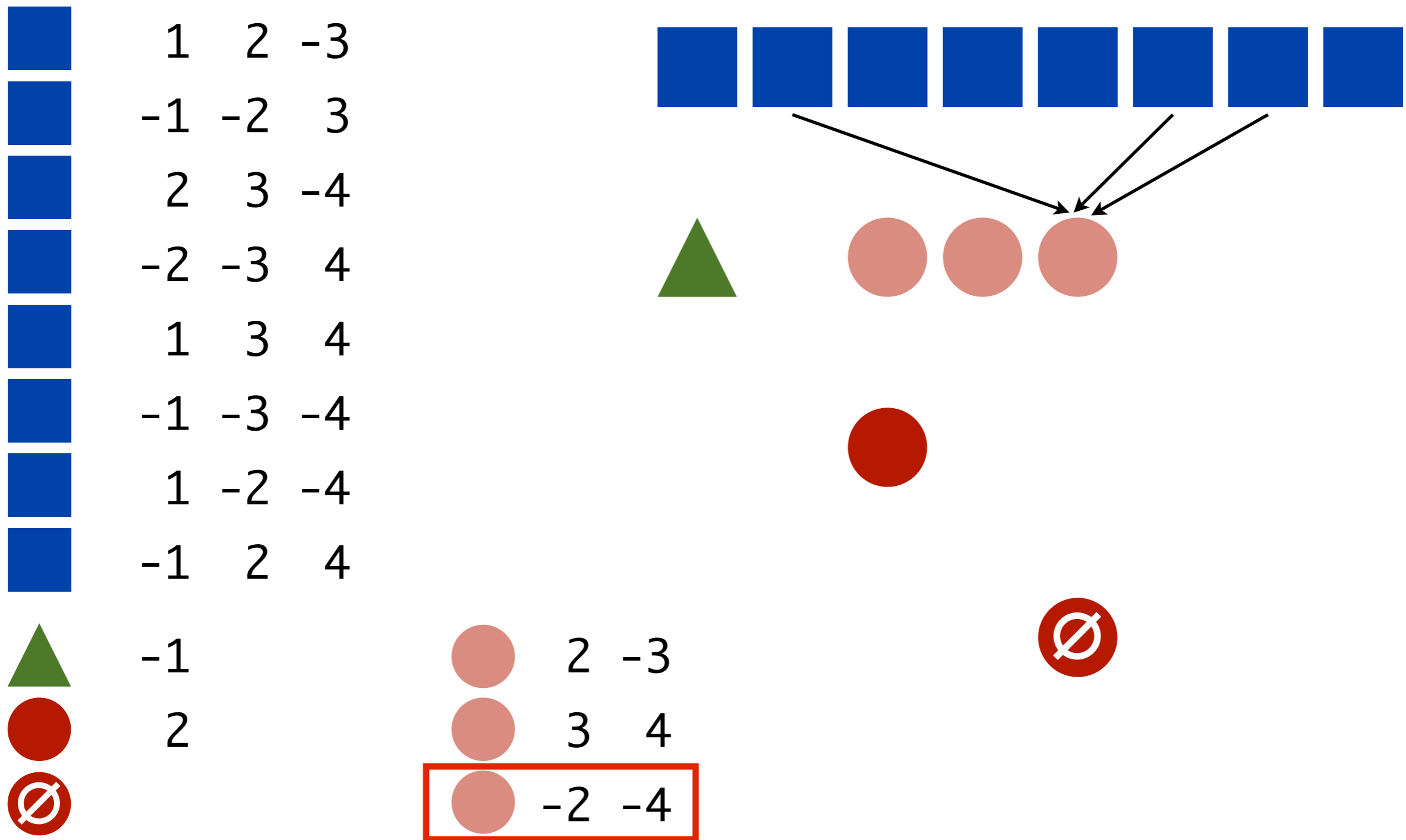
RAT Proof



RAT Proof

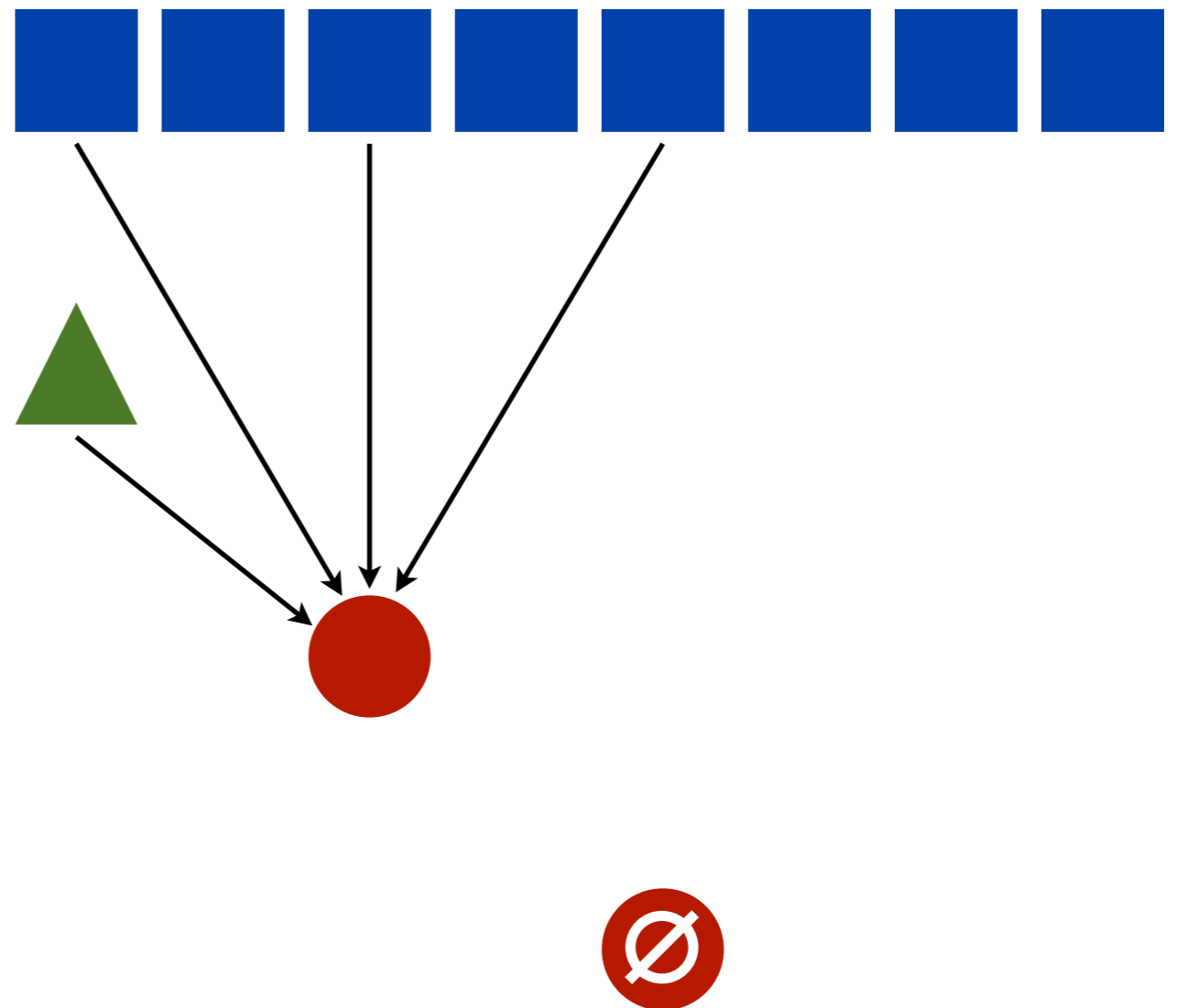


RAT Proof



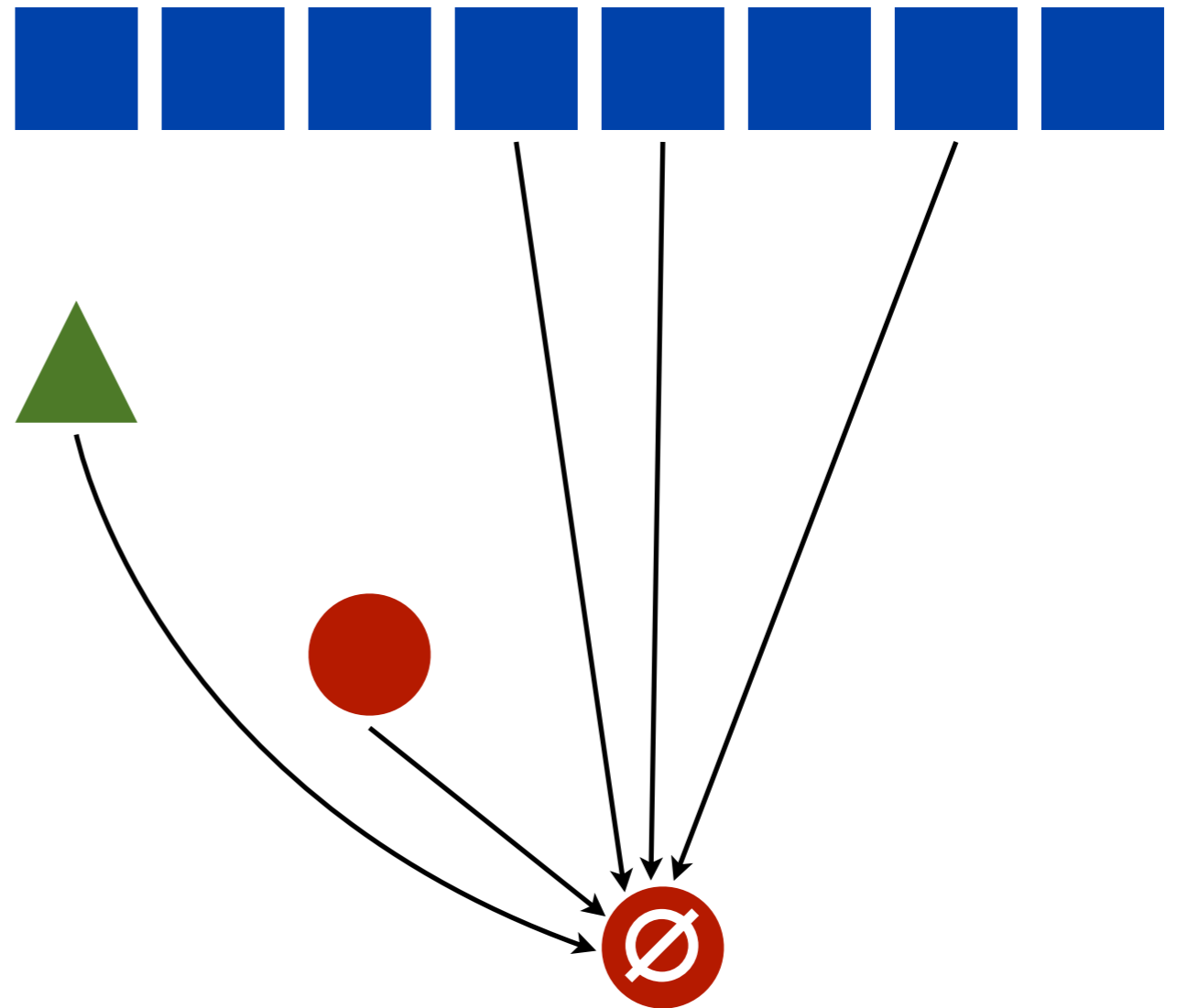
RAT Proof

■	1	2	-3
■	-1	-2	3
■	2	3	-4
■	-2	-3	4
■	1	3	4
■	-1	-3	-4
■	1	-2	-4
■	-1	2	4
▲	-1		
●	2		
∅			



RAT Proof

■	1	2	-3
■	-1	-2	3
■	2	3	-4
■	-2	-3	4
■	1	3	4
■	-1	-3	-4
■	1	-2	-4
■	-1	2	4
▲	-1		
●	2		
⊘			



Incremental Approach

Efficient code can be difficult to verify.

- STOBJs provide array-like memory, but require complex invariants
- Abstract STOBJs simplify these invariants by maintaining an equivalence
- Currently developing a “cons-less” model that does not use STOBJs, but organizes data structures in a similar way.
- Refinements
- Litany of transformations eventually resulting in array-like code

Proof Properties

Easy to Emit

Compact


Checked Efficiently

Verified Checker

Expressive

 Resolution Proofs

 with Verified Checker

 Clausal (RUP) Proofs

 DRUP (DRUP-Trim)

 RAT Proofs

 with Verified Checker

 Proposed Work