

ACL2 Code Proofs

J Strother Moore
Fall, 2013

Lecture 1

About this Course

Very fast, very simple, focused entirely on ACL2 users

Training: Students taught to use tools they don't understand

Education: Students taught to understand tools they can't use

This course errs on the 'education' side. It is one thing to understand in principle how code proofs are done and another to do them at X86-scale. But I want you to *understand*.

The M1 Directory

The M1 model and many proofs about it are distributed as part of the ACL2 Community Books. See

```
books/models/jvm/m1/
```

```
on :dir :system.
```

Henceforth, I call this the “M1 directory.”

The M1 Package

The M1 symbol package imports all the usual ACL2 symbols except

- `push`, `pop`, `pc`, `program`, `step` – reserved for concepts within the M1 semantics
- `nth`, `update-nth`, `nth-update-nth` – to force JVM students to define some elementary list processing functions

Nth and Update-nth

My m1.lisp file defines these two functions and provides the following rules

```
(defthm nth-add1!  
  (implies (natp n)  
            (equal (nth (+ 1 n) list)  
                   (nth n (cdr list)))))  
  
(defthm nth-update-nth  
  (implies (and (natp i) (natp j))  
            (equal (nth i (update-nth j v list))  
                   (if (equal i j)  
                       v  
                       (nth i list)))))
```

```
(defthm update-nth-update-nth-2 ; at same index
  (equal (update-nth i v (update-nth i w list))
         (update-nth i v list)))
```

```
(defthm update-nth-update-nth-1 ; at different indices
  (implies
    (and (natp i)
          (natp j)
          (not (equal i j))))
    (equal (update-nth i v (update-nth j w list))
            (update-nth j w (update-nth i v list))))

:rule-classes
((:rewrite :loop-stopper ((i j update-nth))))
```

Terminology

An *instruction* contains an opcode and some operands or “arguments.”

A *program* is a list of instructions.

An M1 *state* is determined by

- a program counter
- a sequence of local variable values
- a stack
- a program

M1 Instruction Set

Operation	short description
Format	layout of opcode and args
Stack	<i>stack before</i> \Rightarrow <i>stack after</i>
Description	longer description

Stacks are displayed with the topmost item on the right. Unless otherwise noted, the program counter is always incremented by one.

ILOAD

Operation push local n

Format (`ILOAD n`)

Stack $\dots \Rightarrow \dots, v$

Description The value v of local variable n is pushed onto the stack.

ICONST

Operation push constant

Format (ICONST *c*)

Stack $\dots \Rightarrow \dots, c$

Description The constant *c* is pushed onto the stack.

IADD

Operation	add two integers
Format	(IADD)
Stack	$\dots, v_1, v_2 \Rightarrow \dots, r$
Description	Both v_1 and v_2 must be integers. The values are popped from the stack. Their sum, r , is pushed onto the stack.

ISUB

Operation subtract two integers

Format (ISUB)

Stack $\dots, v_1, v_2 \Rightarrow \dots, r$

Description Both v_1 and v_2 must be integers. The values are popped from the stack. The result, r , is $v_1 - v_2$ and is pushed onto the stack.

IMUL

Operation multiply two integers

Format (IMUL)

Stack $\dots, v_1, v_2 \Rightarrow \dots, r$

Description Both v_1 and v_2 must be integers. The values are popped from the stack. Their product, r , is pushed onto the stack.

ISTORE

Operation store into local n

Format (ISTORE n)

Stack $\dots, v \Rightarrow \dots$

Description The value, v , on top of the stack is removed and stored into local n .

GOTO

Operation jump by n

Format (`GOTO n`)

Stack $\dots \Rightarrow \dots$

Description Execution proceeds at offset n from this instruction, where n may be positive or negative. The target address must be in the current program.

IFEQ

Operation	conditional jump by n
Format	(IFEQ n)
Stack	$\dots, v \Rightarrow \dots$
Description	Execution proceeds at offset n from this instruction if v is 0 and at the next instruction otherwise. Pop the stack.

Halting

Any instruction other than one of those above halts the machine.

It is convenient to introduce a canonical *halt* instruction with opcode HALT

ACL2 Definition of M1

See the community book:

```
(include-book "models/jvm/m1/m1" :dir :system)
```

```
(in-package "M1")
```

Demo

An M1 Program

```
((ICONST 0)      ; 0
 (ISTORE 2)      ; 1  a = 0;
 (ILOAD 0)       ; 2  [loop:]
 (IFEQ 10)       ; 3  if x=0 then go to end;
 (ILOAD 0)       ; 4
 (ICONST 1)      ; 5
 (ISUB)          ; 6
 (ISTORE 0)      ; 7  x = x-1;
 (ILOAD 1)       ; 8
 (ILOAD 2)       ; 9
 (IADD)          ;10
 (ISTORE 2)      ;11  a = y+a;
 (GOTO -10)      ;12  go to loop
 (ILOAD 2)       ;13  [end:]
 (HALT))         ;14  ‘‘return’’ a
```

Demo

How Long Does *pi* Run?

```
(defconst *pi*  
  '((ICONST 0)      ; 0  
    (ISTORE 2)     ; 1  a = 0;  
    (ILOAD 0)      ; 2  [loop:]  
    (IFEQ 10)      ; 3  if x=0 then go to end;  
    (ILOAD 0)      ; 4  
    (ICONST 1)     ; 5  
    (ISUB)         ; 6  
    (ISTORE 0)     ; 7  x = x-1;  
    (ILOAD 1)      ; 8  
    (ILOAD 2)      ; 9  
    (IADD)         ;10  
    (ISTORE 2)     ;11  a = y+a;  
    (GOTO -10)     ;12  go to loop  
    (ILOAD 2)      ;13  [end:]  
    (HALT)))      ;14  'return' a
```


PC at Loop and X=0

```
(defconst *pi*
  '( (ICONST 0)      ; 0
      (ISTORE 2)    ; 1  a = 0;
      (ILOAD 0)     ; 2  [loop:]
      (IFEQ 10)     ; 3  if x=0 then go to end;
      (ILOAD 0)     ; 4
      (ICONST 1)    ; 5
      (ISUB)        ; 6
      (ISTORE 0)    ; 7  x = x-1;
      (ILOAD 1)     ; 8
      (ILOAD 2)     ; 9
      (IADD)        ;10
      (ISTORE 2)    ;11  a = y+a;
      (GOTO -10)    ;12  go to loop
      (ILOAD 2)     ;13  [end:]
      (HALT)))      ;14  'return' a
```

PC at Loop and $X > 0$

```
(defconst *pi*
  '( (ICONST 0)      ; 0
      (ISTORE 2)    ; 1  a = 0;
      (ILOAD 0)     ; 2  [loop:]
      (IFEQ 10)     ; 3  if x=0 then go to end;
      (ILOAD 0)     ; 4
      (ICONST 1)    ; 5
      (ISUB)        ; 6
      (ISTORE 0)    ; 7  x = x-1;
      (ILOAD 1)     ; 8
      (ILOAD 2)     ; 9
      (IADD)        ;10
      (ISTORE 2)    ;11  a = y+a;
      (GOTO -10)    ;12  go to loop
      (ILOAD 2)     ;13  [end:]
      (HALT)))     ;14  'return' a
```

PC at Top

```
(defconst *pi*
  '( (ICONST 0)      ; 0
      (ISTORE 2)    ; 1  a = 0;
      (ILOAD 0)     ; 2  [loop:]
      (IFEQ 10)     ; 3  if x=0 then go to end;
      (ILOAD 0)     ; 4
      (ICONST 1)    ; 5
      (ISUB)        ; 6
      (ISTORE 0)    ; 7  x = x-1;
      (ILOAD 1)     ; 8
      (ILOAD 2)     ; 9
      (IADD)        ;10
      (ISTORE 2)    ;11  a = y+a;
      (GOTO -10)    ;12  go to loop
      (ILOAD 2)     ;13  [end:]
      (HALT)))     ;14  'return' a
```

The Clock for *pi*

```
(defun loop-clk (x)
  (if (zp x)
      3
      (clk+ 11
            (loop-clk (- x 1)))))
```

```
(defun clk (x)
  (clk+ 2
        (loop-clk x)))
```

Demo

Turing Equivalence

M1 is a trivial machine, but it is *Turing Equivalent*: anything a Turing Machine can do, M1 can do.

This can be proved with ACL2. See

`turing-equivalence-talk.pdf`

on the M1 directory. For a guide to the proof files, see the discussion of “The Turing Equivalence of M1” in the README file of the M1 directory.

Symbolic Evaluation

(m1 s 11)

=

(m1 (step s) 10)

=

...

=

(step (step (step ... (step s)...)))

Symbolic Evaluation

Suppose $\text{natp}: x, y, a$ and $\neg(\text{zp } x)$.

```
(m1                                     ; 2. (ILOAD 0)
  (make-state 2                         ; 3. (IFEQ 10)
    (list x y a)                        ; 4. (ILOAD 0)
    nil                                  ; 5. (ICONST 1)
    *pi*)                                ; 6. (ISUB)
11)                                     ; 7. (ISTORE 0)
                                       ; 8. (ILOAD 1)
                                       ; 9. (ILOAD 2)
                                       ;10. (IADD)
                                       ;11. (ISTORE 2)
                                       ;12. (GOTO -10)
```


Symbolic Evaluation

Suppose natp: x, y, a and $\neg(zp\ x)$.

```
(m1                                     ; 2. (ILOAD 0)
  (make-state 3                         ; 3. (IFEQ 10)
    (list x y a)                        ; 4. (ILOAD 0)
    (push x nil)                        ; 5. (ICONST 1)
    *pi*)                               ; 6. (ISUB)
10)                                     ; 7. (ISTORE 0)
                                       ; 8. (ILOAD 1)
                                       ; 9. (ILOAD 2)
                                       ;10. (IADD)
                                       ;11. (ISTORE 2)
                                       ;12. (GOTO -10)
```

Symbolic Evaluation

Suppose natp: x, y, a and $\neg(zp\ x)$.

```
(m1                                     ; 2. (ILOAD 0)
  (make-state 4                         ; 3. (IFEQ 10)
    (list x y a)                        ; 4. (ILOAD 0)
    nil                                  ; 5. (ICONST 1)
    *pi*)                                ; 6. (ISUB)
9)                                       ; 7. (ISTORE 0)
                                       ; 8. (ILOAD 1)
                                       ; 9. (ILOAD 2)
                                       ;10. (IADD)
                                       ;11. (ISTORE 2)
                                       ;12. (GOTO -10)
```

Symbolic Evaluation

Suppose natp: x, y, a and $\neg(zp\ x)$.

```
(m1                                     ; 2. (ILOAD 0)
  (make-state 5                         ; 3. (IFEQ 10)
    (list x y a)                        ; 4. (ILOAD 0)
    (push x nil)                        ; 5. (ICONST 1)
    *pi*)                               ; 6. (ISUB)
8)                                     ; 7. (ISTORE 0)
                                       ; 8. (ILOAD 1)
                                       ; 9. (ILOAD 2)
                                       ;10. (IADD)
                                       ;11. (ISTORE 2)
                                       ;12. (GOTO -10)
```

Symbolic Evaluation

Suppose natp: x, y, a and $\neg(zp\ x)$.

```
(m1                                     ; 2. (ILOAD 0)
  (make-state 6                         ; 3. (IFEQ 10)
    (list x y a)                         ; 4. (ILOAD 0)
    (push 1 (push x nil))                ; 5. (ICONST 1)
    *pi*)                                ; 6. (ISUB)
7)                                       ; 7. (ISTORE 0)
                                       ; 8. (ILOAD 1)
                                       ; 9. (ILOAD 2)
                                       ;10. (IADD)
                                       ;11. (ISTORE 2)
                                       ;12. (GOTO -10)
```

Symbolic Evaluation

Suppose natp: x, y, a and $\neg(zp\ x)$.

```
(m1                                     ; 2. (ILOAD 0)
  (make-state 7                         ; 3. (IFEQ 10)
    (list x y a)                         ; 4. (ILOAD 0)
    (push (- x 1) nil)                   ; 5. (ICONST 1)
    *pi*)                                 ; 6. (ISUB)
6)                                       ; 7. (ISTORE 0)
                                       ; 8. (ILOAD 1)
                                       ; 9. (ILOAD 2)
                                       ;10. (IADD)
                                       ;11. (ISTORE 2)
                                       ;12. (GOTO -10)
```

Symbolic Evaluation

Suppose natp: x, y, a and $\neg(zp\ x)$.

```
(m1                                     ; 2. (ILOAD 0)
  (make-state 8                         ; 3. (IFEQ 10)
    (list (- x 1) y a)                 ; 4. (ILOAD 0)
    nil                                 ; 5. (ICONST 1)
    *pi*)                               ; 6. (ISUB)
5)                                       ; 7. (ISTORE 0)
                                       ; 8. (ILOAD 1)
                                       ; 9. (ILOAD 2)
                                       ;10. (IADD)
                                       ;11. (ISTORE 2)
                                       ;12. (GOTO -10)
```

Symbolic Evaluation

Suppose natp: x, y, a and $\neg(zp\ x)$.

```
(m1                                     ; 2. (ILOAD 0)
  (make-state 9                         ; 3. (IFEQ 10)
    (list (- x 1) y a)                  ; 4. (ILOAD 0)
    (push y nil)                        ; 5. (ICONST 1)
    *pi*)                                ; 6. (ISUB)
4)                                       ; 7. (ISTORE 0)
                                       ; 8. (ILOAD 1)
                                       ; 9. (ILOAD 2)
                                       ;10. (IADD)
                                       ;11. (ISTORE 2)
                                       ;12. (GOTO -10)
```

Symbolic Evaluation

Suppose natp: x, y, a and $\neg(zp\ x)$.

```
(m1                                     ; 2. (ILOAD 0)
  (make-state 10                       ; 3. (IFEQ 10)
    (list (- x 1) y a)                 ; 4. (ILOAD 0)
    (push a (push y nil))              ; 5. (ICONST 1)
    *pi*)                               ; 6. (ISUB)
3)                                       ; 7. (ISTORE 0)
                                       ; 8. (ILOAD 1)
                                       ; 9. (ILOAD 2)
                                       ;10. (IADD)
                                       ;11. (ISTORE 2)
                                       ;12. (GOTO -10)
```


Symbolic Evaluation

Suppose natp: x, y, a and $\neg(zp\ x)$.

```
(m1                                     ; 2. (ILOAD 0)
  (make-state 11                       ; 3. (IFEQ 10)
    (list (- x 1) y a)                 ; 4. (ILOAD 0)
    (push (+ y a) nil)                 ; 5. (ICONST 1)
    *pi*)                              ; 6. (ISUB)
2)                                     ; 7. (ISTORE 0)
                                       ; 8. (ILOAD 1)
                                       ; 9. (ILOAD 2)
                                       ;10. (IADD)
                                       ;11. (ISTORE 2)
                                       ;12. (GOTO -10)
```

Symbolic Evaluation

Suppose natp: x, y, a and $\neg(zp\ x)$.

```
(m1                                     ; 2. (ILOAD 0)
  (make-state 12                       ; 3. (IFEQ 10)
    (list (- x 1) y (+ y a))           ; 4. (ILOAD 0)
    nil                                 ; 5. (ICONST 1)
    *pi*)                              ; 6. (ISUB)
1)                                     ; 7. (ISTORE 0)
                                       ; 8. (ILOAD 1)
                                       ; 9. (ILOAD 2)
                                       ;10. (IADD)
                                       ;11. (ISTORE 2)
                                       ;12. (GOTO -10)
```

Symbolic Evaluation

Suppose natp: x, y, a and $\neg(zp\ x)$.

```
(m1                                     ; 2. (ILOAD 0)
  (make-state 2                         ; 3. (IFEQ 10)
    (list (- x 1) y (+ y a))           ; 4. (ILOAD 0)
    nil                                 ; 5. (ICONST 1)
    *pi*)                              ; 6. (ISUB)
0)                                     ; 7. (ISTORE 0)
                                       ; 8. (ILOAD 1)
                                       ; 9. (ILOAD 2)
                                       ;10. (IADD)
                                       ;11. (ISTORE 2)
                                       ;12. (GOTO -10)
```

Symbolic Evaluation

Suppose $\text{natp}: x, y, a$ and $\neg(\text{zp } x)$.

```
(make-state 2 ; 2. (ILOAD 0)
  (list (- x 1) y (+ y a)) ; 3. (IFEQ 10)
  nil ; 4. (ILOAD 0)
  *pi*) ; 5. (ICONST 1)
        ; 6. (ISUB)
        ; 7. (ISTORE 0)
        ; 8. (ILOAD 1)
        ; 9. (ILOAD 2)
        ;10. (IADD)
        ;11. (ISTORE 2)
        ;12. (GOTO -10)
```

Symbolic Evaluation

Possible when `next-inst` and `clock` are fixed.

But case explosion is likely

```
(step (step ... (step (step s))...))
```

if the **outer steps** are expanded before it is possible to determine the `next-inst` produced by the **inner step**.

How do we make ACL2 do symbolic evaluation?

Controlling Symbolic Evaluation

```
; Abstract Data Type Stuff
```

```
(defthm stacks  
  (and (equal (top (push x s)) x)  
        (equal (pop (push x s)) s)  
        (equal (top (cons x s)) x)  
        (equal (pop (cons x s)) s))))
```

```
(in-theory (disable push top pop  
            (:executable-counterpart push)))
```

```

(defthm states
  (and
    (equal (pc (make-state pc locs stk prg))      pc)
    (equal (locals (make-state pc locs stk prg))  locs)
    (equal (stack (make-state pc locs stk prg))   stk)
    (equal (program (make-state pc locs stk prg)) prg)

    (equal (pc (cons pc x))                       pc)
    (equal (locals (cons pc (conslocs x)))         locs)
    ...))

(in-theory (disable make-state pc locals stack program
  (:executable-counterpart make-state)))

```

; Step Stuff

```
(defthm step-opener
  (implies (consp (next-inst s))
           (equal (step s)
                  (do-inst (next-inst s) s))))
(in-theory (disable step))

(defthm m1-opener
  (and (equal (m1 s 0) s)
       (implies (natp i)
                (equal (m1 s (+ 1 i))
                       (m1 (step s) i))))))
(in-theory (disable m1))
```


Demo

Stating Correctness

Partial Correctness:

If computation on ok inputs halts, then the answer is as expected.

Total Correctness:

Computation from ok inputs halts and the answer is as expected.

State-Based Formalization

The most general way to formulate these conditions is in terms of entire M1 states.

(pre s_i) – checks that s_i is an acceptable initial state for your program

(post s_i s_f) – checks that the final state, s_f , is in the expected relation with the initial state s_i

Most-General State Based Partial Correctness

$$\begin{aligned} &\forall s_i, \kappa : \\ &((\text{pre } s_i) \\ &\quad \wedge \\ &\quad (\text{haltedp } (m1 \ s_i \ \kappa))) \\ &\rightarrow \\ &(\text{post } s_i \ (m1 \ s_i \ \kappa)) \end{aligned}$$

Most-General State Based Total Correctness

$\forall s_i \exists \kappa :$

$(\text{pre } s_i)$

\rightarrow

$((\text{haltedp } (m1 \ s_i \ \kappa)))$

\wedge

$(\text{post } s_i \ (m1 \ s_i \ \kappa))$

However...

It is generally more convenient to relate program inputs and outputs rather than whole states.

Partial Correctness

“If computation on ok inputs halts, then the answer is as expected.”

$$\begin{aligned} & (s_i = (\text{make-state } 0 \text{ (list } v_1 \dots) \text{ nil } \pi)) \\ & \wedge (\text{ok-inputs } v_1 \dots) \\ & \wedge s_f = (\text{m1 } s_i \kappa) \\ & \wedge (\text{haltedp } s_f)) \end{aligned}$$

→

$$(\text{top (stack } s_f)) = \theta.$$

Partial Correctness (in ACL2)

```
(implies
  (and
    (equal  $s_i$  (make-state 0 (list  $v_1 \dots$ ) nil  $\pi$ ))
    (ok-inputs  $v_1 \dots$ )
    (equal  $s_f$  (m1  $s_i$   $\kappa$ ))
    (haltedp  $s_f$ ))
  (equal (top (stack  $s_f$ ))  $\theta$ ))
```

You may wish to re-state this as a more effective rule.

Total Correctness

“Computation from ok inputs halts and the answer is as expected.”

$\exists \kappa :$

($s_i = (\text{make-state } 0 \text{ (list } v_1 \dots) \text{ nil } \pi)$
 $\wedge (\text{ok-inputs } v_1 \dots)$
 $\wedge s_f = (\text{m1 } s_i \kappa)$)

\rightarrow

($(\text{haltedp } s_f)$
 \wedge
 $(\text{top (stack } s_f)) = \theta$) .

Total Correctness (in ACL2)

“Computation from ok inputs halts and the answer is as expected.”

$\exists \kappa :$

(implies

(and

(equal s_i (make-state 0 (list $v_1 \dots$) nil π))

(ok-inputs $v_1 \dots$)

(equal s_f (m1 s_i κ)))

(and (haltedp s_f)

(equal (top (stack s_f)) θ)))

Total Correctness (in ACL2)

“Computation from ok inputs halts and the answer is as expected.”

$\exists \kappa$:

(implies

(and

(equal s_i (make-state 0 (list $v_1 \dots$) nil π))

(ok-inputs $v_1 \dots$)

(equal s_f (m1 $s_i \kappa$)))

(and (haltedp s_f)

(equal (top (stack s_f)) θ)))

Total Correctness (in ACL2)

“Computation from ok inputs halts and the answer is as expected.”

```
(implies
  (and
    (equal  $s_i$  (make-state 0 (list  $v_1 \dots$ ) nil  $\pi$ ))
    (ok-inputs  $v_1 \dots$ )
    (equal  $s_f$  (m1  $s_i$  (clk  $v_1 \dots$ ))))
  (and (haltedp  $s_f$ )
    (equal (top (stack  $s_f$ ))  $\theta$ )))
```

Bogus Total Correctness (in ACL2)

“Computation from ok inputs eventually produces the expected answer”

```
(implies
  (and
    (equal  $s_i$  (make-state 0 (list  $v_1 \dots$ ) nil  $\pi$ ))
    (ok-inputs  $v_1 \dots$ )
    (equal  $s_f$  (m1  $s_i$  (clk  $v_1 \dots$ ))))
  (and (haltedp  $s_f$ )
    (equal (top (stack  $s_f$ ))  $\theta$ )))
```

Bogus Total Correctness (in ACL2)

“Computation from ok inputs eventually produces the expected answer”

```
(implies
  (and
    (equal  $s_i$  (make-state 0 (list  $v_1 \dots$ ) nil  $\pi$ ))
    (ok-inputs  $v_1 \dots$ )
    (equal  $s_f$  (m1  $s_i$  (clk  $v_1 \dots$ ))))
  (equal (top (stack  $s_f$ ))  $\theta$ ))
```

Bogus Correctness

It is possible to prove that the program:

```
((ICONST 0)      ; 0   tos = 0;  
 (ICONST 1)      ; 1   loop:  
 (IADD)          ; 2   tos = tos+1;  
 (GOTO -2))      ; 3   goto loop;
```

satisfies the Bogus Correctness Theorem for any natural number producing function, i.e., this Π is a “correct” implementation of every natural number-producing function!

Magic.lisp on M1 Directory

```
(defthm pi-bogusly-computes-fact
  (implies
    (and (natp n)
          (equal sf (m1 (make-state 0 (list n) nil  $\Pi$ )
                        (fact-clk n))))
    (equal (top (stack sf)) (fact n))))
```

```
(defthm pi-bogusly-computes-fib
  (implies
    (and (natp n)
          (equal sf (m1 (make-state 0 (list n) nil  $\Pi$ )
                        (fib-clk n))))
    (equal (top (stack sf)) (fib n))))
```


Now Back to Total Correctness

How do we get ACL2 to do total correctness proofs?

Moving Parts of M1 Proof Engine

- clock functions ✓
- symbolic evaluation ✓
- sequential composition
- how they mesh
- behavior v intention
- dealing with loops
- separation of concerns

Sequential Composition

Thm. $(m1\ s\ (+\ i\ j)) = (m1\ (m1\ s\ i)\ j)$

a.k.a.

```
(defthm m1-clk+  
  (equal (m1 s (clk+ i j))  
         (m1 (m1 s i) j)))
```

where, for reasons explained later

$$(clk+ i j) = (+ i j)$$

and `clk+` is disabled.

Sequential Composition

Thm. $(m1 \text{ s } (+ \ i \ j)) = (m1 \ (m1 \ \text{s} \ i) \ j)$

Proof. By induction on i .

Aside on Induction

Thm. $\forall i, j, s : (\phi \ i \ j \ s)$

Proof. By induction on i .

Base:

$$(zp \ i) \rightarrow \forall j, s : (\phi \ i \ j \ s)$$

Induction Step:

$$(\neg(zp \ i))$$

$$\wedge$$

$$\forall j, s : (\phi \ (- \ i \ 1) \ j \ s))$$

$$\rightarrow$$

$$\forall j, s : (\phi \ i \ j \ s).$$

Aside on Induction

Thm. $\forall i, j, s : (\phi \ i \ j \ s)$

Proof. By induction on i .

Base:

$$(zp \ i) \rightarrow \forall j, s : (\phi \ i \ j \ s)$$

Induction Step:

$$(\neg(zp \ i))$$

\wedge

$$\forall j, s : (\phi \ (- \ i \ 1) \ j \ s))$$

\rightarrow

$$\forall j, s : (\phi \ i \ j \ s).$$

Aside on Induction

Thm. $(\phi \ i \ j \ s)$

Proof. By induction on i .

Base:

$$(zp \ i) \rightarrow \forall j, s : (\phi \ i \ j \ s)$$

Induction Step:

$$(\neg(zp \ i))$$

$$\wedge$$

$$\forall j, s : (\phi \ (- \ i \ 1) \ j \ s))$$

$$\rightarrow$$

$$\forall j, s : (\phi \ i \ j \ s).$$

Aside on Induction

Thm. $(\phi \ i \ j \ s)$

Proof. By induction on i .

Base:

$$(zp \ i) \rightarrow \forall j, s : (\phi \ i \ j \ s)$$

Induction Step:

$$(\neg(zp \ i))$$

\wedge

$$\forall j, s : (\phi \ (- \ i \ 1) \ j \ s))$$

\rightarrow

$$\forall j, s : (\phi \ i \ j \ s).$$

Aside on Induction

Thm. $(\phi \ i \ j \ s)$

Proof. By induction on i .

Base:

$$(zp \ i) \rightarrow (\phi \ i \ j \ s)$$

Induction Step:

$$(\neg(zp \ i))$$

\wedge

$$\forall j, s : (\phi \ (- \ i \ 1) \ j \ s))$$

\rightarrow

$$\forall j, s : (\phi \ i \ j \ s).$$

Aside on Induction

Thm. $(\phi \ i \ j \ s)$

Proof. By induction on i .

Base:

$$(zp \ i) \rightarrow (\phi \ i \ j \ s)$$

Induction Step:

$$(\neg(zp \ i))$$

\wedge

$$\forall j, s : (\phi \ (- \ i \ 1) \ j \ s))$$

\rightarrow

$$\forall j, s : (\phi \ i \ j \ s).$$

Aside on Induction

Thm. $(\phi \ i \ j \ s)$

Proof. By induction on i .

Base:

$$(zp \ i) \rightarrow (\phi \ i \ j \ s)$$

Induction Step:

$$(\neg(zp \ i))$$

\wedge

$$\forall j, s : (\phi \ (- \ i \ 1) \ j \ s))$$

\rightarrow

$$(\phi \ i \ j \ s).$$

Aside on Induction

Thm. $(\phi \ i \ j \ s)$

Proof. By induction on i .

Base:

$$(zp \ i) \rightarrow (\phi \ i \ j \ s)$$

Induction Step:

$$(\neg(zp \ i))$$

\wedge

$$\forall j, s : (\phi \ (- \ i \ 1) \ j \ s))$$

\rightarrow

$$(\phi \ i \ j \ s).$$

Aside on Induction

Thm. $(\phi \ i \ j \ s)$

Proof. By induction on i .

Base:

$$(zp \ i) \rightarrow (\phi \ i \ j \ s)$$

Induction Step:

$$(\neg(zp \ i))$$

$$\wedge (\phi \ (- \ i \ 1) \ \alpha_1 \ \beta_1)$$

$$\wedge \forall j, s : (\phi \ (- \ i \ 1) \ j \ s))$$

\rightarrow

$$(\phi \ i \ j \ s).$$

Aside on Induction

Thm. $(\phi \ i \ j \ s)$

Proof. By induction on i .

Base:

$$(zp \ i) \rightarrow (\phi \ i \ j \ s)$$

Induction Step:

$$(\neg(zp \ i))$$

$$\wedge (\phi \ (- \ i \ 1) \ \alpha_1 \ \beta_1) \wedge (\phi \ (- \ i \ 1) \ \alpha_2 \ \beta_2)$$

$$\wedge \forall j, s : (\phi \ (- \ i \ 1) \ j \ s))$$

\rightarrow

$$(\phi \ i \ j \ s).$$

Aside on Induction

Thm. $(\phi \ i \ j \ s)$

Proof. By induction on i .

Base:

$$(zp \ i) \rightarrow (\phi \ i \ j \ s)$$

Induction Step:

$$(\neg(zp \ i))$$

$$\wedge (\phi \ (- \ i \ 1) \ \alpha_1 \ \beta_1) \ \dots \ \wedge (\phi \ (- \ i \ 1) \ \alpha_k \ \beta_k)$$

$$\wedge \forall j, s : (\phi \ (- \ i \ 1) \ j \ s))$$

\rightarrow

$$(\phi \ i \ j \ s).$$

Aside on Induction

Thm. $(\phi \ i \ j \ s)$

Proof. By induction on i .

Base:

$$(zp \ i) \rightarrow (\phi \ i \ j \ s)$$

Induction Step:

$$\begin{aligned} & (\neg(zp \ i) \\ & \wedge (\phi \ (- \ i \ 1) \ \alpha_1 \ \beta_1) \ \dots \ \wedge (\phi \ (- \ i \ 1) \ \alpha_k \ \beta_k) \\ &) \end{aligned}$$

\rightarrow

$$(\phi \ i \ j \ s).$$

Sequential Composition

Thm. $(m1\ s\ (+\ i\ j)) = (m1\ (m1\ s\ i)\ j)$

Proof. By induction on i .

Base:

$(zp\ i)$

\rightarrow

$(m1\ s\ (+\ i\ j)) = (m1\ (m1\ s\ i)\ j)$

Sequential Composition

Thm. $(m1\ s\ (+\ i\ j)) = (m1\ (m1\ s\ i)\ j)$

Proof. By induction on i .

Base:

$(zp\ i)$

\rightarrow

$(m1\ s\ (+\ i\ j)) = (m1\ (m1\ s\ i)\ j)$

Sequential Composition

Thm. $(m1\ s\ (+\ i\ j)) = (m1\ (m1\ s\ i)\ j)$

Proof. By induction on i .

Base:

$(zp\ i)$

\rightarrow

$(m1\ s\ j) = (m1\ (m1\ s\ i)\ j)$

Sequential Composition

Thm. $(m1\ s\ (+\ i\ j)) = (m1\ (m1\ s\ i)\ j)$

Proof. By induction on i .

Base:

$(zp\ i)$

\rightarrow

$(m1\ s\ j) = (m1\ (m1\ s\ i)\ j)$

Sequential Composition

Thm. $(m1\ s\ (+\ i\ j)) = (m1\ (m1\ s\ i)\ j)$

Proof. By induction on i .

Base:

$(zp\ i)$

\rightarrow

$(m1\ s\ j) = (m1\ s\ j)$

Sequential Composition

Thm. $(m1\ s\ (+\ i\ j)) = (m1\ (m1\ s\ i)\ j)$

Proof. By induction on i .

Base:

$(zp\ i)$

\rightarrow

$(m1\ s\ j) = (m1\ s\ j)$ ✓

Sequential Composition

Thm. $(m1\ s\ (+\ i\ j)) = (m1\ (m1\ s\ i)\ j)$

Proof. By induction on i .

Base: ✓

Induction Step:

$(\neg(zp\ i))$

\wedge

$(m1\ \alpha\ (+\ (-\ i\ 1)\ \beta))$

$= (m1\ (m1\ \alpha\ (-\ i\ 1))\ \beta))$

\rightarrow

$(m1\ s\ (+\ i\ j))$

$= (m1\ (m1\ s\ i)\ j)$

Sequential Composition

Thm. $(m1\ s\ (+\ i\ j)) = (m1\ (m1\ s\ i)\ j)$

Proof. By induction on i .

Base: ✓

Induction Step:

$(\neg(zp\ i))$

\wedge

$(m1\ \alpha\ (+\ (-\ i\ 1)\ \beta))$

$= (m1\ (m1\ \alpha\ (-\ i\ 1))\ \beta))$

\rightarrow

$(m1\ s\ (+\ i\ j))$

$= (m1\ (m1\ s\ i)\ j)$

Sequential Composition

Thm. $(m1\ s\ (+\ i\ j)) = (m1\ (m1\ s\ i)\ j)$

Proof. By induction on i .

Base: ✓

Induction Step:

$(\neg(zp\ i))$

\wedge

$(m1\ \alpha\ (+\ (-\ i\ 1)\ \beta))$

$= (m1\ (m1\ \alpha\ (-\ i\ 1))\ \beta))$

\rightarrow

$(m1\ s\ (+\ i\ j))$

$= (m1\ (m1\ s\ i)\ j)$

Sequential Composition

Thm. $(m1\ s\ (+\ i\ j)) = (m1\ (m1\ s\ i)\ j)$

Proof. By induction on i .

Base: ✓

Induction Step:

$(\neg(zp\ i))$

\wedge

$(m1\ \alpha\ (+\ (-\ i\ 1)\ \beta))$

$= (m1\ (m1\ \alpha\ (-\ i\ 1))\ \beta))$

\rightarrow

$(m1\ (\text{step}\ s)\ (+\ (-\ i\ 1)\ j))$

$= (m1\ (m1\ s\ i)\ j)$

Sequential Composition

Thm. $(m1\ s\ (+\ i\ j)) = (m1\ (m1\ s\ i)\ j)$

Proof. By induction on i .

Base: ✓

Induction Step:

$(\neg(zp\ i))$

\wedge

$(m1\ \alpha\ (+\ (-\ i\ 1)\ \beta))$

$= (m1\ (m1\ \alpha\ (-\ i\ 1))\ \beta))$

\rightarrow

$(m1\ (\text{step}\ s)\ (+\ (-\ i\ 1)\ j))$

$= (m1\ (m1\ s\ i)\ j)$

Sequential Composition

Thm. $(m1\ s\ (+\ i\ j)) = (m1\ (m1\ s\ i)\ j)$

Proof. By induction on i .

Base: ✓

Induction Step:

$(\neg(zp\ i))$

\wedge

$(m1\ \alpha\ (+\ (-\ i\ 1)\ \beta))$

$= (m1\ (m1\ \alpha\ (-\ i\ 1))\ \beta))$

\rightarrow

$(m1\ (\text{step}\ s)\ (+\ (-\ i\ 1)\ j))$

$= (m1\ (m1\ (\text{step}\ s)\ (-\ i\ 1))\ j)$

Sequential Composition

Thm. $(m1\ s\ (+\ i\ j)) = (m1\ (m1\ s\ i)\ j)$

Proof. By induction on i .

Base: ✓

Induction Step:

$(\neg(zp\ i))$

\wedge

$(m1\ \alpha\ (+\ (-\ i\ 1)\ \beta))$

$= (m1\ (m1\ \alpha\ (-\ i\ 1))\ \beta))$

\rightarrow

$(m1\ (\text{step}\ s)\ (+\ (-\ i\ 1)\ j))$

$= (m1\ (m1\ (\text{step}\ s)\ (-\ i\ 1))\ j)$

Sequential Composition

Thm. $(m1\ s\ (+\ i\ j)) = (m1\ (m1\ s\ i)\ j)$

Proof. By induction on i .

Base: ✓

Induction Step:

$(\neg(zp\ i))$

\wedge

$(m1\ (\text{step } s)\ (+\ (-\ i\ 1)\ j))$

$= (m1\ (m1\ (\text{step } s)\ (-\ i\ 1))\ j))$

\rightarrow

$(m1\ (\text{step } s)\ (+\ (-\ i\ 1)\ j))$

$= (m1\ (m1\ (\text{step } s)\ (-\ i\ 1))\ j)$

Sequential Composition

Thm. $(m1\ s\ (+\ i\ j)) = (m1\ (m1\ s\ i)\ j)$

Proof. By induction on i .

Base: ✓

Induction Step:

$(\neg(zp\ i))$

\wedge

$(m1\ (\text{step } s)\ (+\ (-\ i\ 1)\ j))$

$= (m1\ (m1\ (\text{step } s)\ (-\ i\ 1))\ j))$

\rightarrow

$(m1\ (\text{step } s)\ (+\ (-\ i\ 1)\ j))$

$= (m1\ (m1\ (\text{step } s)\ (-\ i\ 1))\ j)$ ✓

Sequential Composition

Thm. $(m1\ s\ (+\ i\ j)) = (m1\ (m1\ s\ i)\ j)$

Proof. By induction on i .

Base: ✓

Induction Step: ✓

Q.E.D.

Note that we did not even need to expand step!

Demo

Moving Parts of M1 Proof Engine

- clock functions ✓
- symbolic evaluation ✓
- sequential composition ✓
- how they mesh
- behavior v intention
- dealing with loops
- separation of concerns

How They Mesh

Thm. $(\phi (m1\ s\ (clk\ x)))$; *s must be ‘‘semi-explicit’’*

By “semi-explicit” I mean that $(next-inst\ s)$ must simplify to a constant under the available hypotheses.

How They Mesh

Thm. $(\phi (m1\ s\ (clk\ x)))$

Base:

$(zp\ x)$

\rightarrow

$(\phi (m1\ s\ (clk\ x)))$

Induction Step:

$(\neg(zp\ x) \wedge (\phi (m1\ \alpha\ (clk\ (-\ x\ 1))))))$

\rightarrow

$(\phi (m1\ s\ (clk\ x)))$

How They Mesh

Thm. $(\phi (m1\ s\ (clk\ x)))$

Base:

$(zp\ x)$

\rightarrow

$(\phi (m1\ s\ (clk\ x))) \ ; \ \{Clock!\}$

Induction Step:

$(\neg(zp\ x) \wedge (\phi (m1\ \alpha\ (clk\ (-\ x\ 1))))))$

\rightarrow

$(\phi (m1\ s\ (clk\ x)))$

How They Mesh

Thm. $(\phi (m1\ s\ (clk\ x)))$

Base:

$(zp\ x)$

\rightarrow

$(\phi (m1\ s\ 3))$

Induction Step:

$(\neg(zp\ x) \wedge (\phi (m1\ \alpha\ (clk\ (-\ x\ 1))))))$

\rightarrow

$(\phi (m1\ s\ (clk\ x)))$

How They Mesh

Thm. $(\phi (m1\ s\ (clk\ x)))$

Base:

$(zp\ x)$

\rightarrow

$(\phi (m1\ s\ 3)) \quad ; \quad \{Symb\ Eval!\}$

Induction Step:

$(\neg(zp\ x) \wedge (\phi (m1\ \alpha\ (clk\ (-\ x\ 1))))))$

\rightarrow

$(\phi (m1\ s\ (clk\ x)))$

How They Mesh

Thm. $(\phi \text{ (m1 s (clk x))})$

Base:

(zp x)

\rightarrow

$(\phi \text{ s'})$

Induction Step:

$(\neg(\text{zp x}) \wedge (\phi \text{ (m1 } \alpha \text{ (clk (- x 1)))))$

\rightarrow

$(\phi \text{ (m1 s (clk x))})$

How They Mesh

Thm. $(\phi (m1\ s\ (clk\ x)))$

Base:

$(zp\ x)$

\rightarrow

$(\phi\ s')$

Induction Step:

$(\neg(zp\ x) \wedge (\phi (m1\ \alpha\ (clk\ (-\ x\ 1))))))$

\rightarrow

$(\phi (m1\ s\ (clk\ x))) ; \{Clock!\}$

How They Mesh

Thm. $(\phi (m1\ s\ (clk\ x)))$

Base:

$(zp\ x)$

\rightarrow

$(\phi\ s')$

Induction Step:

$(\neg(zp\ x) \wedge (\phi (m1\ \alpha\ (clk\ (-\ x\ 1))))))$

\rightarrow

$(\phi (m1\ s\ (clk+ 11\ (clk\ (-\ x\ 1))))))$

Note: We don't want $clk+$ expressions rearranged!

How They Mesh

Thm. $(\phi (m1\ s\ (clk\ x)))$

Base:

$(zp\ x)$

\rightarrow

$(\phi\ s')$

Induction Step:

$(\neg(zp\ x) \wedge (\phi (m1\ \alpha\ (clk\ (-\ x\ 1))))))$

\rightarrow

$(\phi (m1\ s\ (clk+ 11\ (clk\ (-\ x\ 1))))); \{Seq\ Comp!\}$

How They Mesh

Thm. $(\phi (m1\ s\ (clk\ x)))$

Base:

$(zp\ x)$

\rightarrow

$(\phi\ s')$

Induction Step:

$(\neg(zp\ x) \wedge (\phi (m1\ \alpha\ (clk\ (-\ x\ 1))))))$

\rightarrow

$(\phi (m1\ (m1\ s\ 11)\ (clk\ (-\ x\ 1))))$

How They Mesh

Thm. $(\phi (m1\ s\ (clk\ x)))$

Base:

$(zp\ x)$

\rightarrow

$(\phi\ s')$

Induction Step:

$(\neg(zp\ x) \wedge (\phi (m1\ \alpha\ (clk\ (-\ x\ 1))))))$

\rightarrow

$(\phi (m1\ (m1\ s\ 11)\ (clk\ (-\ x\ 1)))) \ ; \ \{Symb\ Eval!\}$

How They Mesh

Thm. $(\phi (m1\ s\ (clk\ x)))$

Base:

$(zp\ x)$

\rightarrow

$(\phi\ s')$

Induction Step:

$(\neg(zp\ x) \wedge (\phi (m1\ \alpha\ (clk\ (-\ x\ 1))))))$

\rightarrow

$(\phi (m1\ s''\ (clk\ (-\ x\ 1))))$

How They Mesh

Thm. $(\phi \text{ (m1 s (clk x))})$

Base:

(zp x)

\rightarrow

$(\phi \text{ s'})$

Induction Step:

$(\neg(\text{zp x}) \wedge (\phi \text{ (m1 } s'' \text{ (clk (- x 1)))))$

\rightarrow

$(\phi \text{ (m1 } s'' \text{ (clk (- x 1))))) ; \{\text{Ind Hyp } \alpha!\}$

Moving Parts of M1 Proof Engine

- clock functions ✓
- symbolic evaluation ✓
- sequential composition ✓
- how they mesh ✓
- behavior v intention
- dealing with loops
- separation of concerns

Behavior v Intention

Decompose code proofs into two big stages:

- behavior: the code π implements some algorithm
- intention: the algorithm satisfies (or is equal to) the specification

In the case of **pi**:

- *algorithm*: (g x y 0), where

```
(defun g (x y a)
  (if (zp x)
      a
      (g (- x 1) y (+ y a))))
```

- *spec*: (* x y)

Behavior v Intention

Decompose code proofs into two big stages:

- behavior: the code π implements $(g \ x \ y \ 0)$.
This is the only part of the proof involving the semantics of the machine.
- intention: $(g \ x \ y \ 0)$ is $(* \ x \ y)$. *This is typically the harder of the two proofs because it lifts the algorithm to some more abstract spec, but it does not involve the semantics of the machine.*

This decomposition is often crucial to managing the complexity of a hard code proof project.

Furthermore, the algorithm often tells ACL2 what it needs to know to do the right induction.

Finish your dealings with the behavior (code v algorithm) before you turn to intention (algorithm v specs).

More precisely, do not let your work on 'intention' interfere with your work on 'behavior.'

Moving Parts of M1 Proof Engine

- clock functions ✓
- symbolic evaluation ✓
- sequential composition ✓
- how they mesh ✓
- behavior v intention ✓
- dealing with loops
- separation of concerns

Dealing with Loops

Consider the ‘behavior’ step – proving that the code implements the algorithm.

If the code loops (i.e., the algorithm is recursive) the proof will be inductive.

Two lessons are drawn from this:

- verify the loop code first – specify the loop as though it were a separate module/subroutine
- make sure your specification characterizes the whole state

Remember: you must be able to symbolically evaluate from where ever the loop (the inductive hypothesis) leaves you!

It is insufficient just to know the *output*.

To the extent that subsequent execution depends on them, you must specify the pc, the locals, the stack, and the program so you can continue executing from the state 'produced' by your inductive hypothesis. [Note: Our running example does not illustrate this lesson.]

Recall Program *pi*

```
(defconst *pi*
  '( (ICONST 0)      ; 0
      (ISTORE 2)    ; 1  a = 0;
      (ILOAD 0)     ; 2  [loop:]
      (IFEQ 10)     ; 3  if x=0 then go to end;
      (ILOAD 0)     ; 4
      (ICONST 1)    ; 5
      (ISUB)        ; 6
      (ISTORE 0)    ; 7  x = x-1;
      (ILOAD 1)     ; 8
      (ILOAD 2)     ; 9
      (IADD)        ;10
      (ISTORE 2)    ;11  a = y+a;
      (GOTO -10)    ;12  go to loop
      (ILOAD 2)     ;13  [end:]
      (HALT)        ;14  'return' a
  ))
```


Entry-Level Code Correctness

```
(   $s_i$  = (make-state 0
                                (list x y)
                                nil
                                 $\pi$ )
```

```
   $\wedge$  (natp x)  $\wedge$  (natp y)
```

```
   $\wedge$   $s_f$  = (m1  $s_i$  (clk x)))
```

→

```
( (haltedp  $s_f$ )
```

```
   $\wedge$ 
```

```
  (top (stack  $s_f$ )) = (g x y 0))
```

Loop Correctness: Proof by induction on x .

$((\text{natp } x) \wedge (\text{natp } y) \wedge (\text{natp } a))$

\rightarrow

$(\text{m1 } (\text{make-state } 2 \text{ (list } x \text{ } y \text{ } a) \text{ nil } *pi*))$
 $(\text{loop-clk } x))$

$=$

$(\text{make-state}$
 $14 \quad \quad \quad ; \text{ final HALTed pc}$
 $(\text{list } 0 \text{ } y \text{ (g } x \text{ } y \text{ } a)) ; \text{ final locals}$
 $(\text{push (g } x \text{ } y \text{ } a) \text{ nil}) ; \text{ final stack}$
 $*pi*) \quad \quad \quad ; \text{ final program}$

Base: (zp x)

((natp x) \wedge (natp y) \wedge (natp a))

→

(m1 (make-state 2 (list x y a) nil *pi*)
 (loop-clk x))

=

(make-state
 14
 (list 0 y (g x y a))
 (push (g x y a) nil)
 pi)

Base: (zp x)

((natp x) \wedge (natp y) \wedge (natp a))

→

(m1 (make-state 2 (list x y a) nil *pi*)
 (loop-clk x))

=

(make-state
 14
 (list 0 y (g x y a))
 (push (g x y a) nil)
 pi)

Base: (zp x)

((natp x) \wedge (natp y) \wedge (natp a))

→

(m1 (make-state 2 (list x y a) nil *pi*)
3)

=

(make-state
14
(list 0 y (g x y a))
(push (g x y a) nil)
pi)

Base: (zp x)

((natp x) \wedge (natp y) \wedge (natp a))

→

(m1 (make-state 2 (list x y a) nil *pi*)
3)

= {*by symb eval*}

(make-state

14

(list 0 y (g x y a))

(push (g x y a) nil)

pi) ✓

Step: $\neg(\text{zp } x)$

[Ind Hyp]:

$((\text{natp } (- x 1)) \wedge (\text{natp } \alpha) \wedge (\text{natp } \beta))$

\rightarrow

$(\text{m1 } (\text{make-state } 2 (\text{list } (- x 1) \alpha \beta) \text{ nil } *pi*)$
 $\quad (\text{loop-clk } (- x 1)))$

$=$

$(\text{make-state}$
 14
 $(\text{list } 0 \alpha (\text{g } (- x 1) \alpha \beta))$
 $(\text{push } (\text{g } (- x 1) \alpha \beta) \text{ nil})$
 $*pi*)$

Step: $\neg(\text{zp } x)$

[Ind Concl]:

$((\text{natp } x) \wedge (\text{natp } y) \wedge (\text{natp } a))$

\rightarrow

$(\text{m1 } (\text{make-state } 2 \text{ (list } x \text{ y } a) \text{ nil } *pi*)$
 $\quad (\text{loop-clk } x))$

$=$

$(\text{make-state}$
 $\quad 14$
 $\quad (\text{list } 0 \text{ y } (\text{g } x \text{ y } a))$
 $\quad (\text{push } (\text{g } x \text{ y } a) \text{ nil})$
 $\quad *pi*)$

Step: $\neg(\text{zp } x)$

[Ind Concl]:

$((\text{natp } x) \wedge (\text{natp } y) \wedge (\text{natp } a))$

\rightarrow

(m1 (make-state 2 (list x y a) nil *pi*)
 (loop-clk x))

=

(make-state
 14
 (list 0 y (g x y a))
 (push (g x y a) nil)
 pi)

Step: $\neg(\text{zp } x)$

[Ind Concl]:

$((\text{natp } (- x 1)) \wedge (\text{natp } y) \wedge (\text{natp } a))$

\rightarrow

$(\text{m1 } (\text{make-state } 2 (\text{list } x y a) \text{ nil } *pi*)$
 $\quad (\text{clk+ } 11 (\text{loop-clk } (- x 1))))$

$=$

$(\text{make-state}$
 $\quad 14$
 $\quad (\text{list } 0 y (\text{g } x y a))$
 $\quad (\text{push } (\text{g } x y a) \text{ nil})$
 $\quad *pi*)$

Step: $\neg(\text{zp } x)$

[Ind Concl]:

$((\text{natp } (- x 1)) \wedge (\text{natp } y) \wedge (\text{natp } a))$

\rightarrow

$(\text{m1 } (\text{make-state } 2 (\text{list } x y a) \text{ nil } *pi*)$
 $\quad (\text{clk+ } 11 (\text{loop-clk } (- x 1)))) \{seq\ comp\}$

=

$(\text{make-state}$
 14
 $(\text{list } 0 y (\text{g } x y a))$
 $(\text{push } (\text{g } x y a) \text{ nil})$
 $*pi*)$

Step: $\neg(\text{zp } x)$

[Ind Concl]:

$((\text{natp } (- x 1)) \wedge (\text{natp } y) \wedge (\text{natp } a))$

\rightarrow

$(\text{m1 } (\text{m1 } (\text{make-state } 2 (\text{list } x y a) \text{ nil } *pi*)$
11)
 $(\text{loop-clk } (- x 1)))$

=

$(\text{make-state}$
14
 $(\text{list } 0 y (\text{g } x y a))$
 $(\text{push } (\text{g } x y a) \text{ nil})$
 $*pi*)$

Step: $\neg(\text{zp } x)$

[Ind Concl]:

$((\text{natp } (- x 1)) \wedge (\text{natp } y) \wedge (\text{natp } a))$

\rightarrow

$(\text{m1 } (\text{m1 } (\text{make-state } 2 (\text{list } x y a) \text{ nil } *pi*)$
 $11) \{symb\ eval\}$
 $(\text{loop-clk } (- x 1))))$

$=$

$(\text{make-state}$
 14
 $(\text{list } 0 y (\text{g } x y a))$
 $(\text{push } (\text{g } x y a) \text{ nil})$
 $*pi*)$

Step: $\neg(\text{zp } x)$

[Ind Concl]:

$((\text{natp } (- x 1)) \wedge (\text{natp } y) \wedge (\text{natp } a))$

\rightarrow

$(\text{m1 } (\text{make-state } 2 (\text{list } (- x 1) y (+ y a)) \text{ nil } *pi*)$
 $\quad (\text{loop-clk } (- x 1)))$

$=$

$(\text{make-state}$
 14
 $(\text{list } 0 y (\text{g } x y a))$
 $(\text{push } (\text{g } x y a) \text{ nil})$
 $*pi*)$

Step: $\neg(\text{zp } x)$

[Ind Hyp]:

$((\text{natp } (- x 1)) \wedge (\text{natp } \alpha) \wedge (\text{natp } \beta))$

\rightarrow

$(\text{m1 } (\text{make-state } 2 (\text{list } (- x 1) \alpha \beta) \text{ nil } *pi*)$
 $\quad (\text{loop-clk } (- x 1)))$

$=$

$(\text{make-state}$
 14
 $(\text{list } 0 \alpha (\text{g } (- x 1) \alpha \beta))$
 $(\text{push } (\text{g } (- x 1) \alpha \beta) \text{ nil})$
 $*pi*)$

Step: $\neg(\text{zp } x)$

[Ind Concl]:

$((\text{natp } (- x 1)) \wedge (\text{natp } y) \wedge (\text{natp } a))$

\rightarrow

$(\text{m1 } (\text{make-state } 2 (\text{list } (- x 1) y (+ y a)) \text{ nil } *pi*)$
 $\quad (\text{loop-clk } (- x 1)))$

$=$

$(\text{make-state}$
 14
 $(\text{list } 0 y (\text{g } x y a))$
 $(\text{push } (\text{g } x y a) \text{ nil})$
 $*pi*)$

Step: $\neg(\text{zp } x)$

[Ind Hyp]:

$((\text{natp } (- x 1)) \wedge (\text{natp } \alpha) \wedge (\text{natp } \beta))$

\rightarrow

$(\text{m1 } (\text{make-state } 2 (\text{list } (- x 1) \alpha \beta) \text{ nil } *pi*)$
 $\quad (\text{loop-clk } (- x 1)))$

$=$

$(\text{make-state}$
 14
 $(\text{list } 0 \alpha (\text{g } (- x 1) \alpha \beta))$
 $(\text{push } (\text{g } (- x 1) \alpha \beta) \text{ nil})$
 $*pi*)$

Step: $\neg(\text{zp } x)$

[Ind Hyp]:

$((\text{natp } (- x 1)) \wedge (\text{natp } y) \wedge (\text{natp } (+ y a)))$

\rightarrow

$(\text{m1 } (\text{make-state } 2 (\text{list } (- x 1) y (+ y a)) \text{ nil } *pi*)$
 $\quad (\text{loop-clk } (- x 1)))$

$=$

$(\text{make-state}$
 14
 $(\text{list } 0 y (\text{g } (- x 1) y (+ y a)))$
 $(\text{push } (\text{g } (- x 1) y (+ y a)) \text{ nil})$
 $*pi*)$

Induct as suggested by $(\text{g } x y a)$!

Step: $\neg(\text{zp } x)$

[Ind Hyp]:

$((\text{natp } (- x 1)) \wedge (\text{natp } y) \wedge (\text{natp } (+ y a)))$

\rightarrow

$(\text{m1 } (\text{make-state } 2 (\text{list } (- x 1) y (+ y a)) \text{ nil } *pi*)$
 $\quad (\text{loop-clk } (- x 1)))$

$=$

$(\text{make-state}$
 14
 $(\text{list } 0 y (\text{g } (- x 1) y (+ y a)))$
 $(\text{push } (\text{g } (- x 1) y (+ y a)) \text{ nil})$
 $*pi*)$

Step: $\neg(\text{zp } x)$

[Ind Concl]:

$((\text{natp } (- x 1)) \wedge (\text{natp } y) \wedge (\text{natp } a))$

\rightarrow

$(\text{m1 } (\text{make-state } 2 (\text{list } (- x 1) y (+ y a)) \text{ nil } *pi*)$
 $\quad (\text{loop-clk } (- x 1)))$

$=$

$(\text{make-state}$
 14
 $(\text{list } 0 y (\text{g } x y a))$
 $(\text{push } (\text{g } x y a) \text{ nil})$
 $*pi*)$

Step: $\neg(\text{zp } x)$

[Ind Concl]:

$((\text{natp } (- x 1)) \wedge (\text{natp } y) \wedge (\text{natp } a))$

\rightarrow

```
(make-state 14
  (list 0 y (g (- x 1) y (+ y a)))
  (push (g (- x 1) y (+ y a)) nil)
  *pi*) {ind hyp}
```

=

```
(make-state 14
  (list 0 y (g x y a))
  (push (g x y a) nil)
  *pi*)
```

Step: $\neg(\text{zp } x)$

[Ind Concl]:

$((\text{natp } (- x 1)) \wedge (\text{natp } y) \wedge (\text{natp } a))$

\rightarrow

```
(make-state 14
  (list 0 y (g (- x 1) y (+ y a)))
  (push (g (- x 1) y (+ y a)) nil)
  *pi*)
```

=

```
(make-state 14
  (list 0 y (g x y a))
  (push (g x y a) nil)
  *pi*)
```

Step: $\neg(\text{zp } x)$

[Ind Concl]:

$((\text{natp } (- x 1)) \wedge (\text{natp } y) \wedge (\text{natp } a))$

\rightarrow

```
(make-state 14
  (list 0 y (g (- x 1) y (+ y a)))
  (push (g (- x 1) y (+ y a)) nil)
  *pi*)
```

=

```
(make-state 14
  (list 0 y (g (- x 1) y (+ y a)))
  (push (g (- x 1) y (+ y a)) nil)
  *pi*)
```

Step: $\neg(\text{zp } x)$

[Ind Concl]:

$((\text{natp } (- x 1)) \wedge (\text{natp } y) \wedge (\text{natp } a))$

\rightarrow

```
(make-state 14
  (list 0 y (g (- x 1) y (+ y a)))
  (push (g (- x 1) y (+ y a)) nil)
  *pi*)
```

=

```
(make-state 14
  (list 0 y (g (- x 1) y (+ y a)))
  (push (g (- x 1) y (+ y a)) nil)
  *pi*) ✓
```


Loop Correctness – :REWRITE Rule

$((\text{natp } x) \wedge (\text{natp } y) \wedge (\text{natp } a))$

→

`(m1 (make-state 2 (list x y a) nil *pi*)
 (loop-clk x))`

=

```
(make-state  
  14 ; final HALTed pc  
  (list 0 y (g x y a)) ; final locals  
  (push (g x y a) nil) ; final stack  
  *pi*) ; final program
```

When done with a code block, disable its clock!

Entry Correctness

$((\text{natp } x) \wedge (\text{natp } y))$

\rightarrow

$(\text{m1 } (\text{make-state } 0 \text{ (list } x \text{ } y) \text{ nil } *pi*))$
 $(\text{clk } x))$

$=$

$(\text{make-state}$
 14
 $(\text{list } 0 \text{ } y \text{ (g } x \text{ } y \text{ } 0))$
 $(\text{push } (\text{g } x \text{ } y \text{ } 0) \text{ nil})$
 $*pi*)$

Entry Correctness

`((natp x) ^ (natp y))`

\rightarrow

`(m1 (make-state 0 (list x y) nil *pi*)
 (clk x))`

$=$

`(make-state
 14
 (list 0 y (g x y 0))
 (push (g x y 0) nil)
 pi)`

Entry Correctness

`((natp x) ^ (natp y))`

\rightarrow

`(m1 (make-state 0 (list x y) nil *pi*)
 (clk+ 2 (loop-clk x)))`

`=`

`(make-state
 14
 (list 0 y (g x y 0))
 (push (g x y 0) nil)
 pi)`

Entry Correctness

`((natp x) ^ (natp y))`

→

`(m1 (make-state 0 (list x y) nil *pi*)
 (clk+ 2 (loop-clk x)))`

=

`(make-state
 14
 (list 0 y (g x y 0))
 (push (g x y 0) nil)
 pi)`

Entry Correctness

`((natp x) ^ (natp y))`

\rightarrow

`(m1 (m1 (make-state 0 (list x y) nil *pi*) 2)
 (loop-clk x))`

`=`

`(make-state
 14
 (list 0 y (g x y 0))
 (push (g x y 0) nil)
 pi)`

Entry Correctness

`((natp x) ^ (natp y))`

\rightarrow

`(m1 (m1 (make-state 0 (list x y) nil *pi*) 2)
 (loop-clk x))`

`=`

`(make-state
 14
 (list 0 y (g x y 0))
 (push (g x y 0) nil)
 pi)`

Entry Correctness

`((natp x) ^ (natp y))`

\rightarrow

`(m1 (make-state 2 (list x y 0) nil *pi*)
 (loop-clk x))`

`=`

`(make-state
 14
 (list 0 y (g x y 0))
 (push (g x y 0) nil)
 pi)`

Entry Correctness

`((natp x) ^ (natp y))`

\rightarrow

`(m1 (make-state 2 (list x y 0) nil *pi*)
 (loop-clk x)) {Loop Correctness}`

`=`

`(make-state
 14
 (list 0 y (g x y 0))
 (push (g x y 0) nil)
 pi)`

Entry Correctness

`((natp x) ^ (natp y))`

\rightarrow

`(make-state 14 (list 0 y (g x y 0))
 (push (g x y 0) nil)
 pi)`

$=$

`(make-state 14 (list 0 y (g x y 0))
 (push (g x y 0) nil)
 pi)` ✓

Entry Correctness – :REWRITE Rule

`((natp x) ^ (natp y))`

`→`

`(m1 (make-state 0 (list x y) nil *pi*)
 (clk x))`

`=`

`(make-state
 14
 (list 0 y (g x y 0))
 (push (g x y 0) nil)
 pi)`

Note: Disable clk!

Demo

Moving Parts of M1 Proof Engine

- clock functions ✓
- symbolic evaluation ✓
- sequential composition ✓
- how they mesh ✓
- behavior v intention ✓
- dealing with loops ✓
- separation of concerns

Separation of Concerns

Once we have dealt with behavior (code implements algorithm), we turn to intention (algorithm implements spec).

During the behavior proofs, you want ACL2's manipulation of the algorithm to mimic its manipulation of code.

But by proving lemmas to relate the algorithm to the spec, you may start changing how ACL2 manipulates the algorithm.

Changing ACL2's View of the Algorithm

Lemma: $(g\ x\ y\ a) = (+\ a\ (*\ x\ y))$

Thm: $(g\ x\ y\ 0) = (*\ x\ y)$

Main Goal: Code Satisfies the Spec

```
(   $s_i$  = (make-state 0  
                (list x y)  
                nil  
                *pi*)
```

```
   $\wedge$  (natp x)  $\wedge$  (natp y)
```

```
   $\wedge$   $s_f$  = (m1  $s_i$  (clk x)))
```

→

```
( (haltedp  $s_f$ )
```

```
   $\wedge$ 
```

```
    (top (stack  $s_f$ )) = (* x y)).
```


Main Goal: Code Satisfies the Spec

```
(   $s_i$  = (make-state 0
           (list x y)
           nil
           *pi*))
```

```
  ^ (natp x) ^ (natp y)
```

```
  ^  $s_f$  = (m1  $s_i$  (clk x))
```

→

```
( (haltedp  $s_f$ )
```

```
  ^
```

```
  (top (stack  $s_f$ )) = (* x y)).
```

Main Goal: Code Satisfies the Spec

```
((natp x) ∧ (natp y)
 ∧ sf = (m1 (make-state 0
              (list x y)
              nil
              *pi*)
           (clk x)))
```

→

```
( (haltedp sf)
 ∧
 (top (stack sf) = (* x y)).
```

Main Goal: Code Satisfies the Spec

```
((natp x) ∧ (natp y)
 ∧ sf = (m1 (make-state 0
              (list x y)
              nil
              *pi*)
           (clk x))
```

→

```
( (haltedp sf)
 ∧
 (top (stack sf)) = (* x y)).
```

Main Goal: Code Satisfies the Spec

```
((natp x) ∧ (natp y)
 ∧ sf = (m1 (make-state 0
              (list x y)
              nil
              *pi*)
           (clk x)) {Entry Correctness})
```

→

```
( (haltedp sf)
 ∧
 (top (stack sf)) = (* x y)).
```

Main Goal: Code Satisfies the Spec

```
((natp x) ∧ (natp y)
 ∧ sf = (make-state
          14
          (list 0 y (g x y 0))
          (push (g x y 0) nil)
          *pi*))
```

→

```
( (haltedp sf)
 ∧
 (top (stack sf)) = (* x y)).
```

Main Goal: Code Satisfies the Spec

```
((natp x) ∧ (natp y)
 ∧ sf = (make-state
          14
          (list 0 y (g x y 0))
          (push (g x y 0) nil)
          *pi*))
```

→

```
( (haltedp sf)
 ∧
 (top (stack sf)) = (* x y)).
```

Main Goal: Code Satisfies the Spec

```
((natp x) ∧ (natp y)
 ∧ sf = (make-state
          14
          (list 0 y (* x y))
          (push (* x y) nil)
          *pi*))
```

→

```
( (haltedp sf)
 ∧
 (top (stack sf)) = (* x y)).
```

Main Goal: Code Satisfies the Spec

```
((natp x) ∧ (natp y)
 ∧ sf = (make-state
           14
           (list 0 y (* x y))
           (push (* x y) nil)
           *pi*))
```

→

```
( (haltedp sf)
 ∧
 (top (stack sf) = (* x y)).
```


Main Goal: Code Satisfies the Spec

```
((natp x) ∧ (natp y)
 ∧ sf = (make-state
           14
           (list 0 y (* x y))
           (push (* x y) nil)
           *pi*))
```

→

```
( (haltedp sf)
 ∧
 (top (stack sf) = (* x y)).
```

Q.E.D.

Demo