

Synchronization: Implementing Barriers Promises + Futures

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CS378H

Today

- Questions?
- Administrivia
 - Lab 2 due sooner than you'd like
- Material for the day
 - Barrier implementation
 - Promises & Futures
- Acknowledgements
 - Thanks to Gadi Taubenfield: I borrowed from some of his slides on barriers



Faux Quiz (answer any N, 5 min)

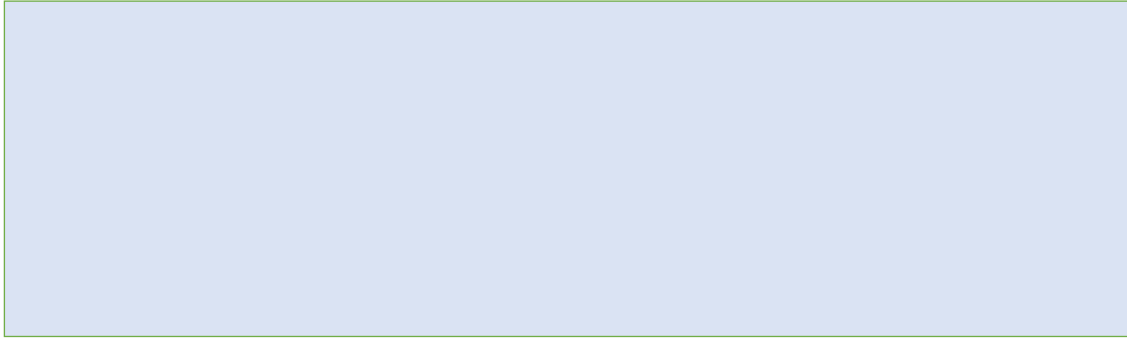
- How are promises and futures related? Since there is disagreement on the nomenclature, don't worry about which is which—just describe what the different objects are and how they function.

Barriers

Barriers



Review: Barrier Basics

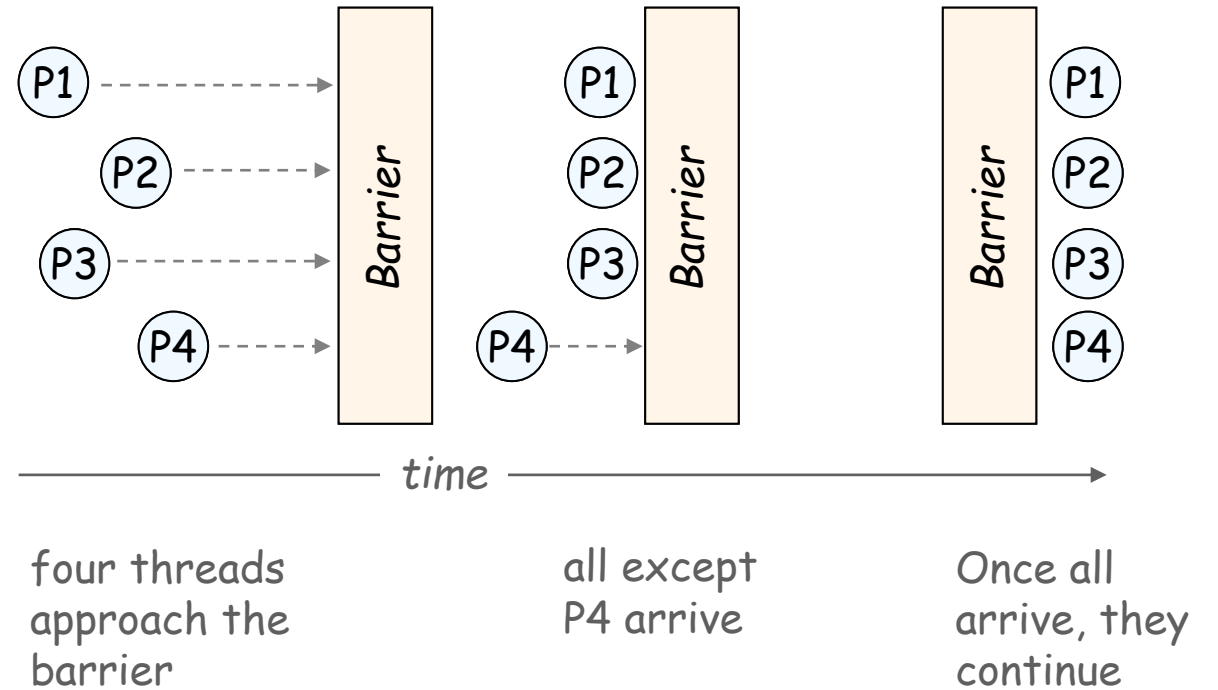


Review: Barrier Basics

- *Coordination mechanism*
- *participants wait until all reach same point.*
- *Once all reach it, all can pass.*

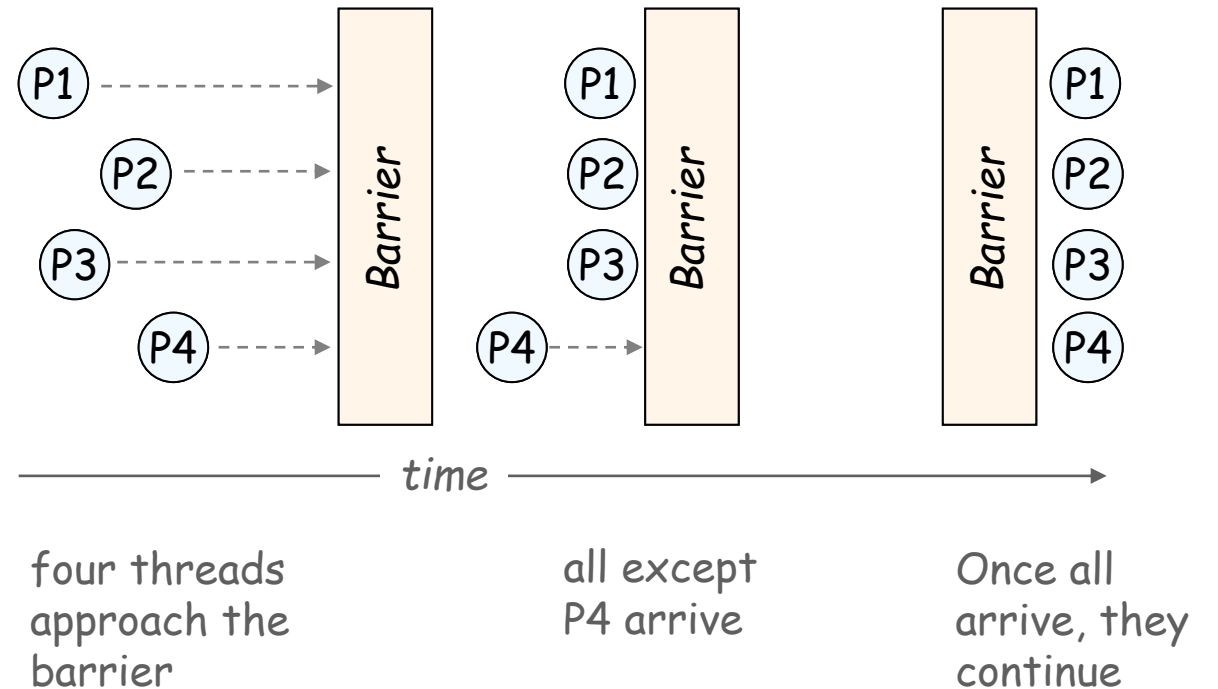
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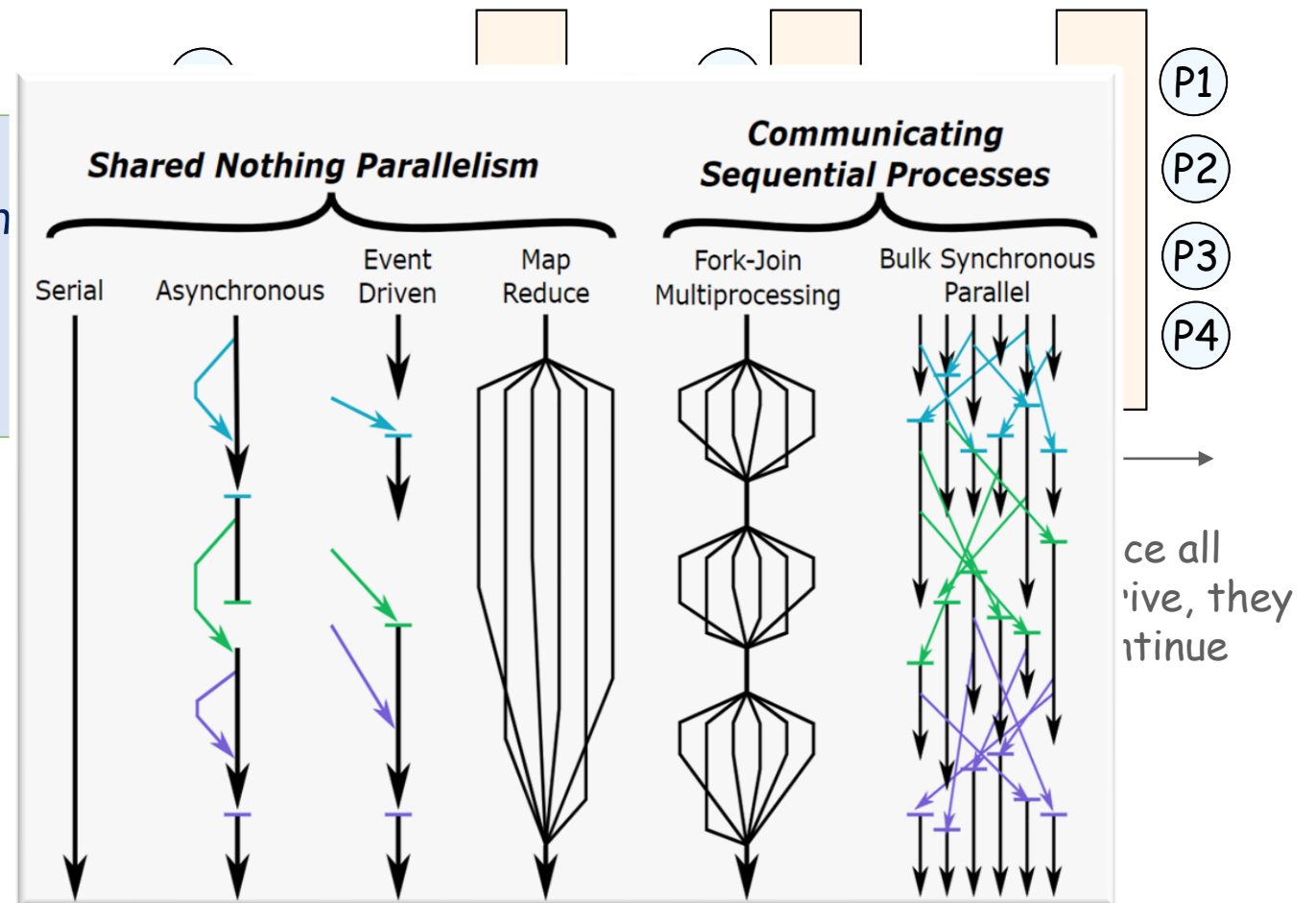
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- Coordination mechanism
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- Once all reach it, all can pass.
- **Workhorse of BSP programming models**



Review: Barrier Basics

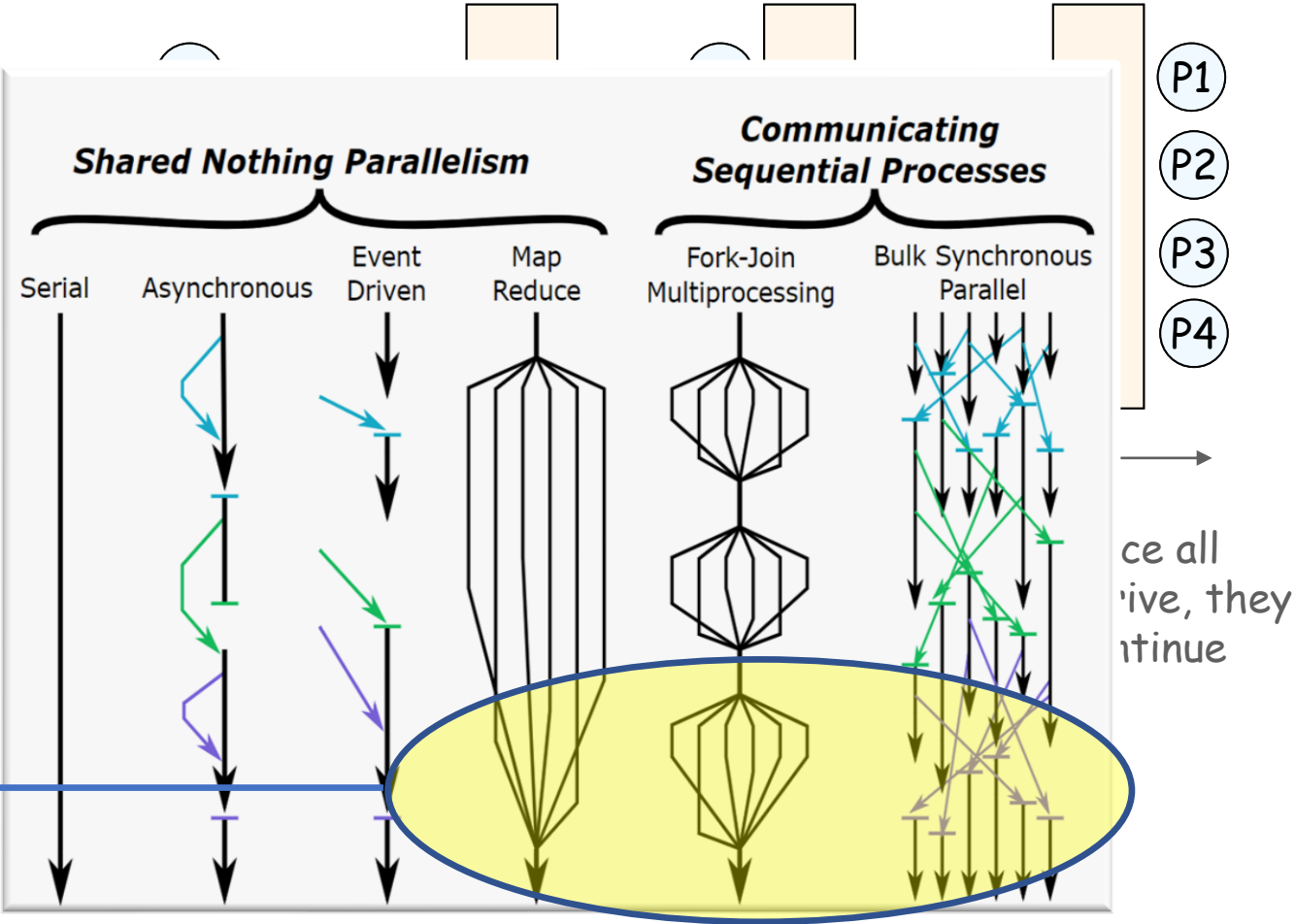
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- **Workhorse of BSP programming models**

Fundamental primitive in many parallel models



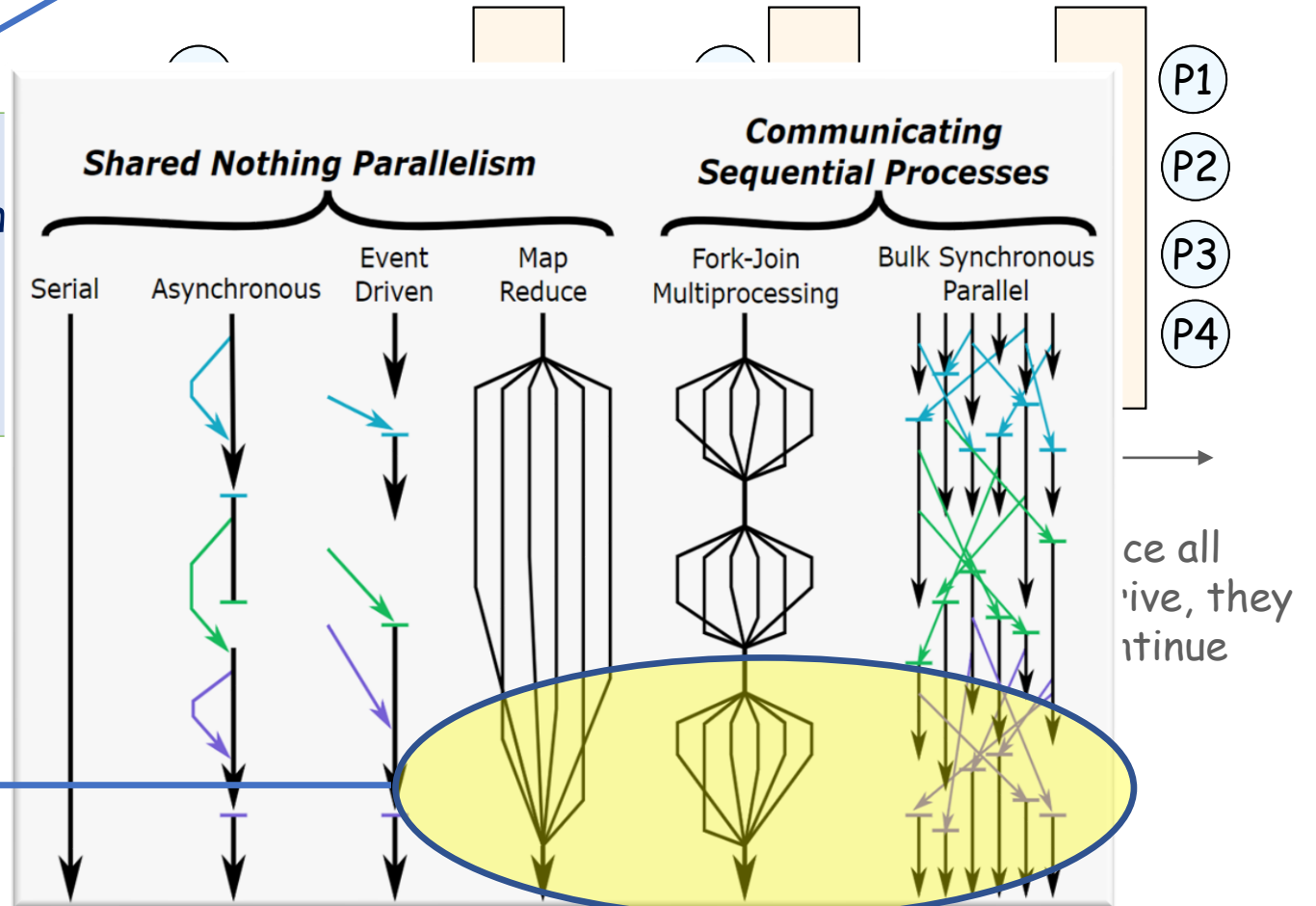
Review: Barrier Basics

Can you make a lock with a barrier?

Coordination mechanism

- participants wait until all reach same point
- Once all reach it, all can pass.
- Workhorse of BSP programming models

Fundamental primitive in many parallel models





Barriers based on counters



Counter Barrier Ingredients

Fetch-and-Increment register

- A shared register that supports a F&I operation:
- Input: register r
- Atomic operation:
 - r is incremented by 1
 - the old value of r is returned

```
function fetch-and-increment (r : register)
  orig_r := r;
  r := r + 1;
  return (orig_r);
end-function
```

Await

- For brevity, we use the **await** macro
- Not an operation of an object
- This is just “spinning”

```
macro await (condition : boolean condition)
  repeat
    cond = eval(condition);
  until (cond)
end-macro
```

Simple Barrier Using an Atomic Counter

shared	counter: fetch and increment reg. – $\{0,..n\}$, initially = 0 go: atomic bit, initial value does not matter
local	local.go: a bit, initial value does not matter local.counter: register

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Pros/Cons?

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Pros/Cons?

- There is high memory contention on *go* bit
- Reducing the contention:
 - Replace the *go* bit with *n* bits:
go[1],...,*go*[*n*]
 - Process p_i may spin only on the bit *go*[*i*]

A Local Spinning Counter Barrier

Program of a Thread i

shared	counter: fetch and increment reg. – $\{0,..n\}$, initially = 0
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A Local Spinning Counter Barrier

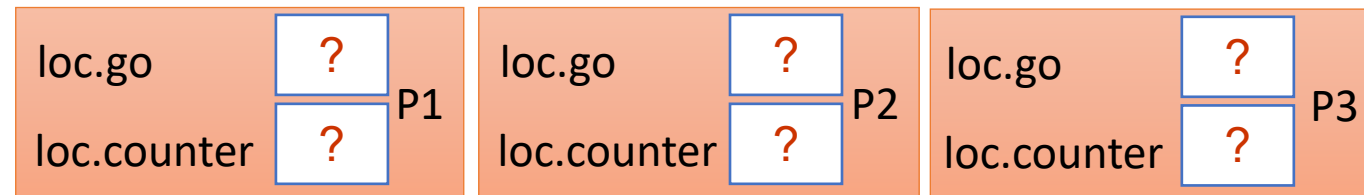
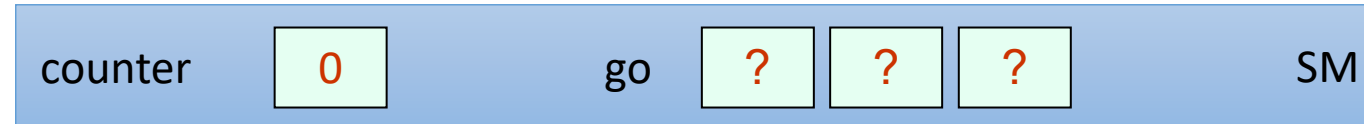
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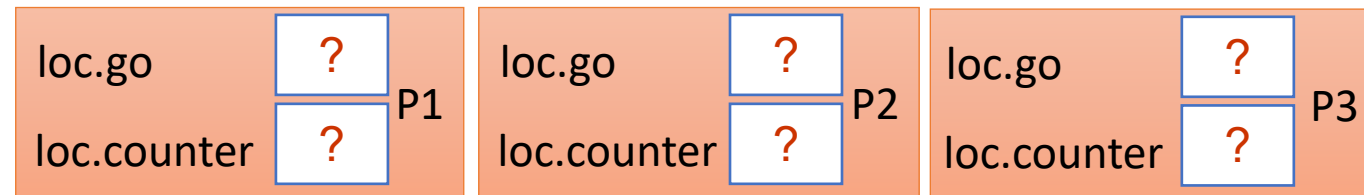
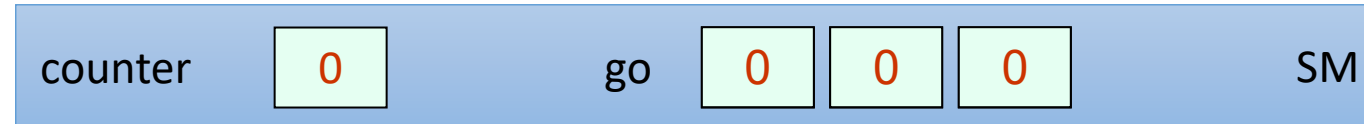
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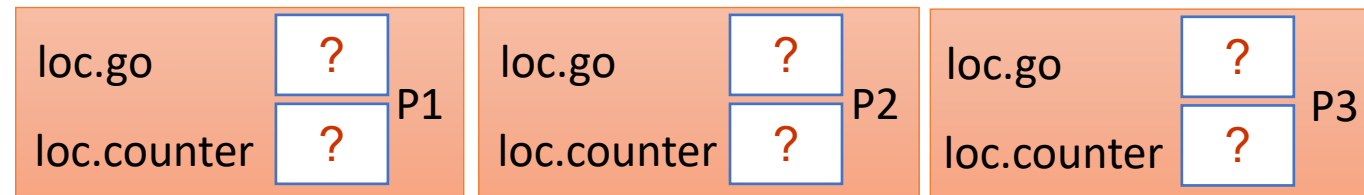
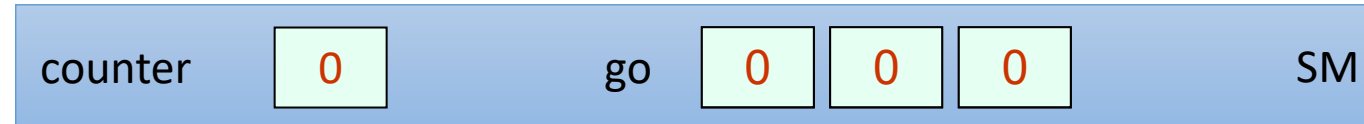
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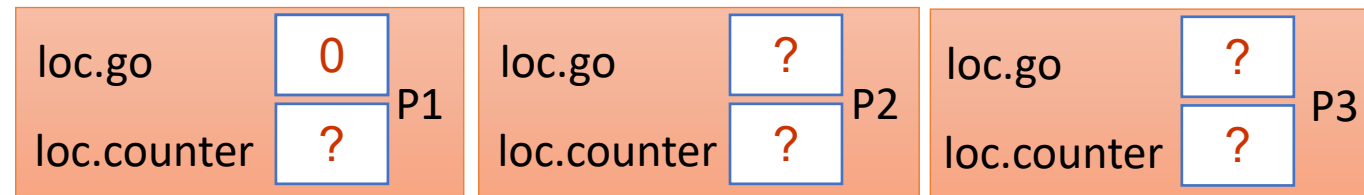
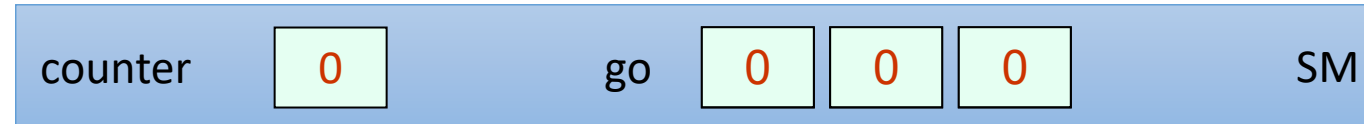
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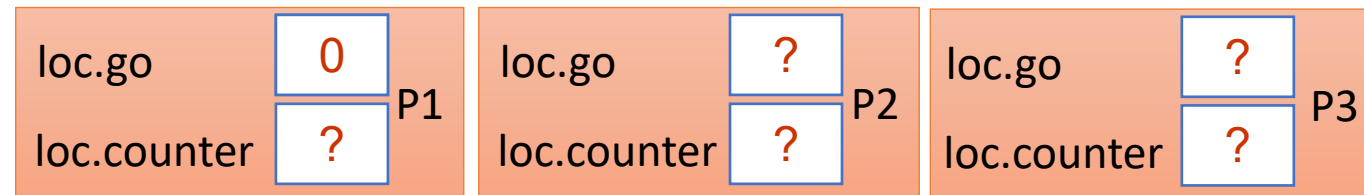
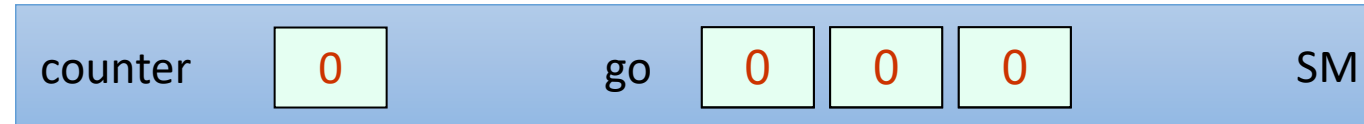
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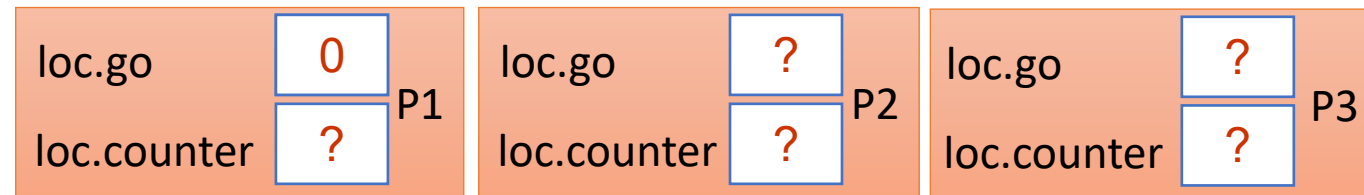
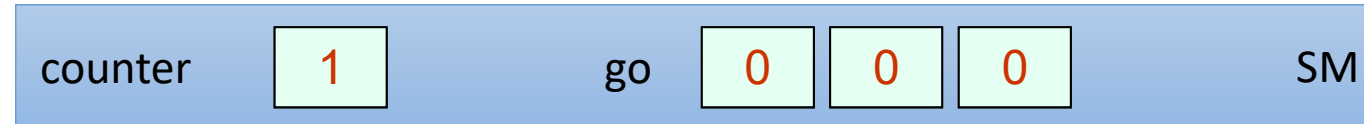


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A Local Spinning Counter Barrier

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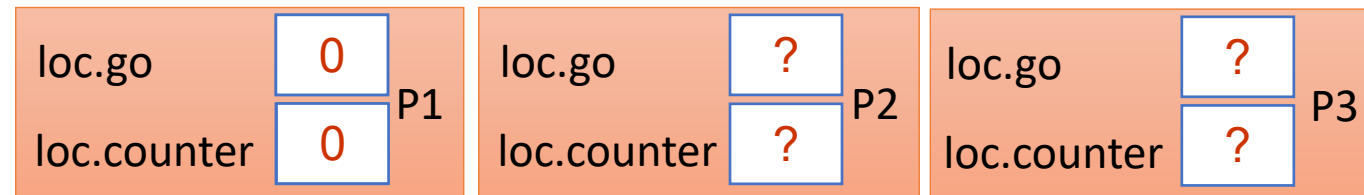
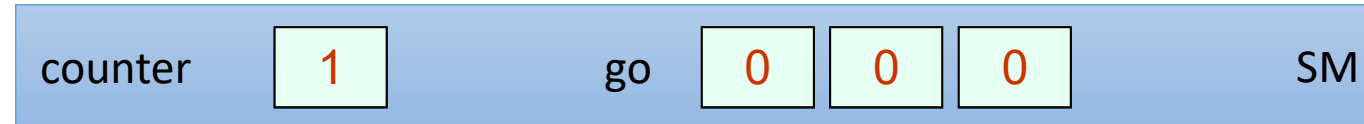


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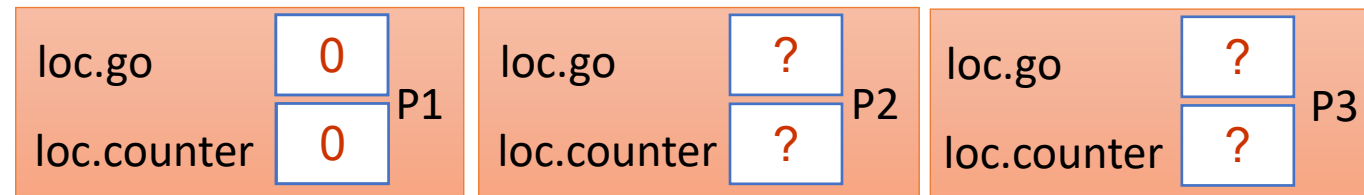
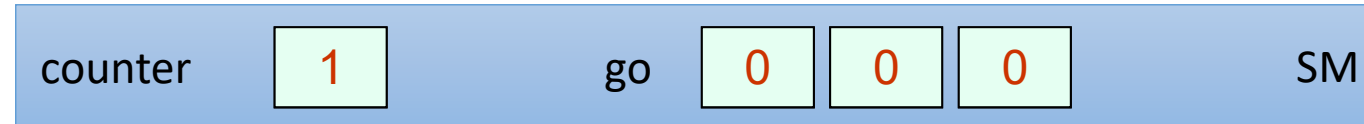


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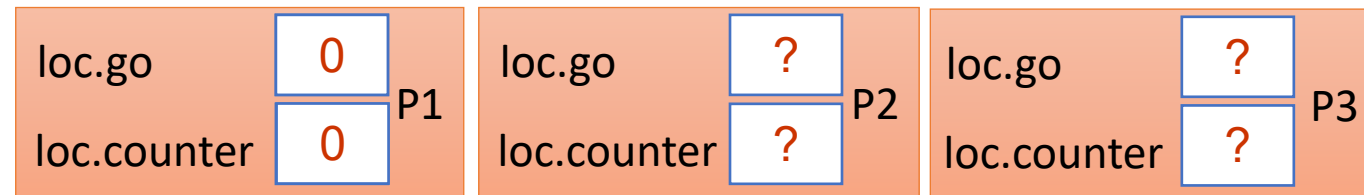
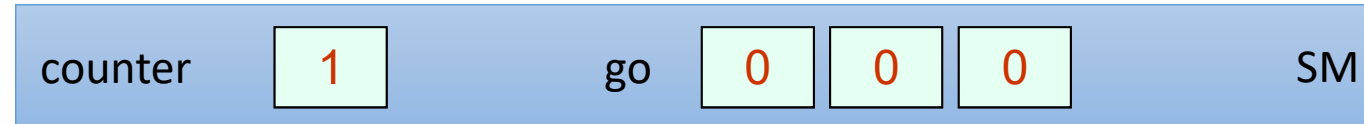
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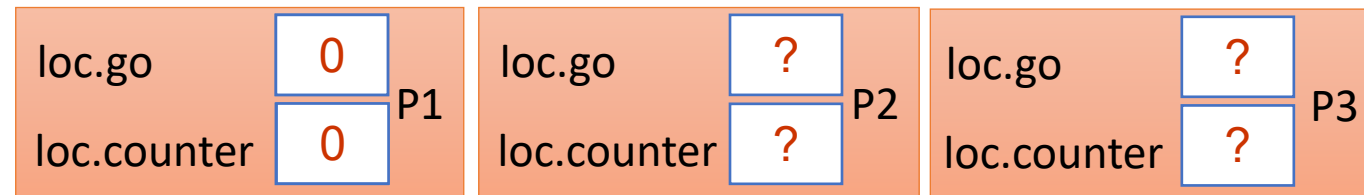
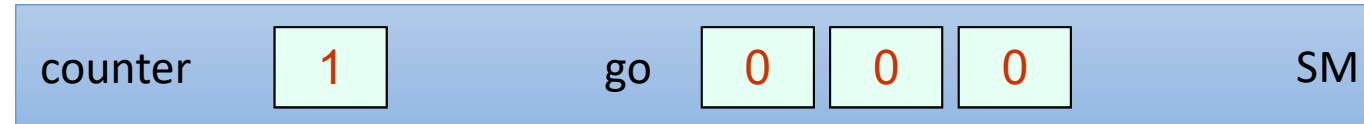
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0+1≠3

A Local Spinning Counter Barrier

Example Run for n=3 Threads

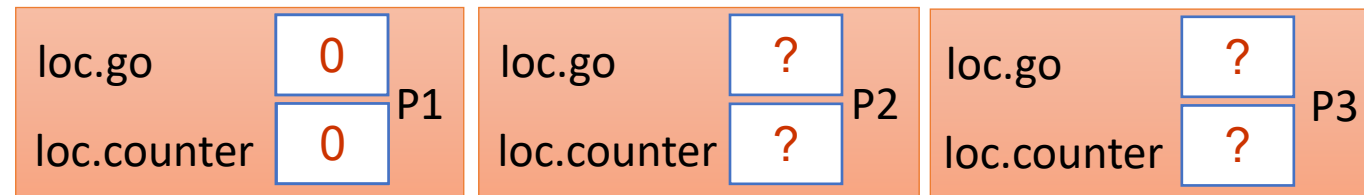
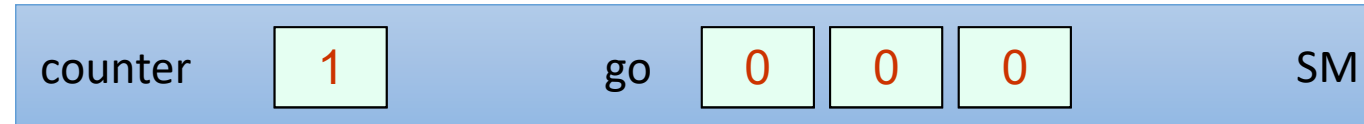


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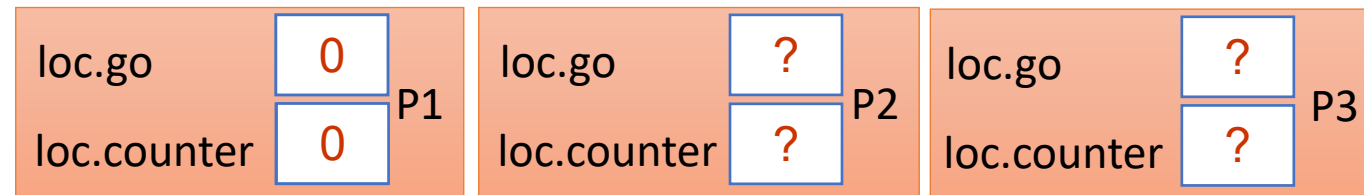
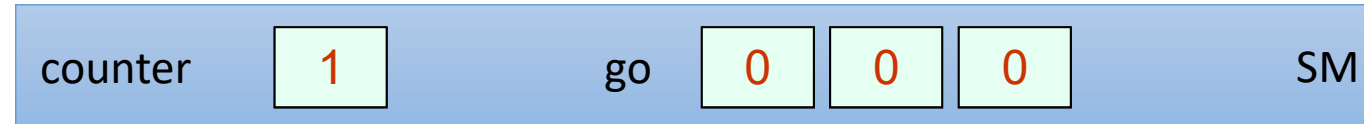
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P1 →

P1 Busy wait

A Local Spinning Counter Barrier

Example Run for n=3 Threads

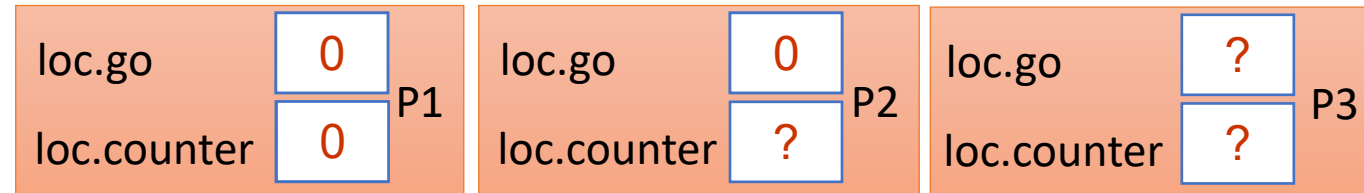
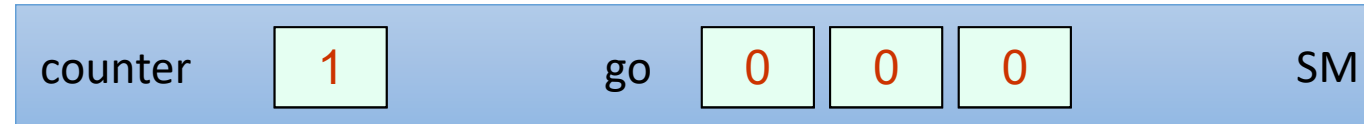


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P1 Busy wait

A Local Spinning Counter Barrier

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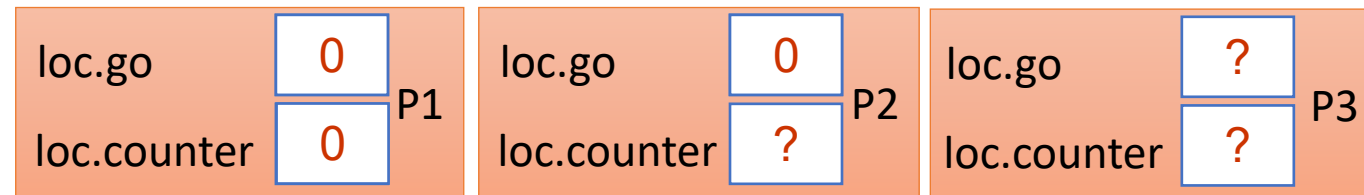
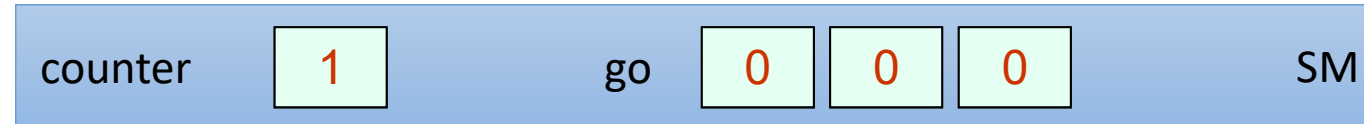


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A Local Spinning Counter Barrier

Example Run for n=3 Threads



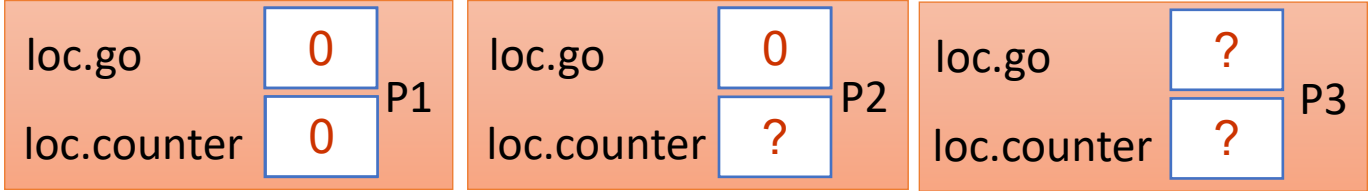
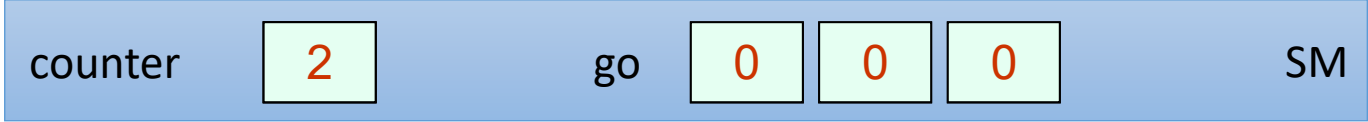
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P2 → (points to line 2)
P1 → (points to line 6)

P1 Busy wait (points to the await statement)

A Local Spinning Counter Barrier

Example Run for n=3 Threads



```

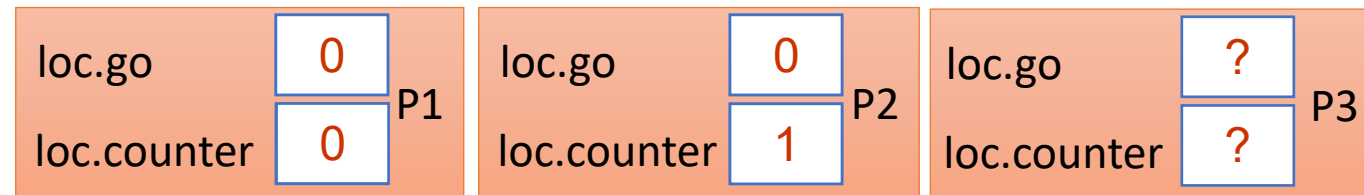
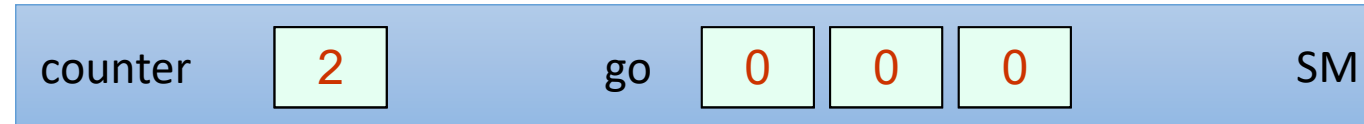
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Annotations:

- P2 → points to line 2
- P1 → points to line 6
- A box labeled "P1 Busy wait" has an arrow pointing to the `await` statement in line 6.

A Local Spinning Counter Barrier

Example Run for n=3 Threads



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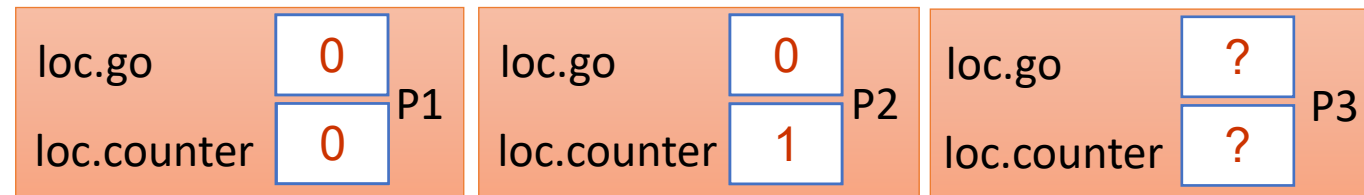
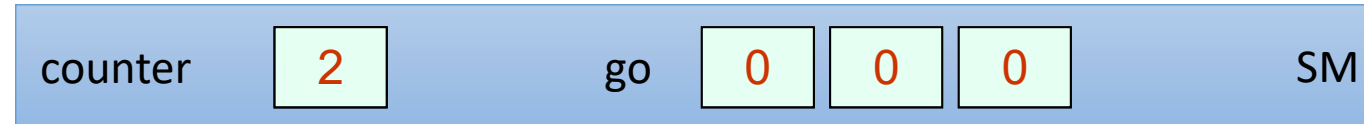
P2 →

P1 →

P1 Busy wait

A Local Spinning Counter Barrier

Example Run for n=3 Threads



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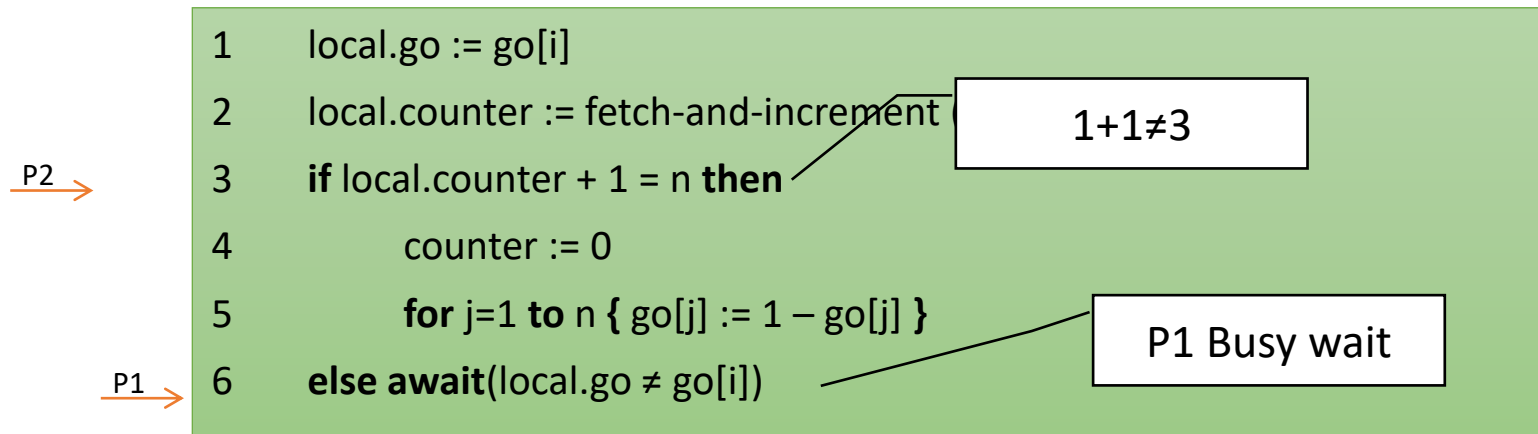
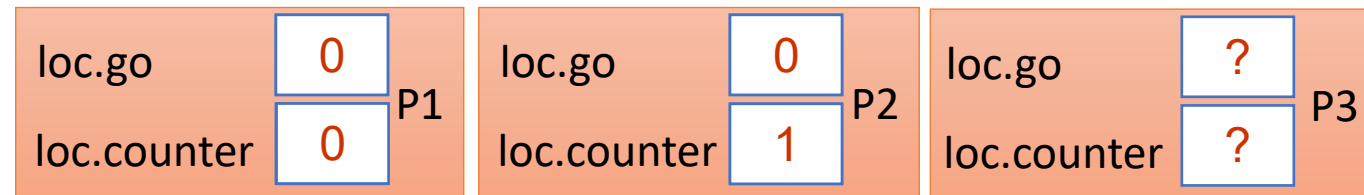
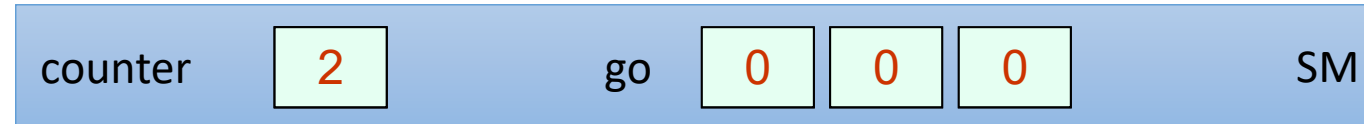
P2 →

P1 →

P1 Busy wait

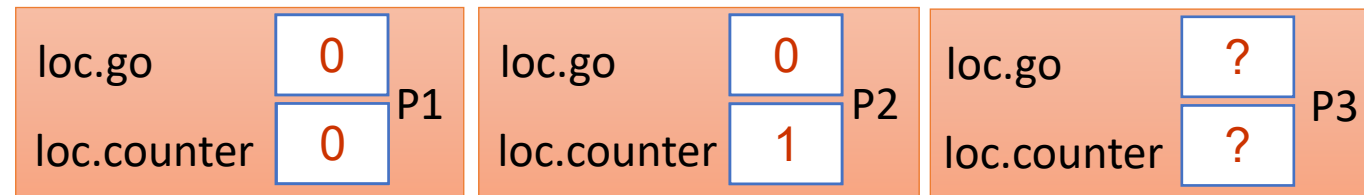
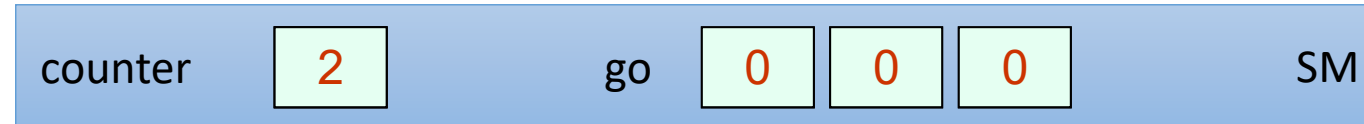
A Local Spinning Counter Barrier

Example Run for n=3 Threads



A Local Spinning Counter Barrier

Example Run for n=3 Threads



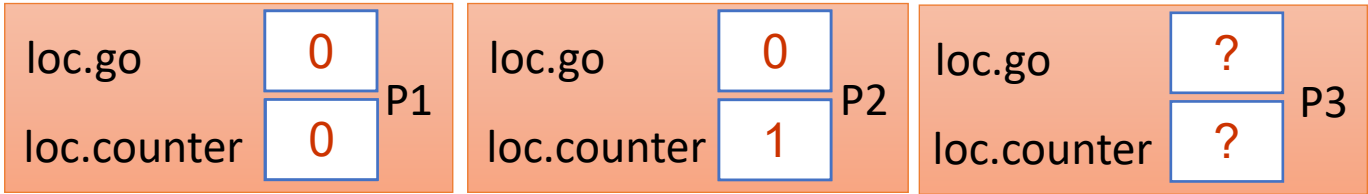
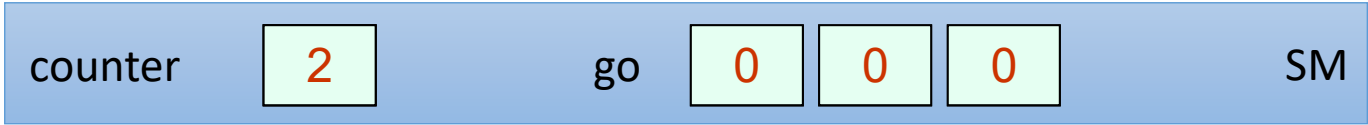
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P1,P2 Busy wait



A Local Spinning Counter Barrier

Example Run for n=3 Threads



P3 →

```

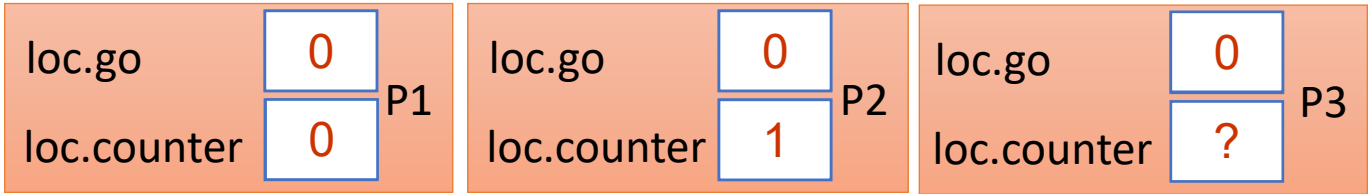
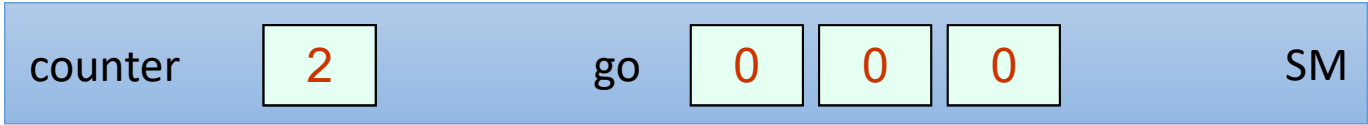
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6  else await(local.go ≠ go[i])
    
```

P1,P2 Busy wait

P2 → P1 →

A Local Spinning Counter Barrier

Example Run for n=3 Threads



P3 →

```

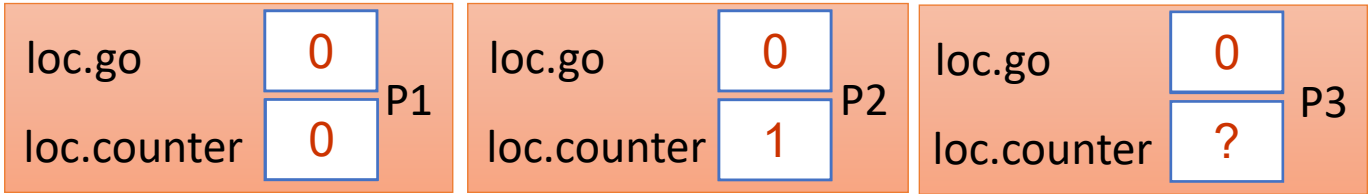
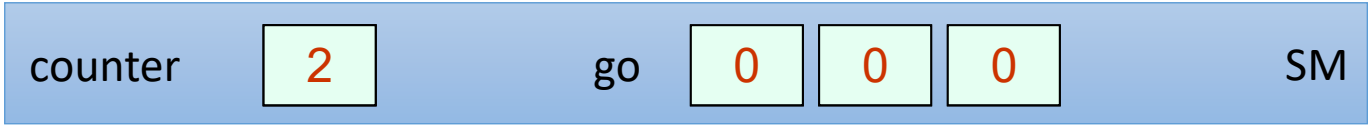
1 local.go := go[i]
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3 if local.counter + 1 = n then
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5     for j=1 to n { go[j] := 1 - go[j] }
6 else await(local.go ≠ go[i])
    
```

P1,P2 Busy wait

P2 → P1 →

A Local Spinning Counter Barrier

Example Run for n=3 Threads



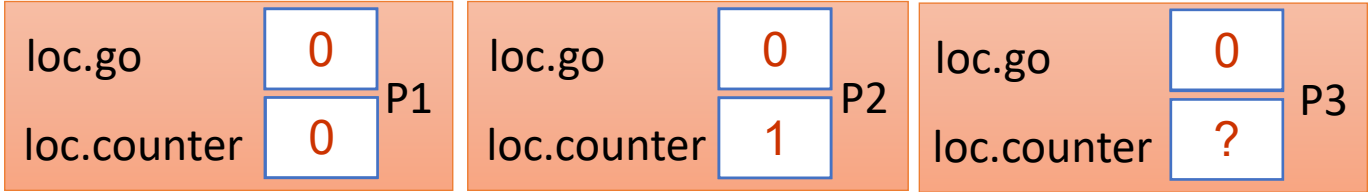
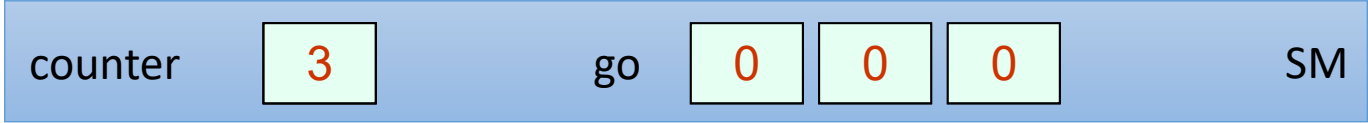
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6 else await(local.go ≠ go[i])
```

Annotations:

- P3 → (points to line 2)
- P2 → P1 → (points to line 6)
- P1, P2 Busy wait (points to line 6)

A Local Spinning Counter Barrier

Example Run for n=3 Threads



```

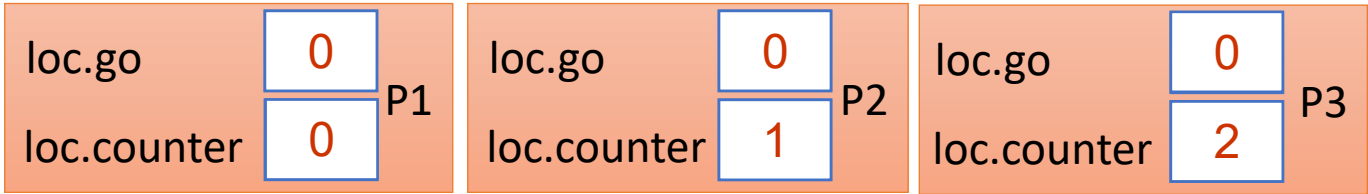
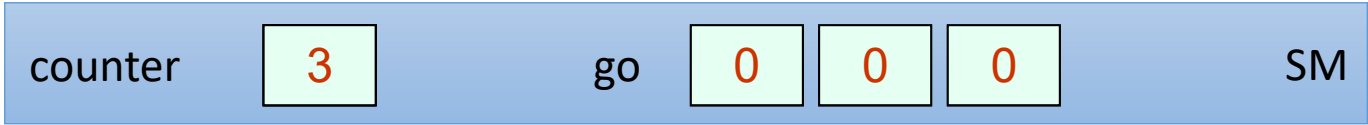
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```

Annotations:

- P3 → (points to line 2)
- P2 → P1 → (points to line 6)
- P1, P2 Busy wait (points to the await statement in line 6)

A Local Spinning Counter Barrier

Example Run for n=3 Threads



```

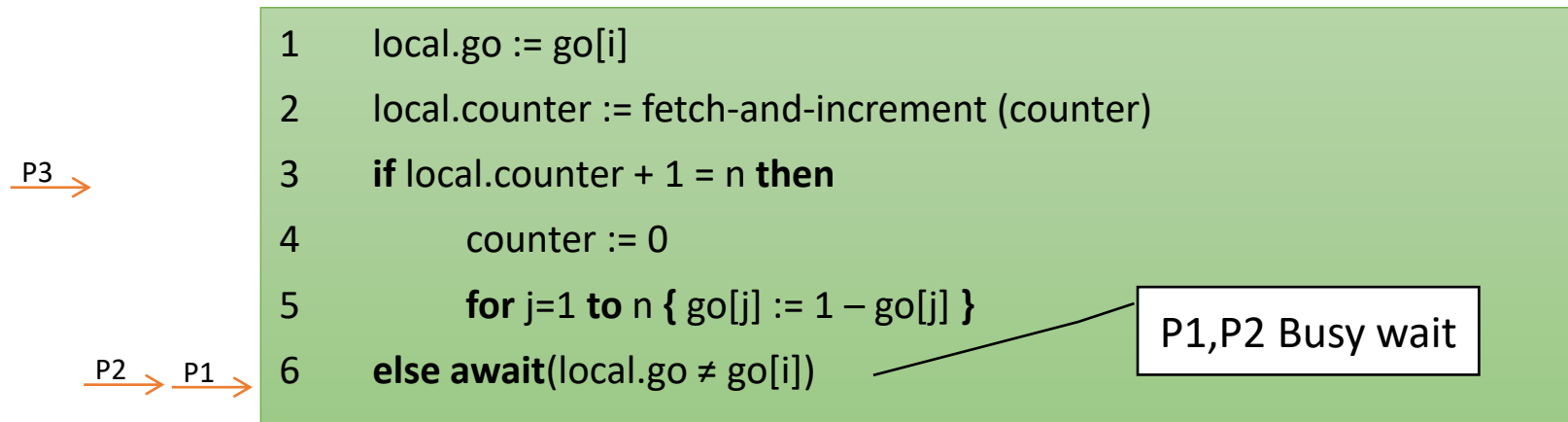
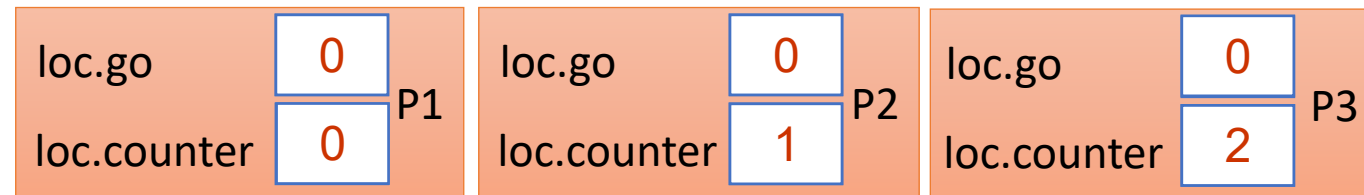
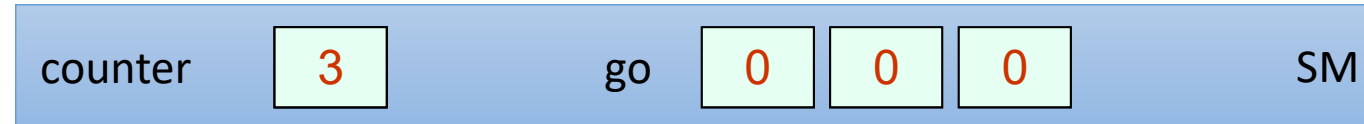
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Annotations:

- P3 → (points to line 2)
- P2 → P1 → (points to line 6)
- P1, P2 Busy wait (points to line 6)

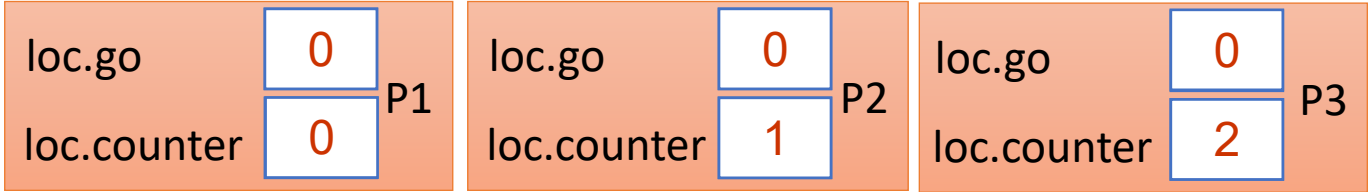
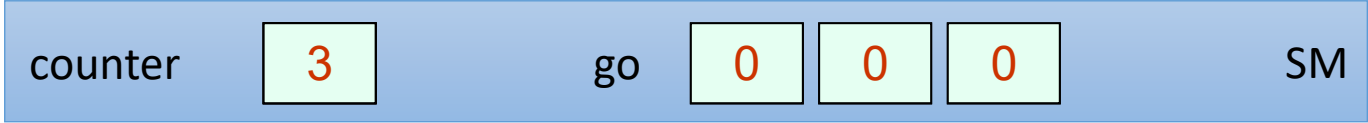
A Local Spinning Counter Barrier

Example Run for n=3 Threads



A Local Spinning Counter Barrier

Example Run for n=3 Threads



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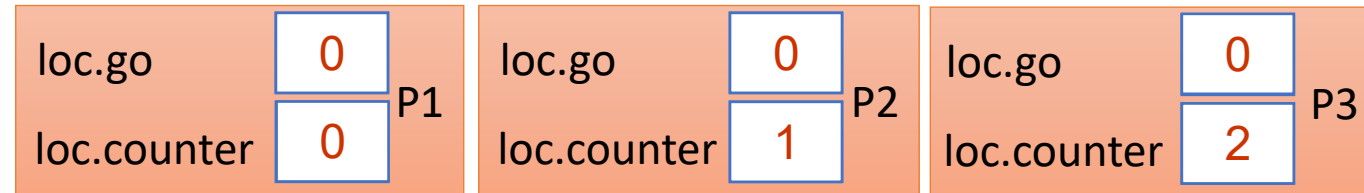
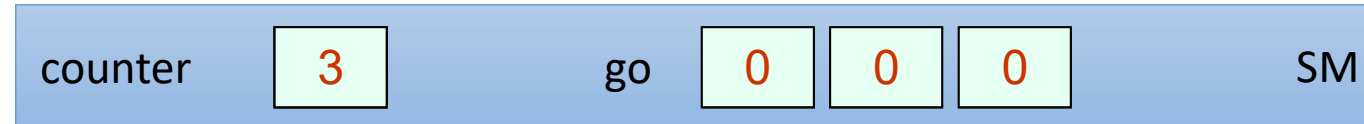
Annotations:

- Line 2: $2+1=3$
- Line 6: P1, P2 Busy wait

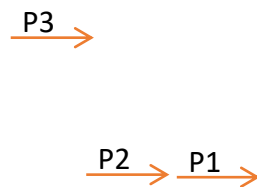
Execution flow: P3 → P2 → P1

A Local Spinning Counter Barrier

Example Run for n=3 Threads



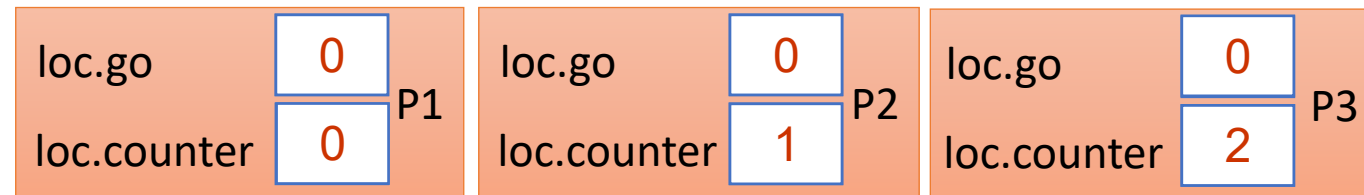
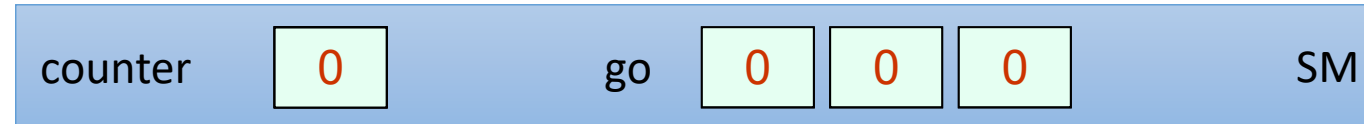
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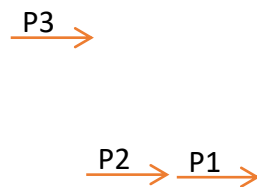
P1,P2 Busy wait

A Local Spinning Counter Barrier

Example Run for n=3 Threads



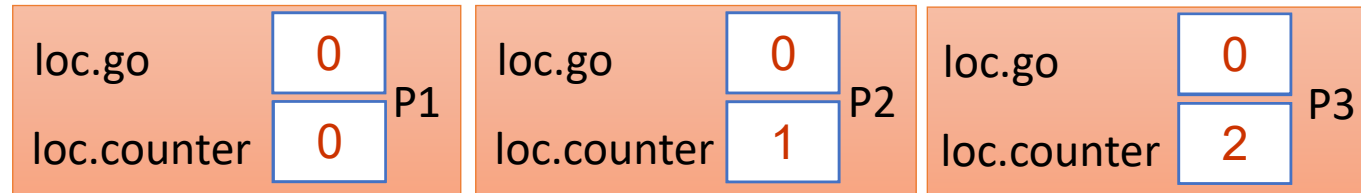
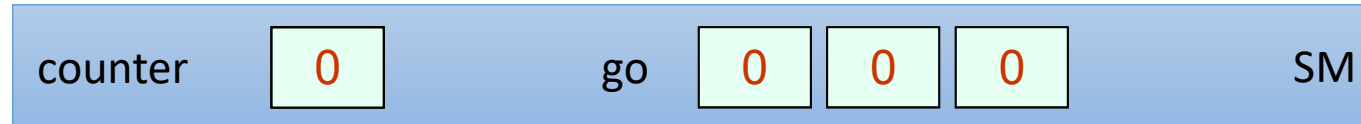
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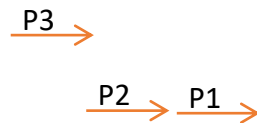
P1,P2 Busy wait

A Local Spinning Counter Barrier

Example Run for n=3 Threads



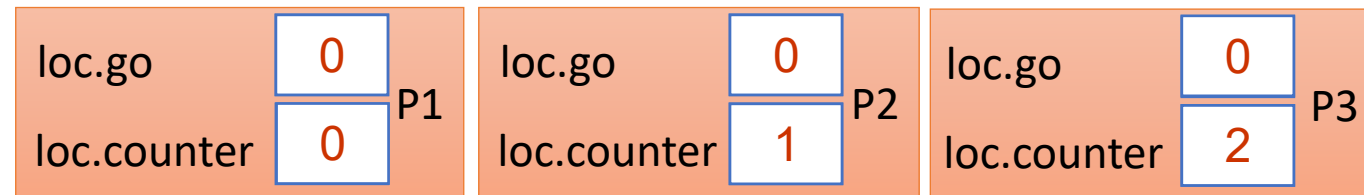
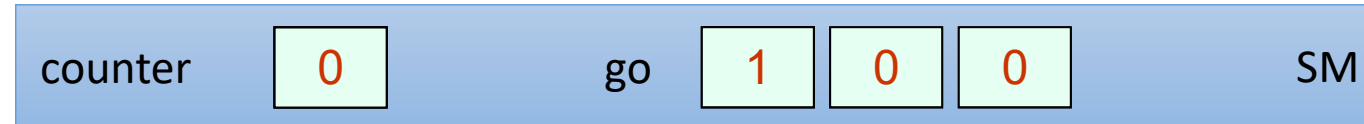
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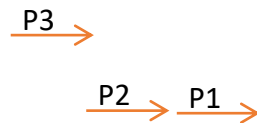
P1,P2 Busy wait

A Local Spinning Counter Barrier

Example Run for n=3 Threads



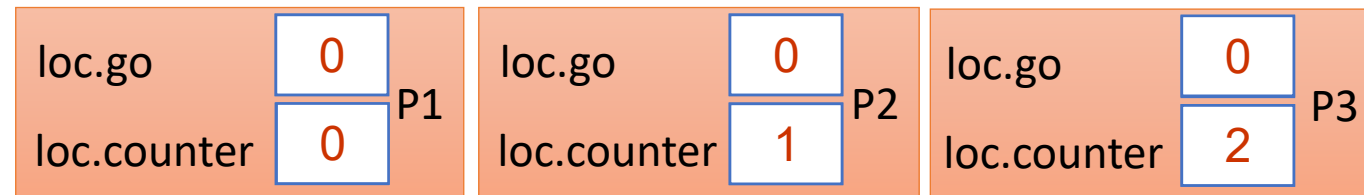
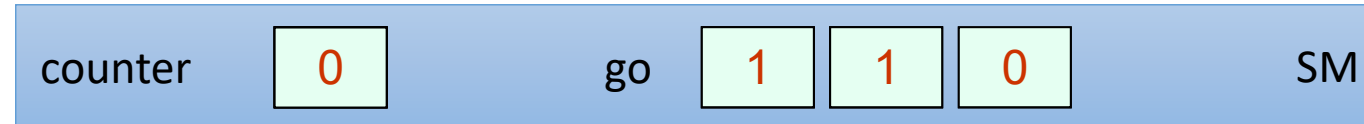
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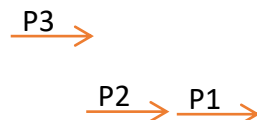
P1,P2 Busy wait

A Local Spinning Counter Barrier

Example Run for n=3 Threads



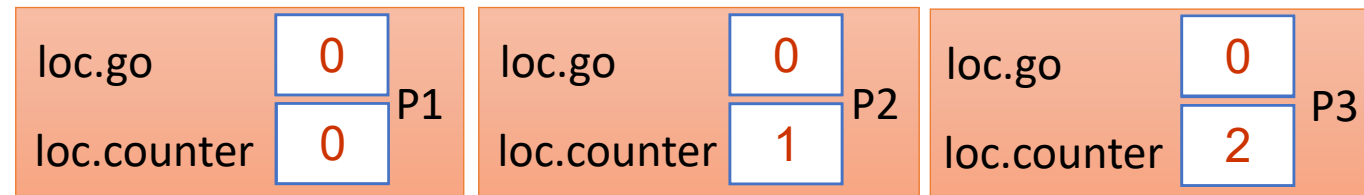
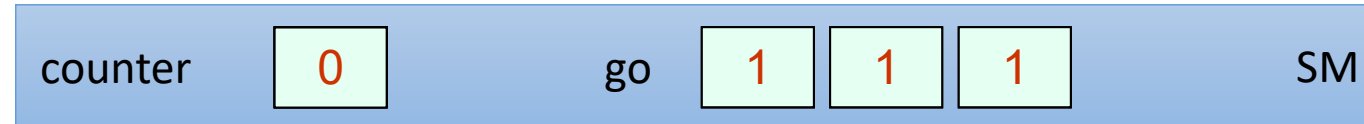
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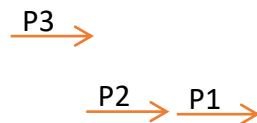
P1,P2 Busy wait

A Local Spinning Counter Barrier

Example Run for n=3 Threads



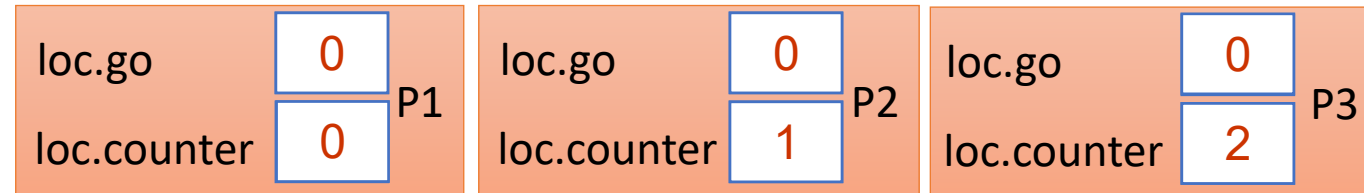
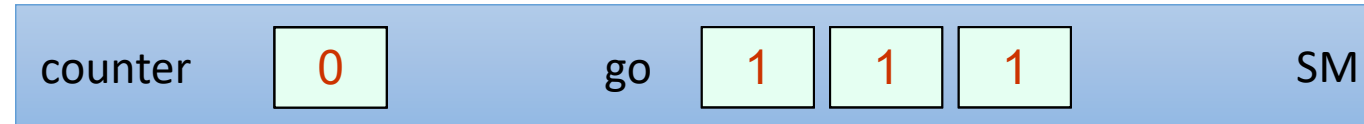
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P1,P2 Busy wait

A Local Spinning Counter Barrier

Example Run for n=3 Threads

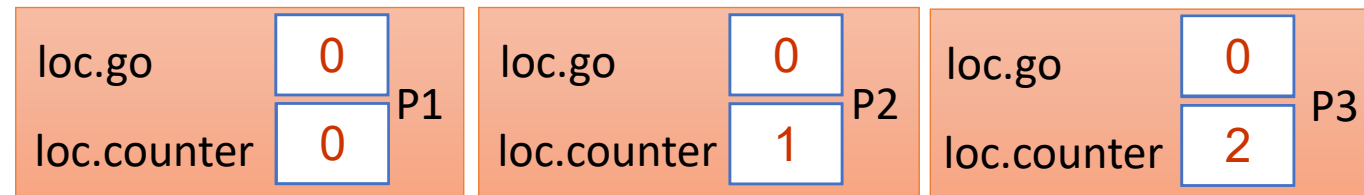
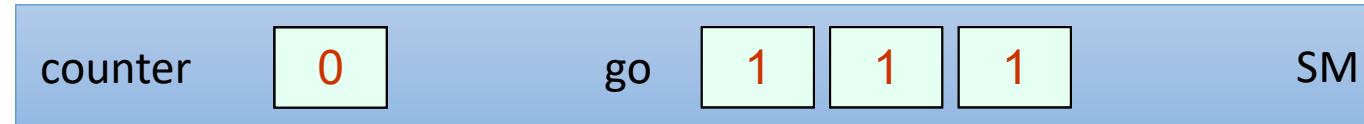


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A Local Spinning Counter Barrier

Example Run for n=3 Threads



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```



Pros/Cons?
Does this actually reduce contention?

Comparison of counter-based Barriers

Simple Barrier

- Pros:

- Cons:

Simple Barrier with go array

- Pros:

- Cons:

Comparison of counter-based Barriers

Simple Barrier

- **Pros:**
 - Very Simple
 - Shared memory: $O(\log n)$ *bits*
 - Takes $O(1)$ until last waiting p is awoken
- **Cons:**
 - High contention on the go bit
 - Contention on the counter register (*)

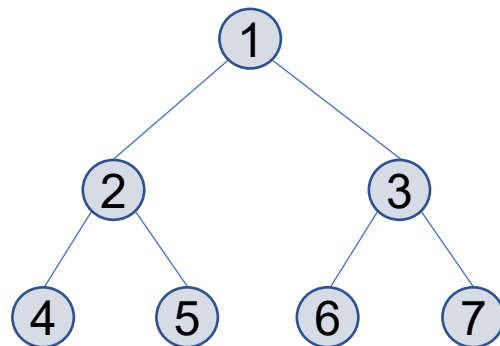
Simple Barrier with go array

- **Pros:**
 - Low contention on the go array
 - In some models:
 - spinning is done on local memory
 - remote mem. ref.: $O(1)$
- **Cons:**
 - Shared memory: $O(n)$
 - Still contention on the counter register (*)
 - Takes $O(n)$ until last waiting p is awoken

Tree Barriers

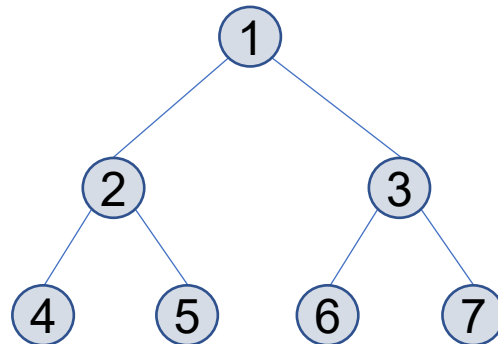


A Tree-based Barrier



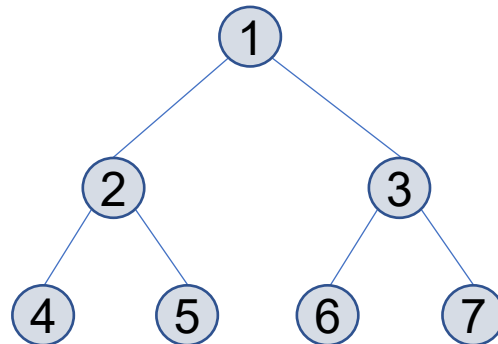
A Tree-based Barrier

- Threads are organized in a binary tree
- Each node is owned by a predetermined thread



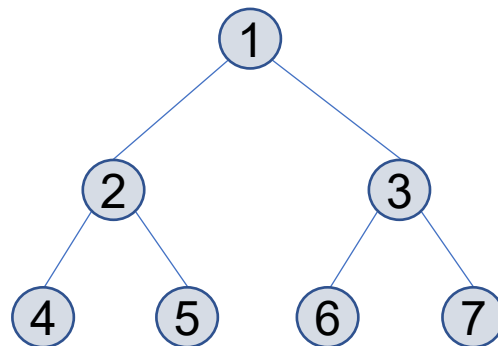
A Tree-based Barrier

- Threads are organized in a binary tree
- Each node is owned by a predetermined thread
- Each thread waits until its 2 children arrive
 - combines results
 - passes them on to its parent

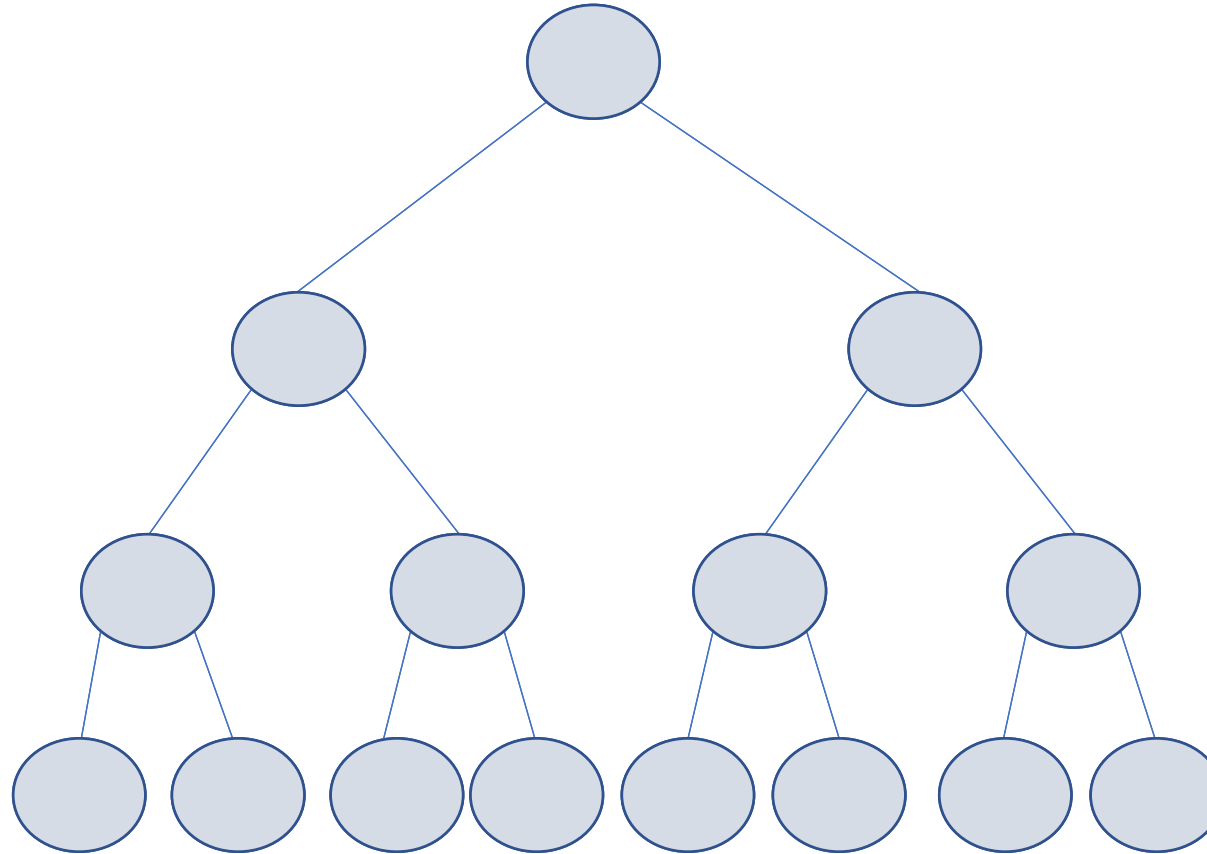


A Tree-based Barrier

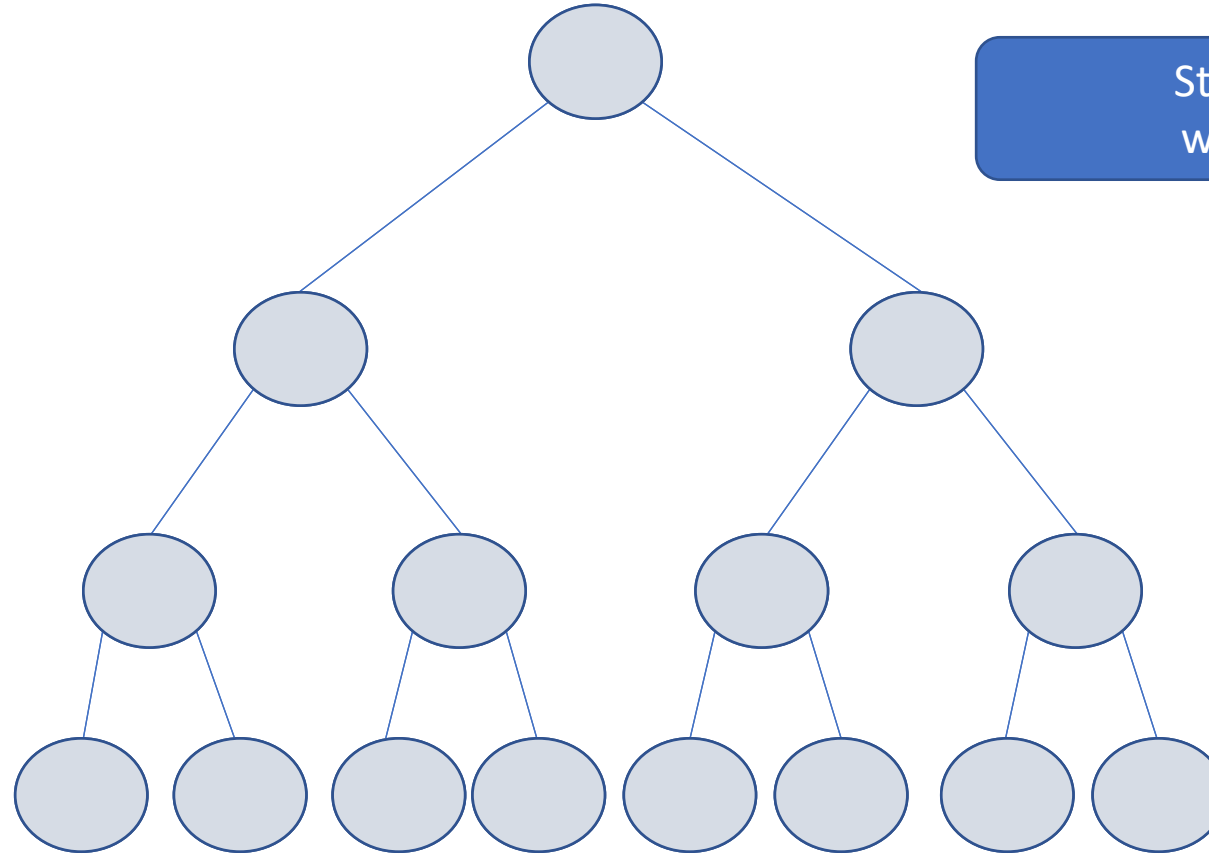
- Threads are organized in a binary tree
- Each node is owned by a predetermined thread
- Each thread waits until its 2 children arrive
 - combines results
 - passes them on to its parent
- Root learns that its 2 children have arrived → tells children they can go
- The signal propagates down the tree until all the threads get the message



A Tree-based Barrier: indexing

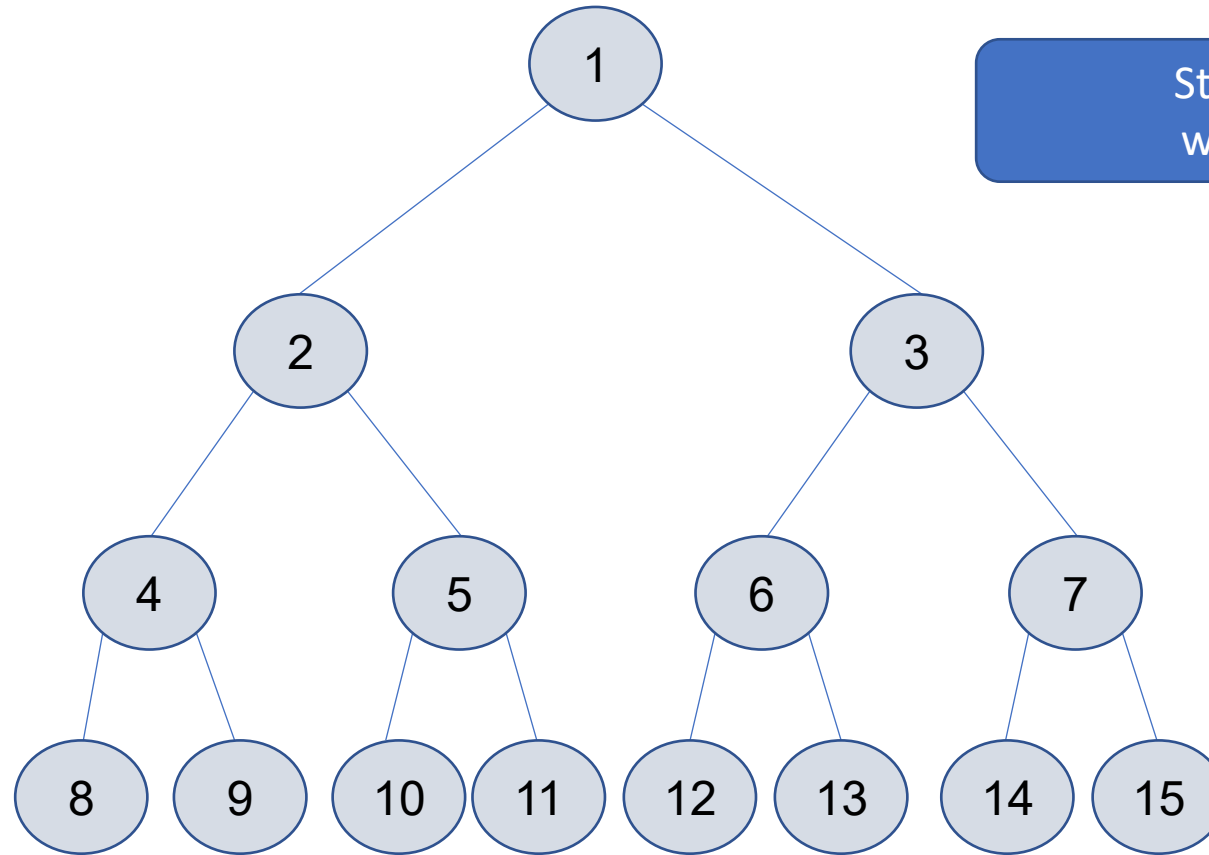


A Tree-based Barrier: indexing



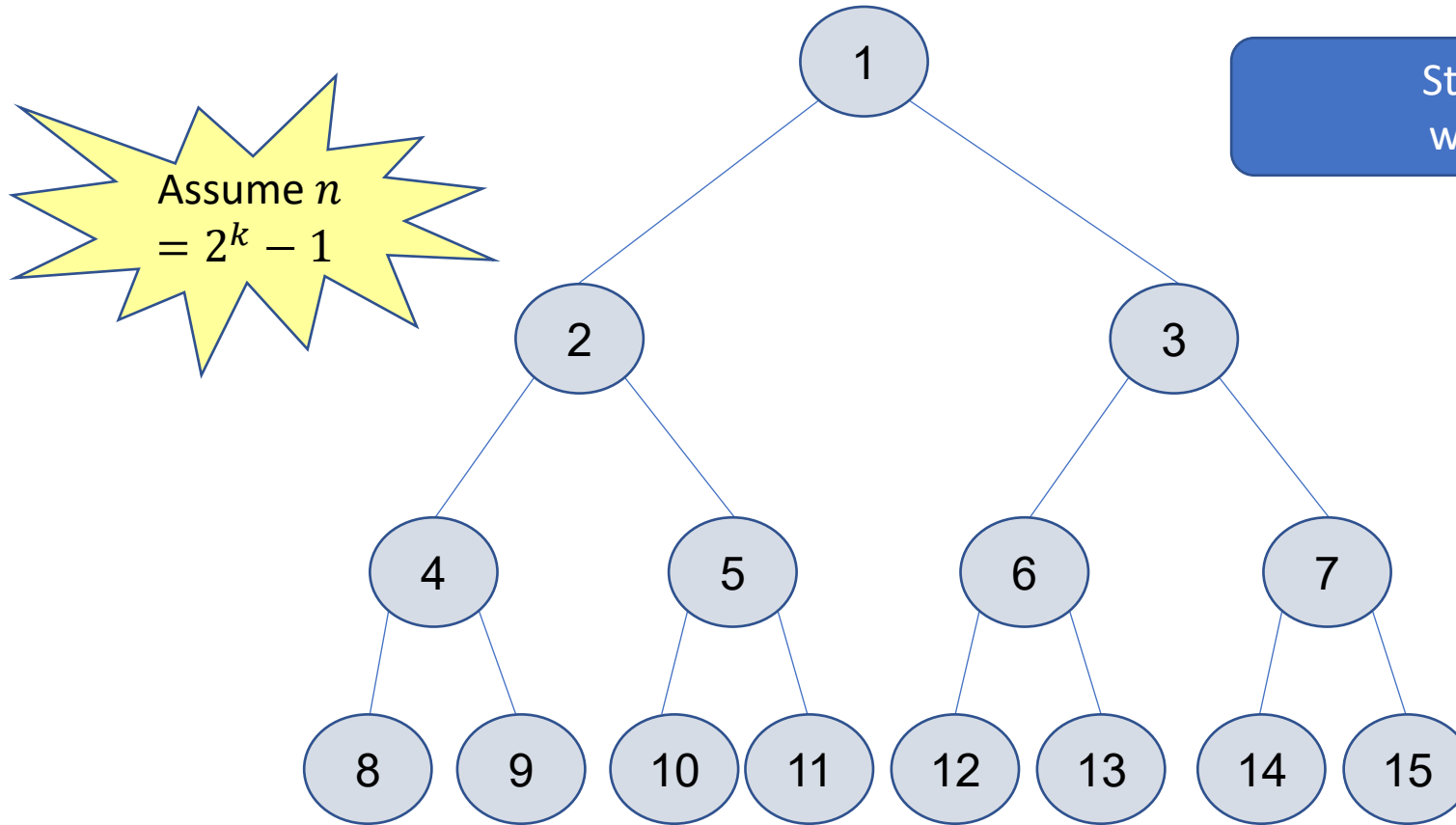
Step 1: label numerically with depth-first traversal

A Tree-based Barrier: indexing

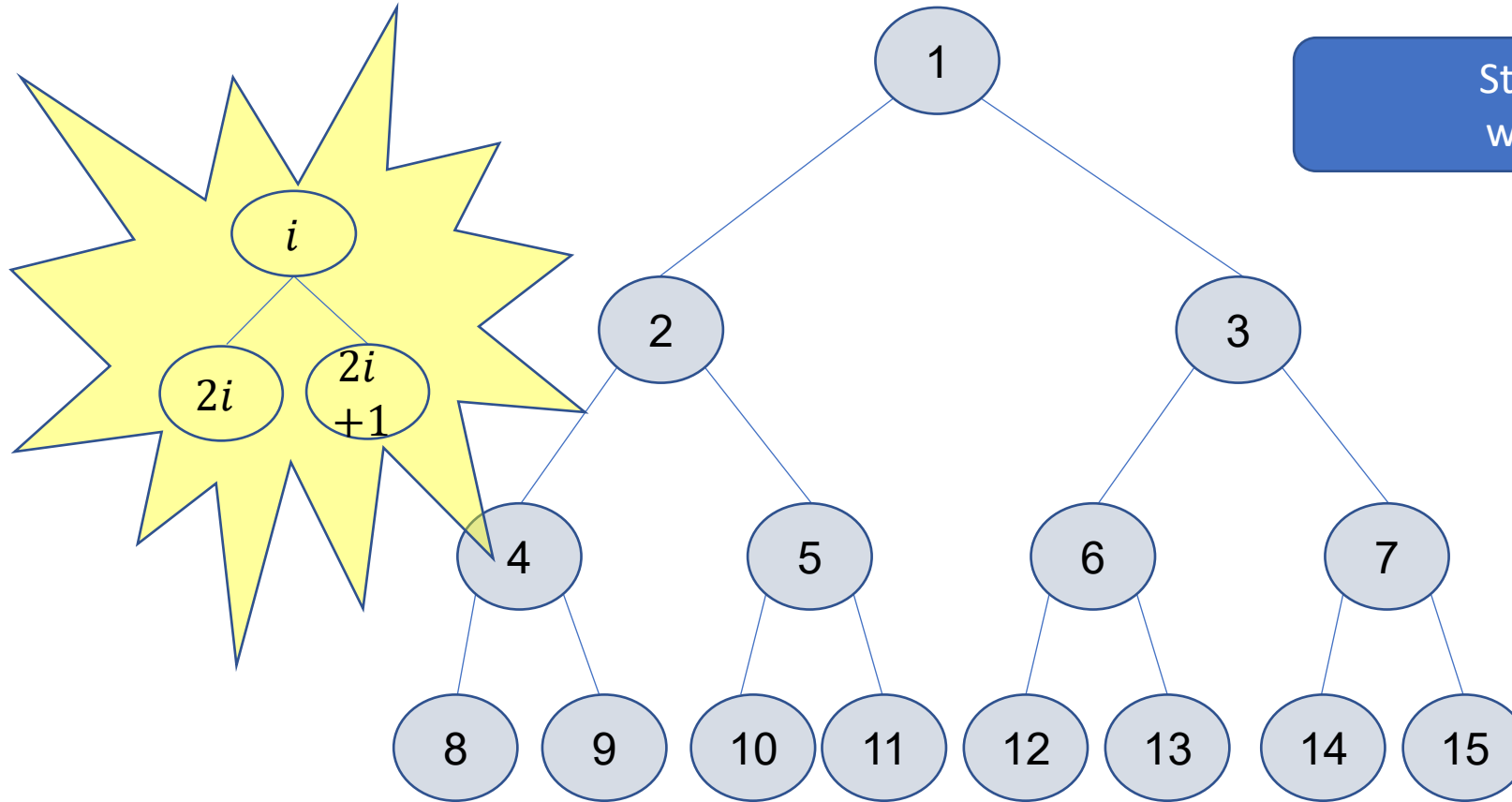


Step 1: label numerically with depth-first traversal

A Tree-based Barrier: indexing



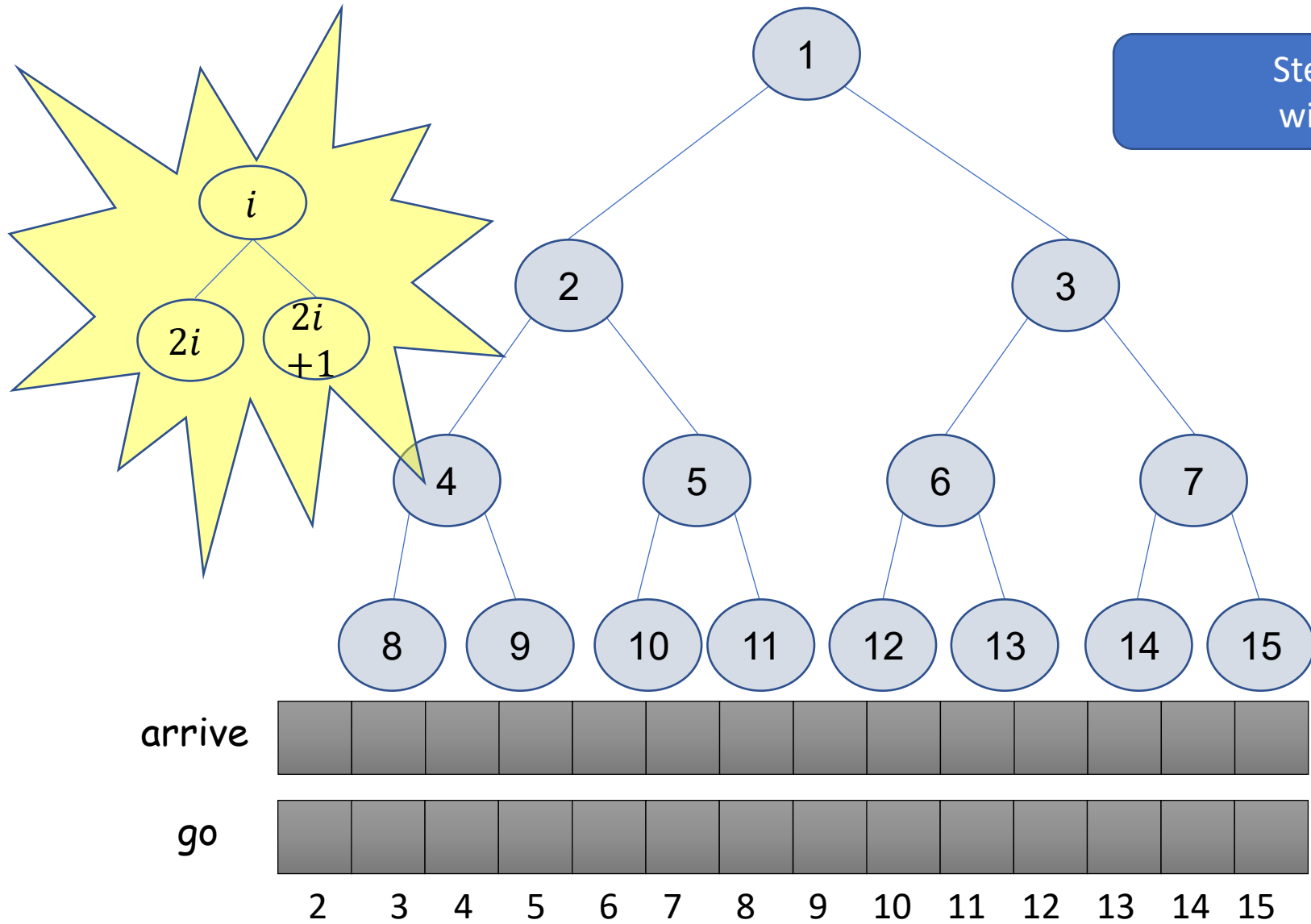
A Tree-based Barrier: indexing



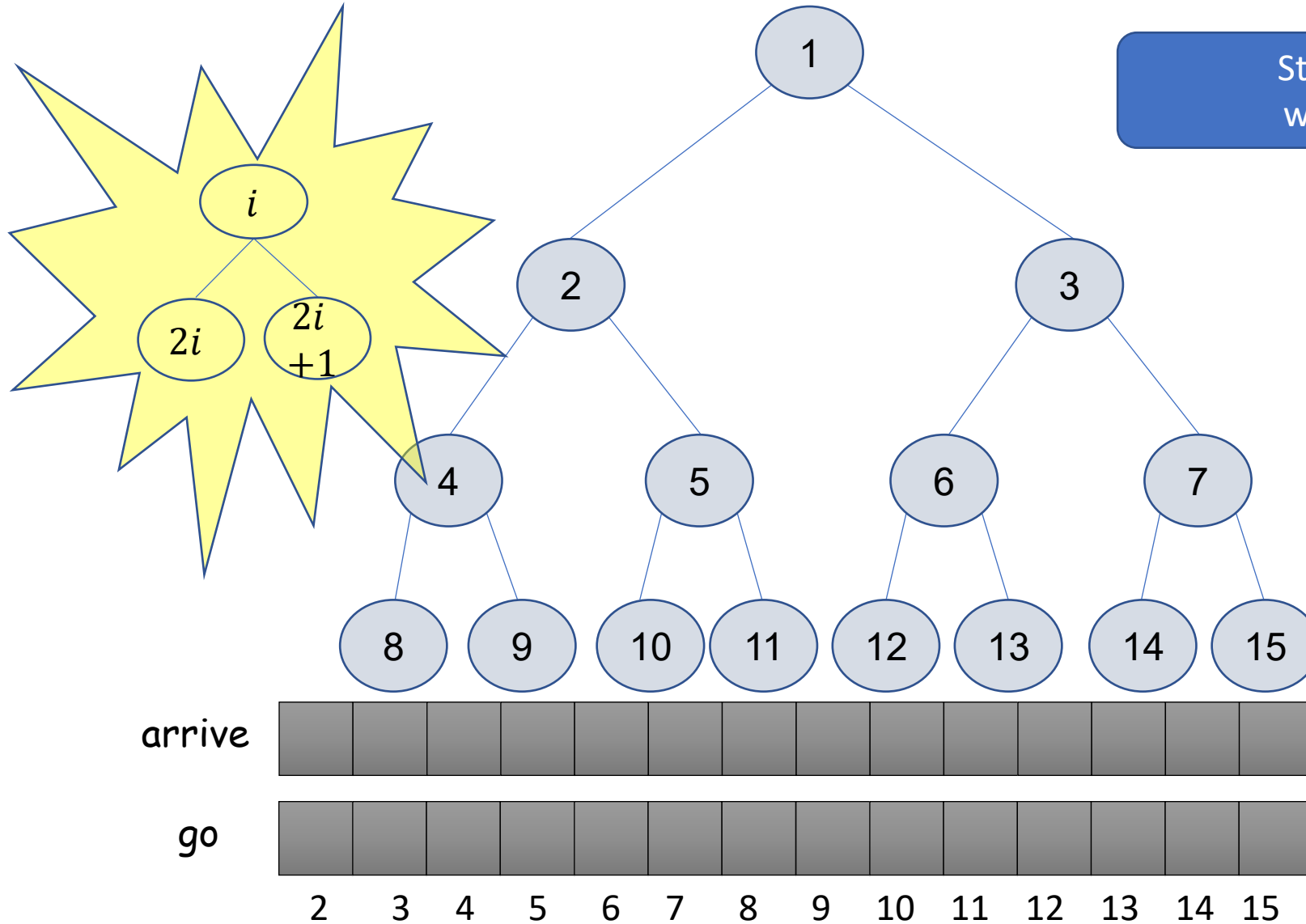
Step 1: label numerically with depth-first traversal

A Tree-based Barrier: indexing

Step 1: label numerically with depth-first traversal



A Tree-based Barrier: indexing



Indexing starts from 2
Root \rightarrow 1, doesn't need wait objects

A Tree-based Barrier program of thread i

```
shared   arrive[2..n]: array of atomic bits, initial values = 0  
          go[2..n]: array of atomic bits, initial values = 0
```

```
1  if i=1 then                                // root  
2      await(arrive[2] = 1); arrive[2] := 0  
3      await(arrive[3] = 1); arrive[3] := 0  
4      go[2] = 1; go[3] = 1  
5  else if i ≤ (n-1)/2 then                    // internal node  
6      await(arrive[2i] = 1); arrive[2i] := 0  
7      await(arrive[2i+1] = 1); arrive[2i+1] := 0  
8      arrive[i] := 1  
9      await(go[i] = 1); go[i] := 0  
10     go[2i] = 1; go[2i+1] := 1  
11  else                                        // leaf  
12     arrive[i] := 1  
13     await(go[i] = 1); go[i] := 0 fi  
14  fi
```

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Root

Internal

Leaf

A Tree-based Barrier program of thread i

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Root

Internal

Leaf

Root:

- Wait for arriving children
- Tell children to go

A Tree-based Barrier program of thread i

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Root

Internal

Leaf

Root:

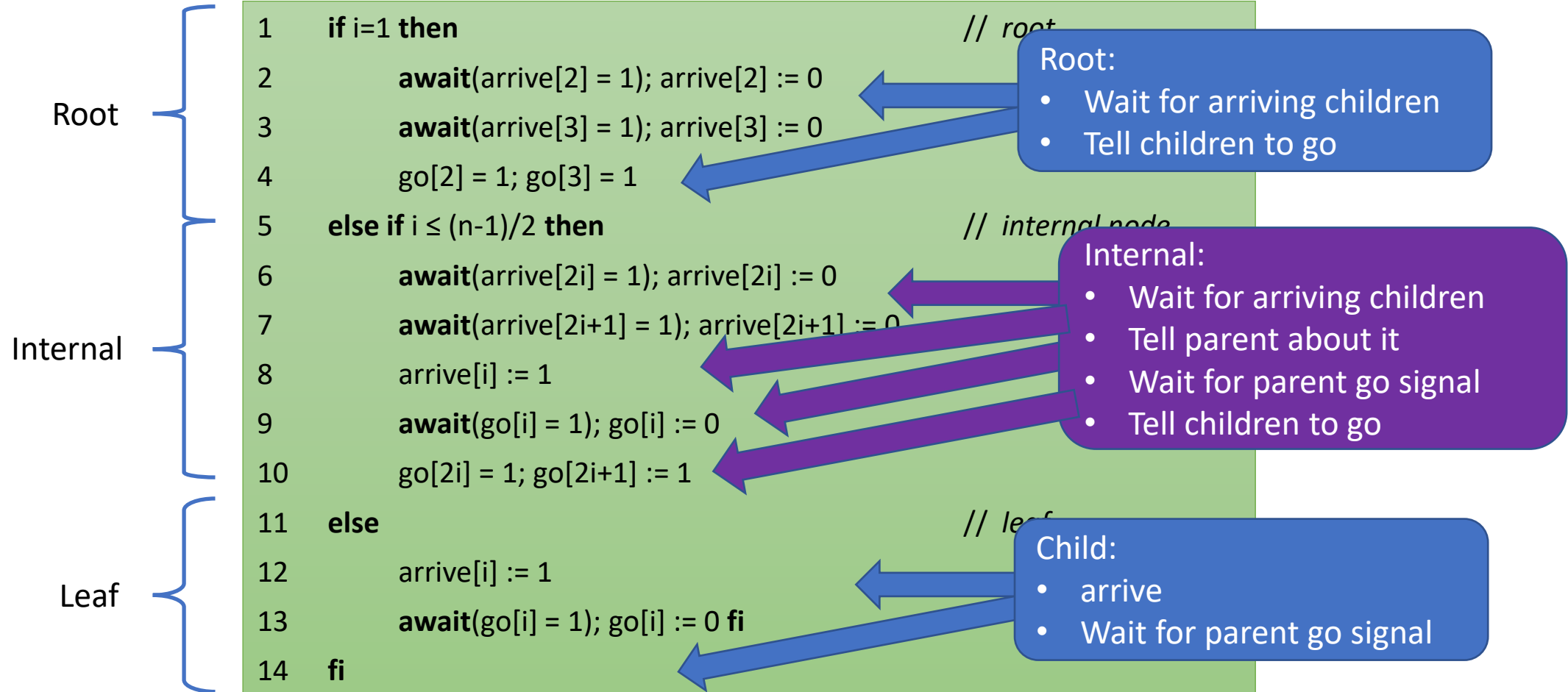
- Wait for arriving children
- Tell children to go

Internal:

- Wait for arriving children
- Tell parent about it
- Wait for parent go signal
- Tell children to go

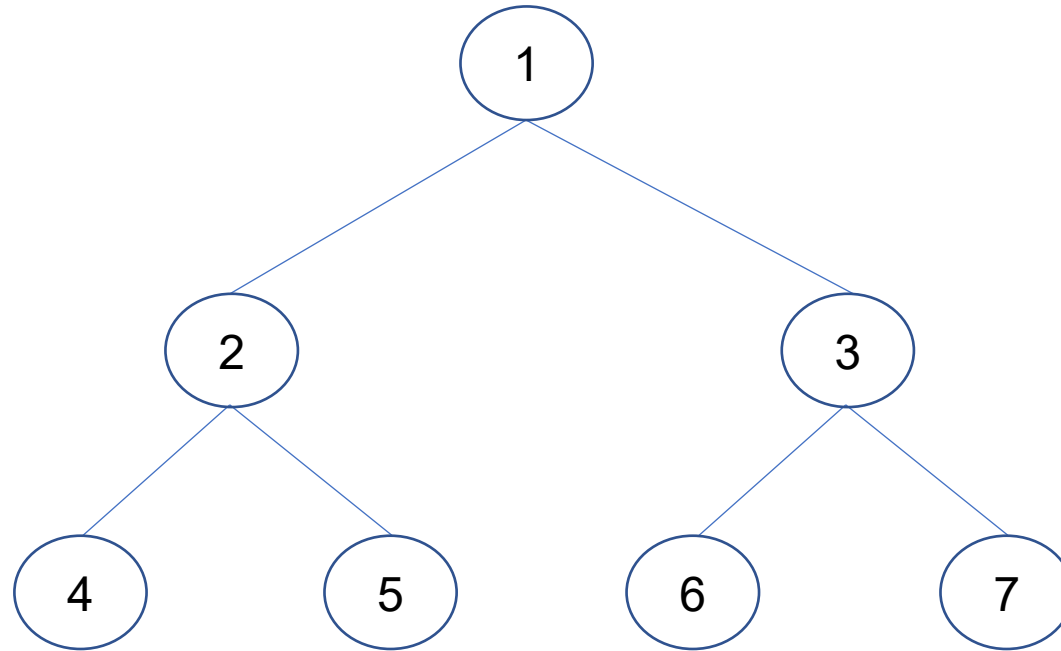
A Tree-based Barrier program of thread i

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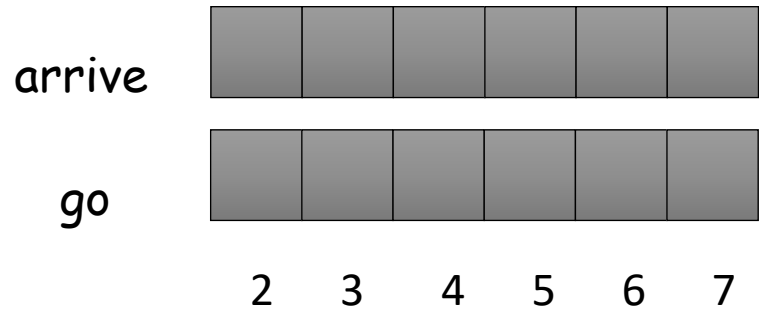
A Tree-based Barrier

Example Run for n=7 threads



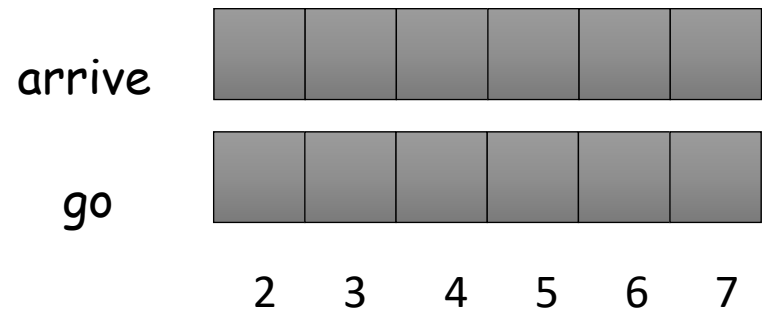
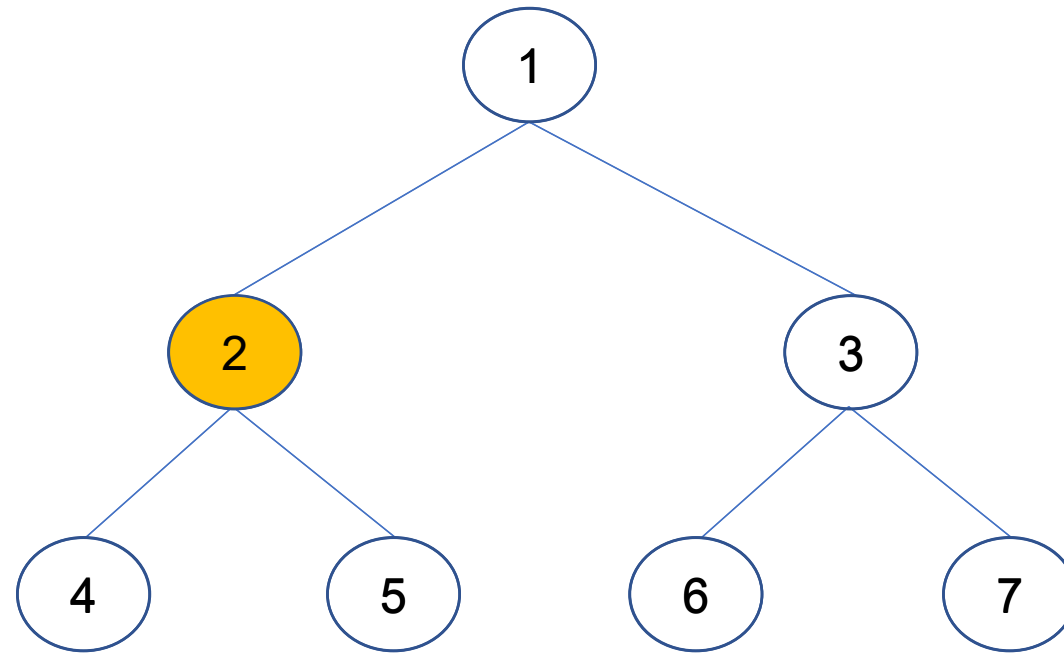
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A Tree-based Barrier

Example Run for n=7 threads



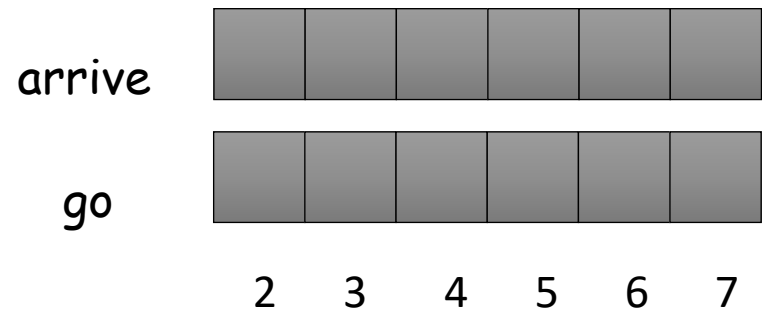
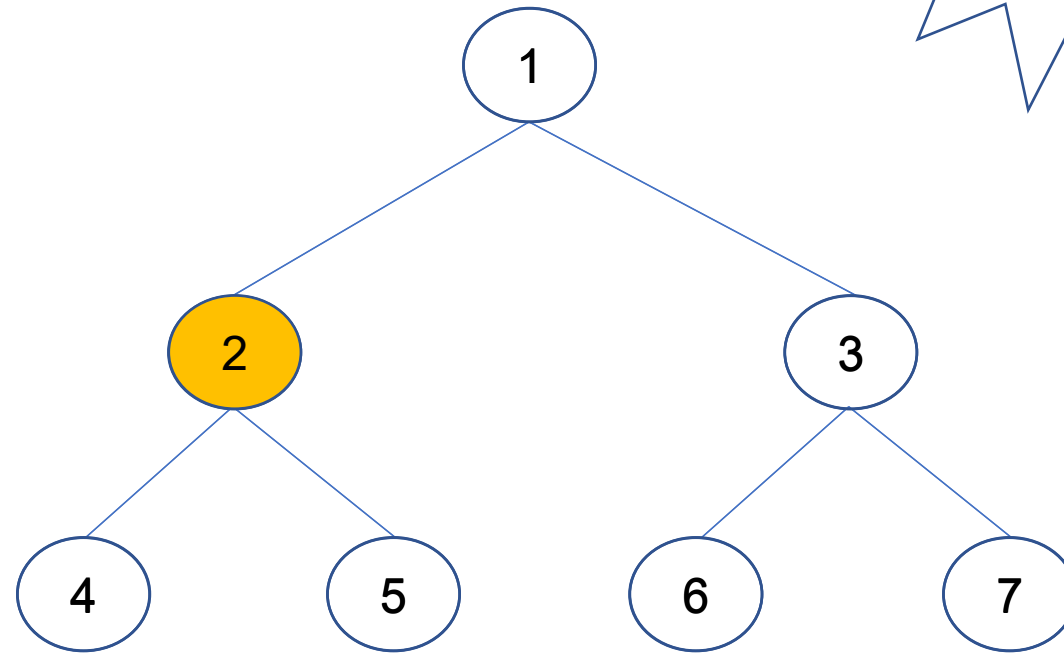
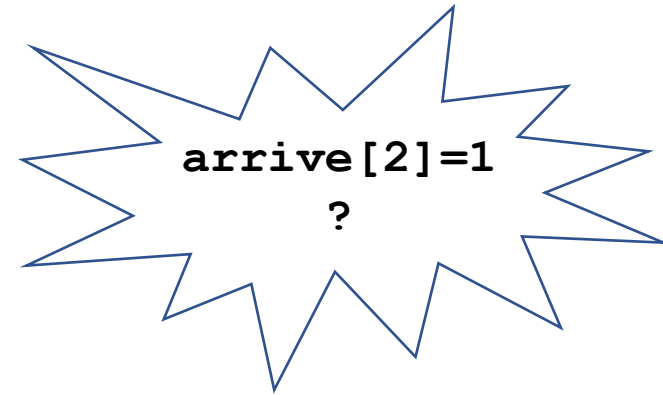
```

shared arrive[2..n]: array of atomic bits, initial values = 0
shared go[2..n]: array of atomic bits, initial values = 0

1 if i=1 then // root
2   await(arrive[2] = 1); arrive[2] := 0
3   await(arrive[3] = 1); arrive[3] := 0
4   go[2] = 1; go[3] = 1
5 else if i ≤ (n-1)/2 then // internal node
6   await(arrive[2i] = 1); arrive[2i] := 0
7   await(arrive[2i+1] = 1); arrive[2i+1] := 0
8   arrive[i] := 1
9   await(go[i] = 1); go[i] := 0
10  go[2i] = 1; go[2i+1] := 1
11 else // leaf
12  arrive[i] := 1
13  await(go[i] = 1); go[i] := 0 fi
14 fi
  
```

A Tree-based Barrier

Example Run for n=7 threads



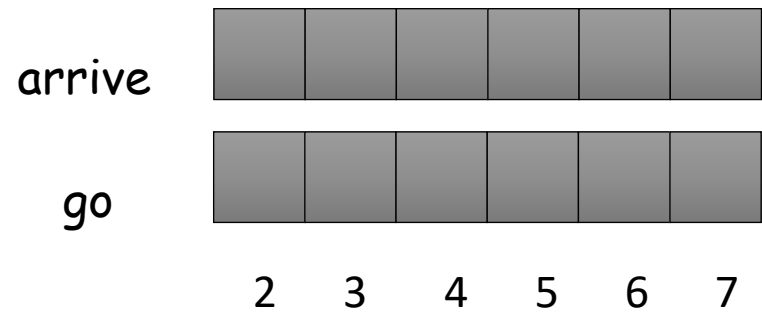
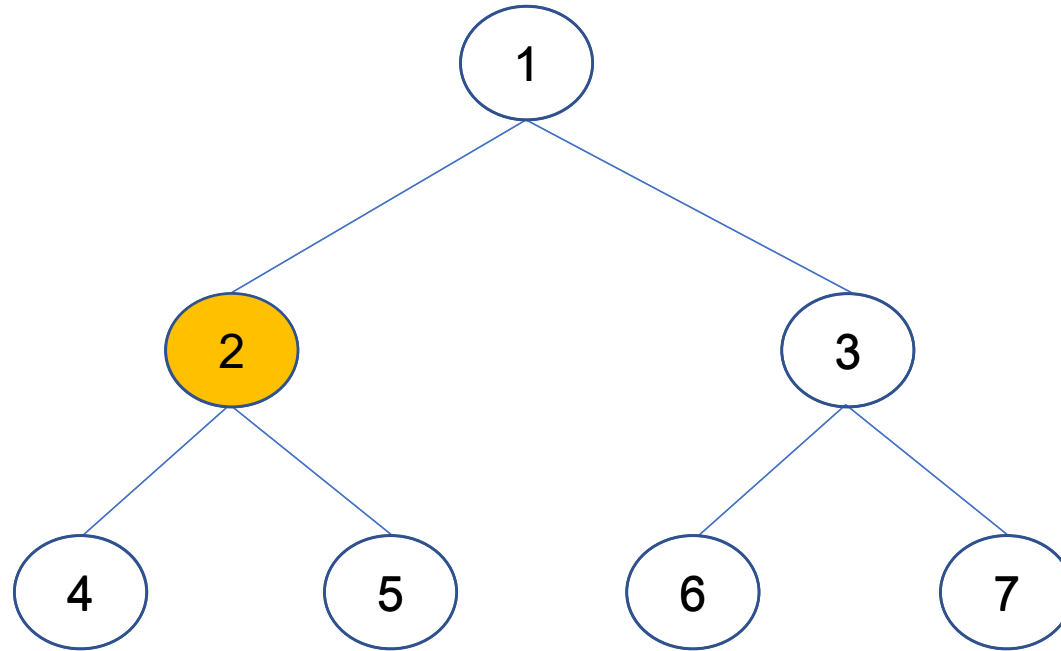
```

shared  arrive[2..n]: array of atomic bits, initial values = 0
        go[2..n]: array of atomic bits, initial values = 0

1  if i=1 then // root
2      await(arrive[2] = 1); arrive[2] := 0
3      await(arrive[3] = 1); arrive[3] := 0
4      go[2] = 1; go[3] = 1
5  else if i ≤ (n-1)/2 then // internal node
6      await(arrive[2i] = 1); arrive[2i] := 0
7      await(arrive[2i+1] = 1); arrive[2i+1] := 0
8      arrive[i] := 1
9      await(go[i] = 1); go[i] := 0
10     go[2i] = 1; go[2i+1] := 1
11 else // leaf
12     arrive[i] := 1
13     await(go[i] = 1); go[i] := 0 fi
14 fi
    
```

A Tree-based Barrier

Example Run for n=7 threads



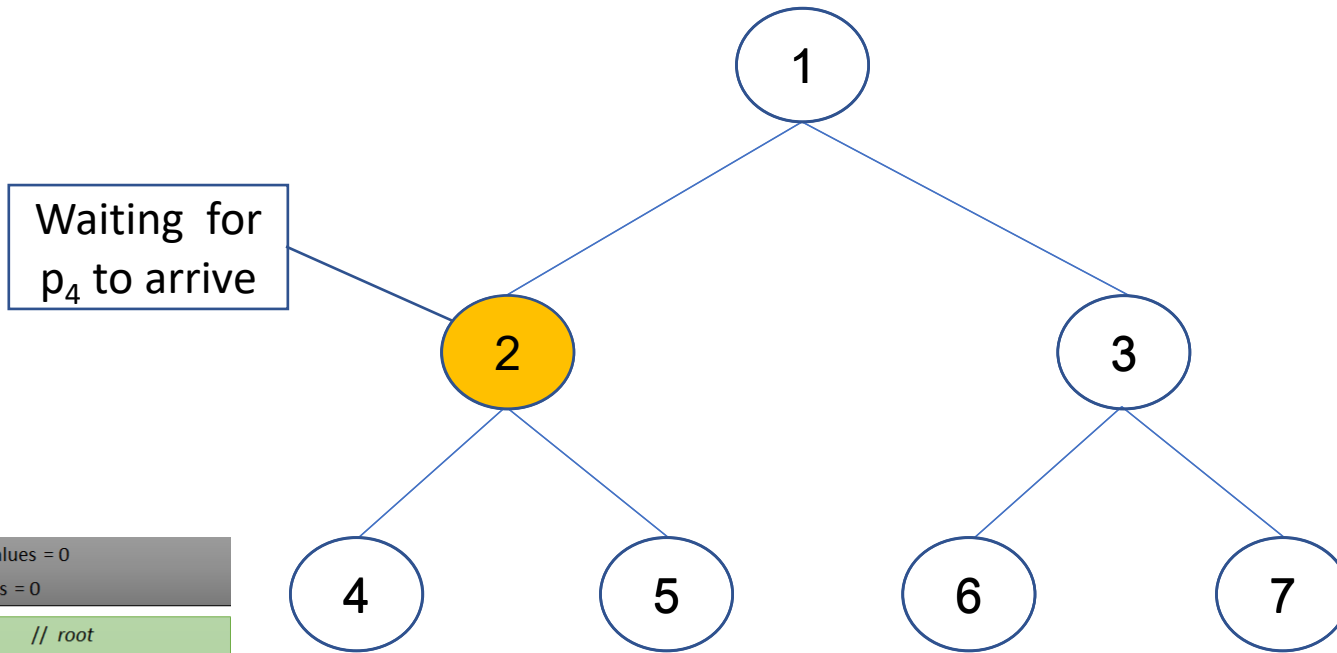
```

shared arrive[2..n]: array of atomic bits, initial values = 0
shared go[2..n]: array of atomic bits, initial values = 0

1 if i=1 then // root
2   await(arrive[2] = 1); arrive[2] := 0
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4   go[2] = 1; go[3] = 1
5 else if i ≤ (n-1)/2 then // internal node
6   await(arrive[2i] = 1); arrive[2i] := 0
7   await(arrive[2i+1] = 1); arrive[2i+1] := 0
8   arrive[i] := 1
9   await(go[i] = 1); go[i] := 0
10  go[2i] = 1; go[2i+1] := 1
11 else // leaf
12  arrive[i] := 1
13  await(go[i] = 1); go[i] := 0 fi
14 fi
  
```

A Tree-based Barrier

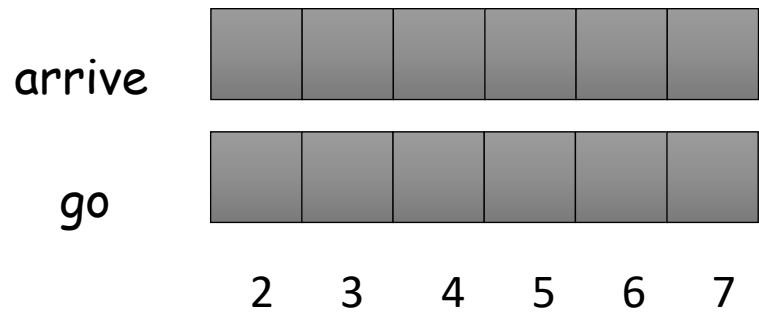
Example Run for n=7 threads



```

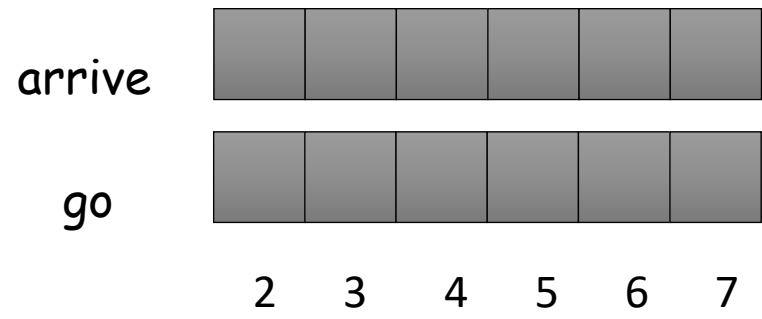
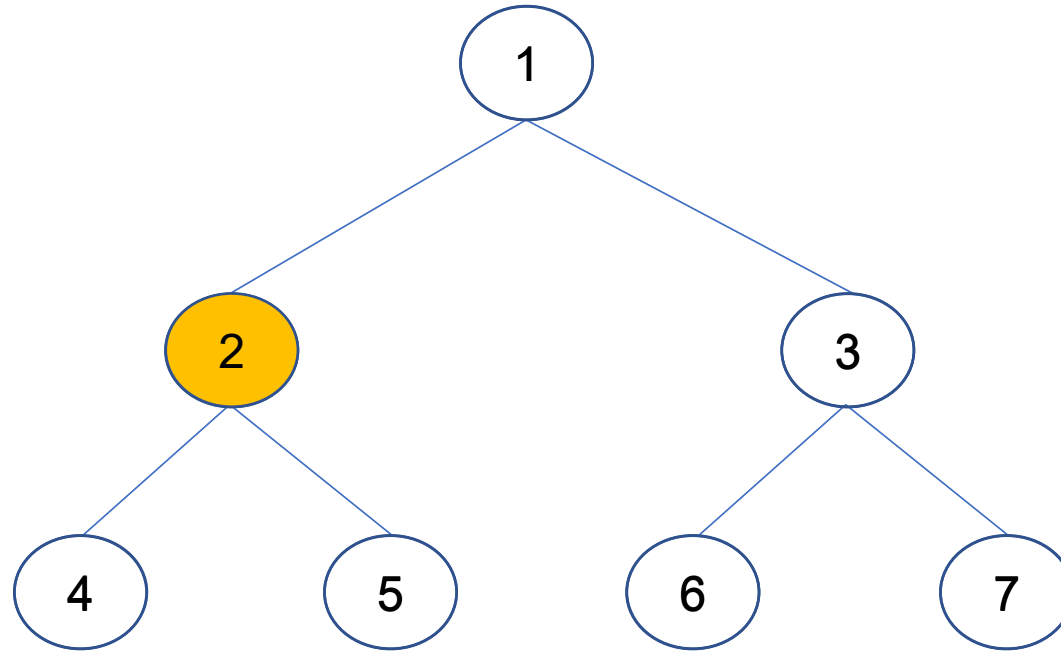
shared arrive[2..n]: array of atomic bits, initial values = 0
shared go[2..n]: array of atomic bits, initial values = 0

1  if i=1 then // root
2    await(arrive[2] = 1); arrive[2] := 0
3    await(arrive[3] = 1); arrive[3] := 0
4    go[2] = 1; go[3] = 1
5  else if i ≤ (n-1)/2 then // internal node
6    await(arrive[2i] = 1); arrive[2i] := 0
7    await(arrive[2i+1] = 1); arrive[2i+1] := 0
8    arrive[i] := 1
9    await(go[i] = 1); go[i] := 0
10   go[2i] = 1; go[2i+1] := 1
11 else // leaf
12   arrive[i] := 1
13   await(go[i] = 1); go[i] := 0 fi
14 fi
  
```



A Tree-based Barrier

Example Run for n=7 threads



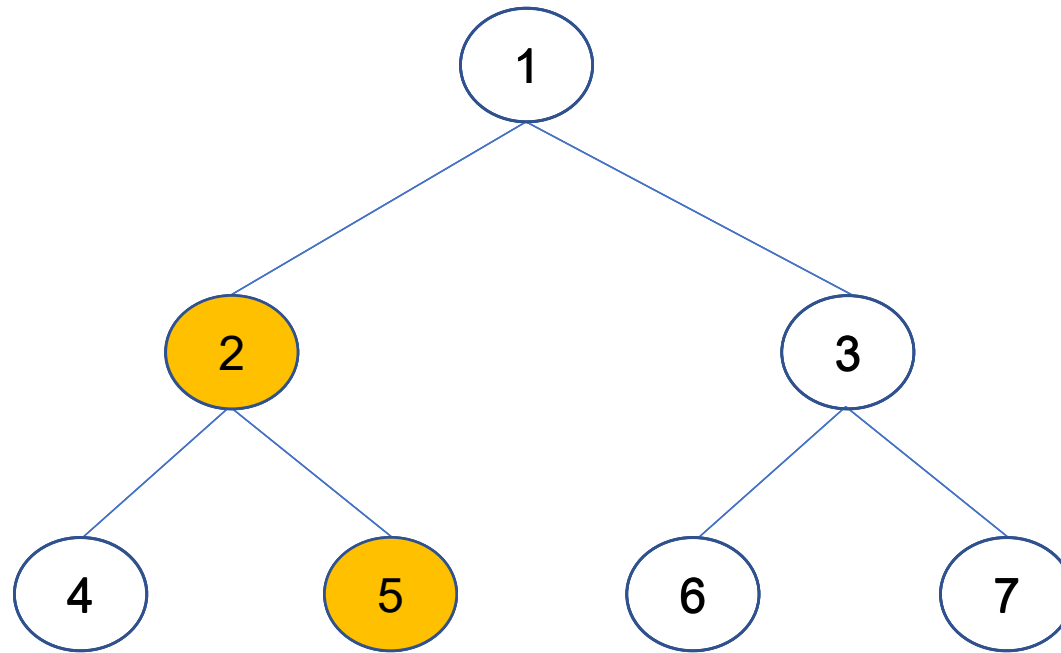
```

shared arrive[2..n]: array of atomic bits, initial values = 0
shared go[2..n]: array of atomic bits, initial values = 0

1 if i=1 then // root
2   await(arrive[2] = 1); arrive[2] := 0
3   await(arrive[3] = 1); arrive[3] := 0
4   go[2] = 1; go[3] = 1
5 else if i ≤ (n-1)/2 then // internal node
6   await(arrive[2i] = 1); arrive[2i] := 0
7   await(arrive[2i+1] = 1); arrive[2i+1] := 0
8   arrive[i] := 1
9   await(go[i] = 1); go[i] := 0
10  go[2i] = 1; go[2i+1] := 1
11 else // leaf
12  arrive[i] := 1
13  await(go[i] = 1); go[i] := 0 fi
14 fi
  
```

A Tree-based Barrier

Example Run for n=7 threads

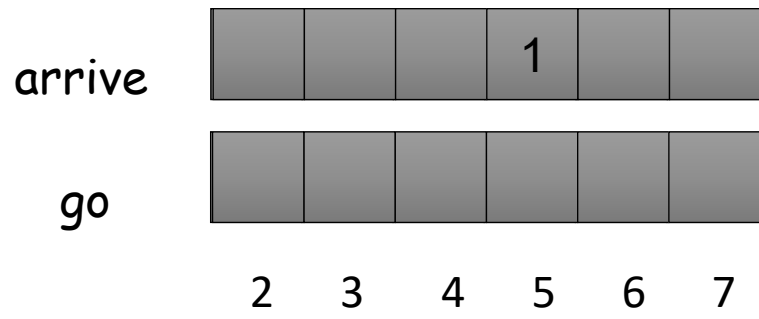


```

shared arrive[2..n]: array of atomic bits, initial values = 0
shared go[2..n]: array of atomic bits, initial values = 0
  
```

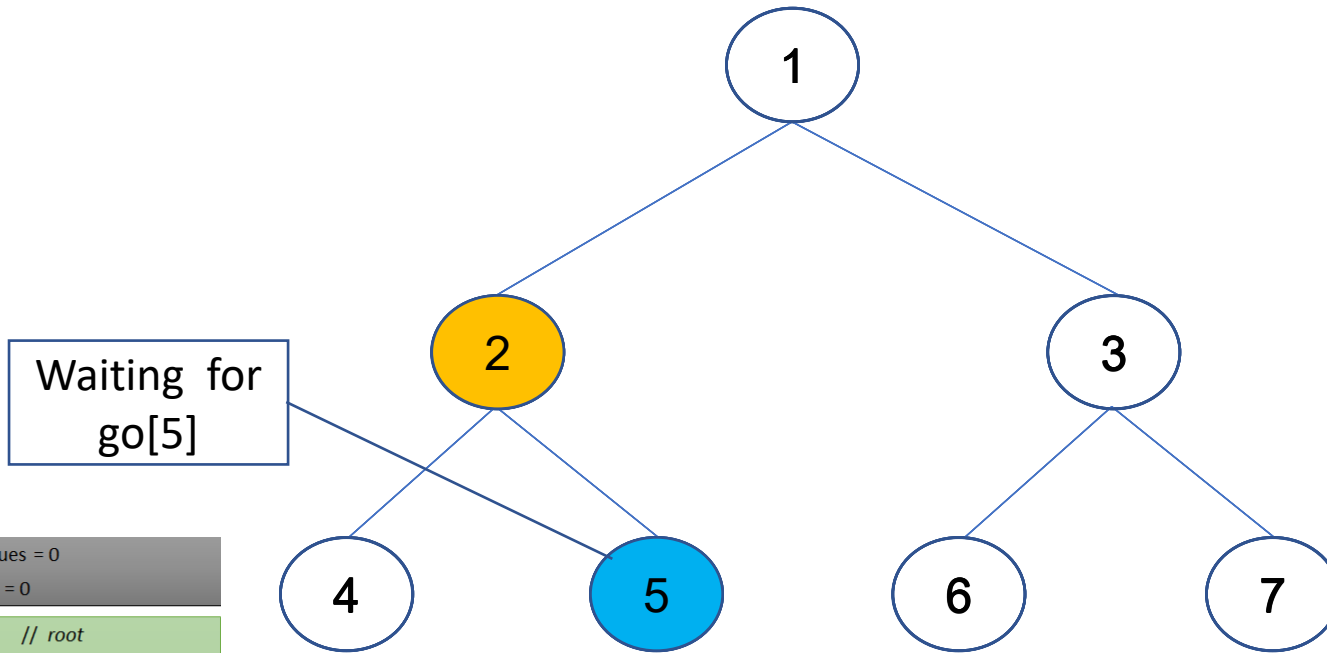
```

1  if i=1 then // root
2    await(arrive[2] = 1); arrive[2] := 0
3    await(arrive[3] = 1); arrive[3] := 0
4    go[2] = 1; go[3] = 1
5  else if i ≤ (n-1)/2 then // internal node
6    await(arrive[2i] = 1); arrive[2i] := 0
7    await(arrive[2i+1] = 1); arrive[2i+1] := 0
8    arrive[i] := 1
9    await(go[i] = 1); go[i] := 0
10   go[2i] = 1; go[2i+1] := 1
11 else // leaf
12   arrive[i] := 1
13   await(go[i] = 1); go[i] := 0 fi
14 fi
  
```



A Tree-based Barrier

Example Run for n=7 threads

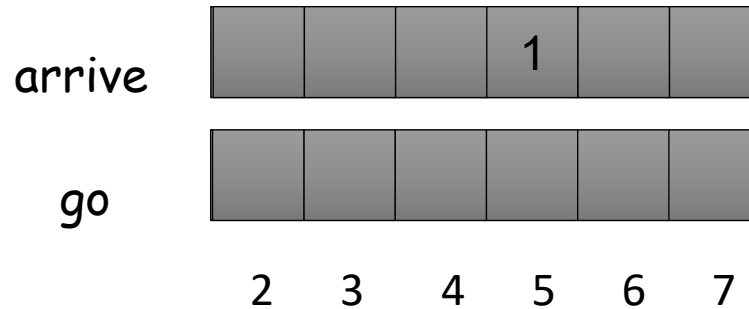


Waiting for
go[5]

```

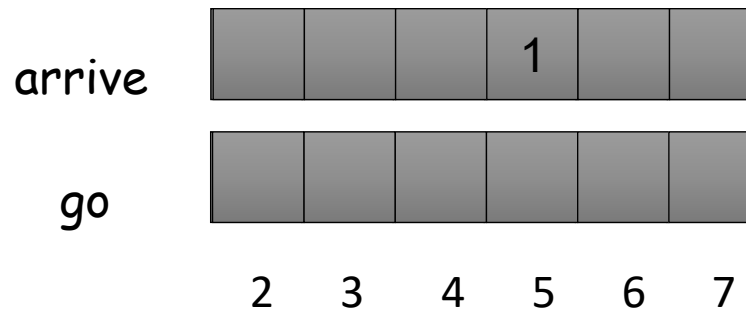
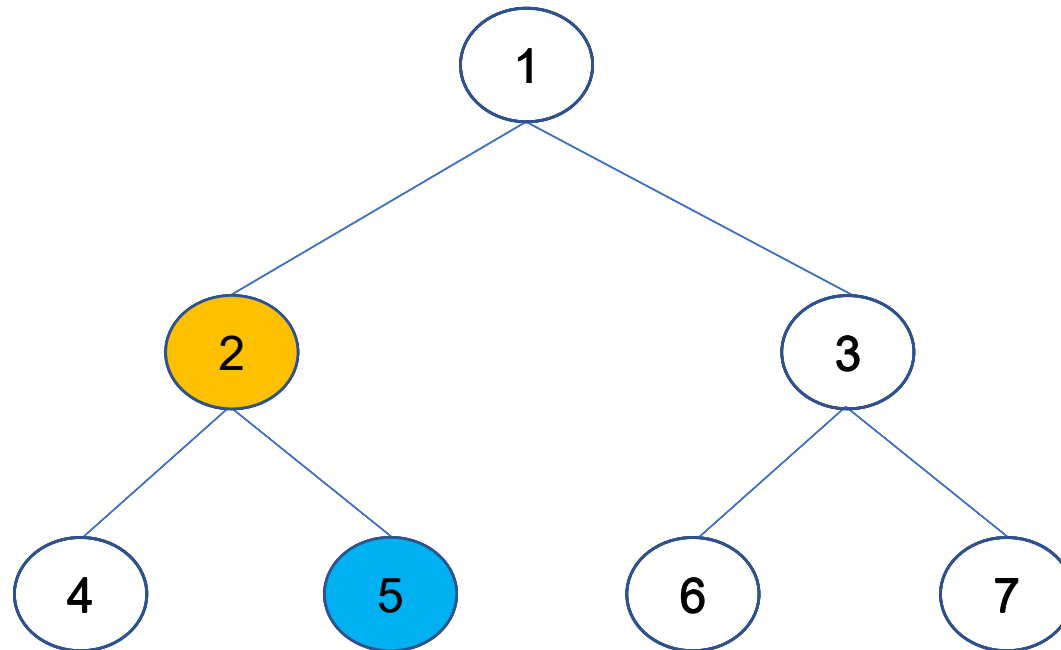
shared arrive[2..n]: array of atomic bits, initial values = 0
shared go[2..n]: array of atomic bits, initial values = 0

1 if i=1 then // root
2   await(arrive[2] = 1); arrive[2] := 0
3   await(arrive[3] = 1); arrive[3] := 0
4   go[2] = 1; go[3] = 1
5 else if i ≤ (n-1)/2 then // internal node
6   await(arrive[2i] = 1); arrive[2i] := 0
7   await(arrive[2i+1] = 1); arrive[2i+1] := 0
8   arrive[i] := 1
9   await(go[i] = 1); go[i] := 0
10  go[2i] = 1; go[2i+1] := 1
11 else // leaf
12  arrive[i] := 1
13  await(go[i] = 1); go[i] := 0 fi
14 fi
  
```



A Tree-based Barrier

Example Run for n=7 threads



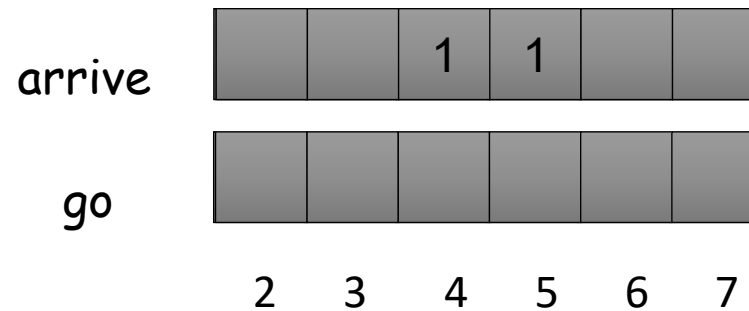
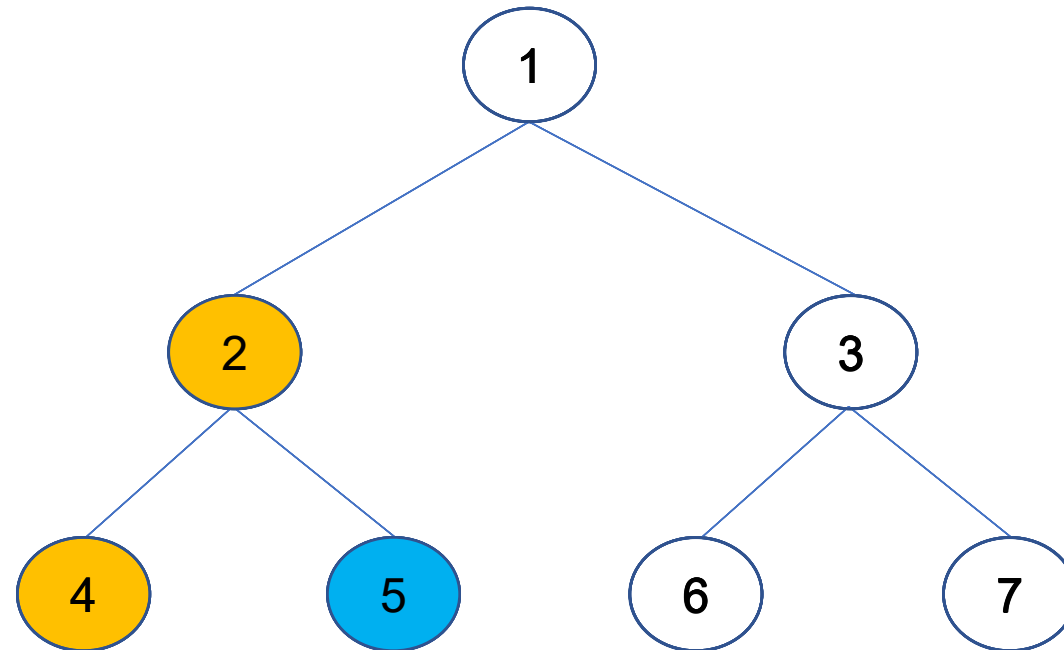
```

shared arrive[2..n]: array of atomic bits, initial values = 0
shared go[2..n]: array of atomic bits, initial values = 0

1 if i=1 then // root
2   await(arrive[2] = 1); arrive[2] := 0
3   await(arrive[3] = 1); arrive[3] := 0
4   go[2] = 1; go[3] = 1
5 else if i ≤ (n-1)/2 then // internal node
6   await(arrive[2i] = 1); arrive[2i] := 0
7   await(arrive[2i+1] = 1); arrive[2i+1] := 0
8   arrive[i] := 1
9   await(go[i] = 1); go[i] := 0
10  go[2i] = 1; go[2i+1] := 1
11 else // leaf
12  arrive[i] := 1
13  await(go[i] = 1); go[i] := 0 fi
14 fi
  
```


A Tree-based Barrier

Example Run for n=7 threads



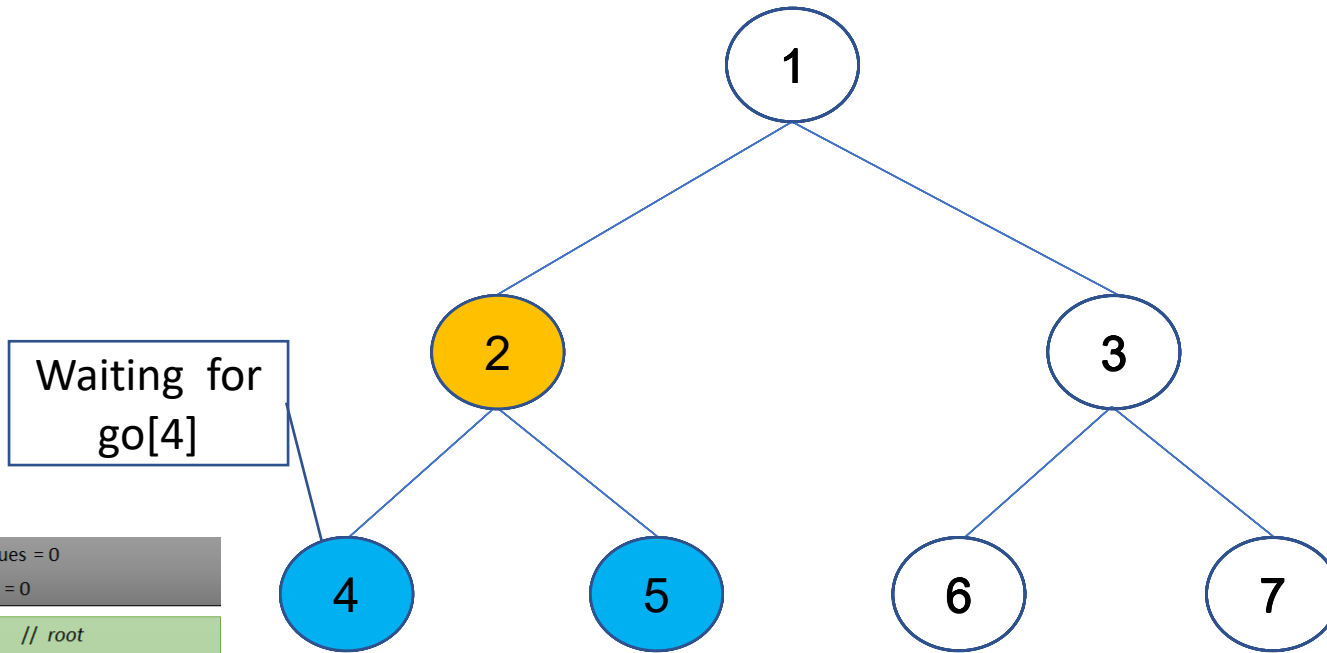
```

shared arrive[2..n]: array of atomic bits, initial values = 0
shared go[2..n]: array of atomic bits, initial values = 0

1  if i=1 then // root
2    await(arrive[2] = 1); arrive[2] := 0
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4    go[2] = 1; go[3] = 1
5  else if i ≤ (n-1)/2 then // internal node
6    await(arrive[2i] = 1); arrive[2i] := 0
7    await(arrive[2i+1] = 1); arrive[2i+1] := 0
8    arrive[i] := 1
9    await(go[i] = 1); go[i] := 0
10   go[2i] = 1; go[2i+1] := 1
11 else // leaf
12   arrive[i] := 1
13   await(go[i] = 1); go[i] := 0 fi
14 fi
  
```

A Tree-based Barrier

Example Run for n=7 threads

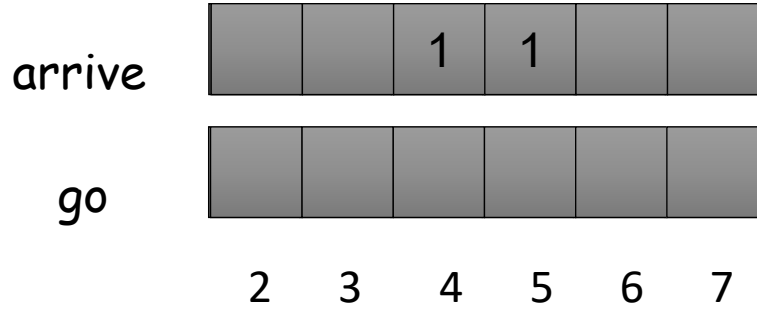


Waiting for
go[4]

```

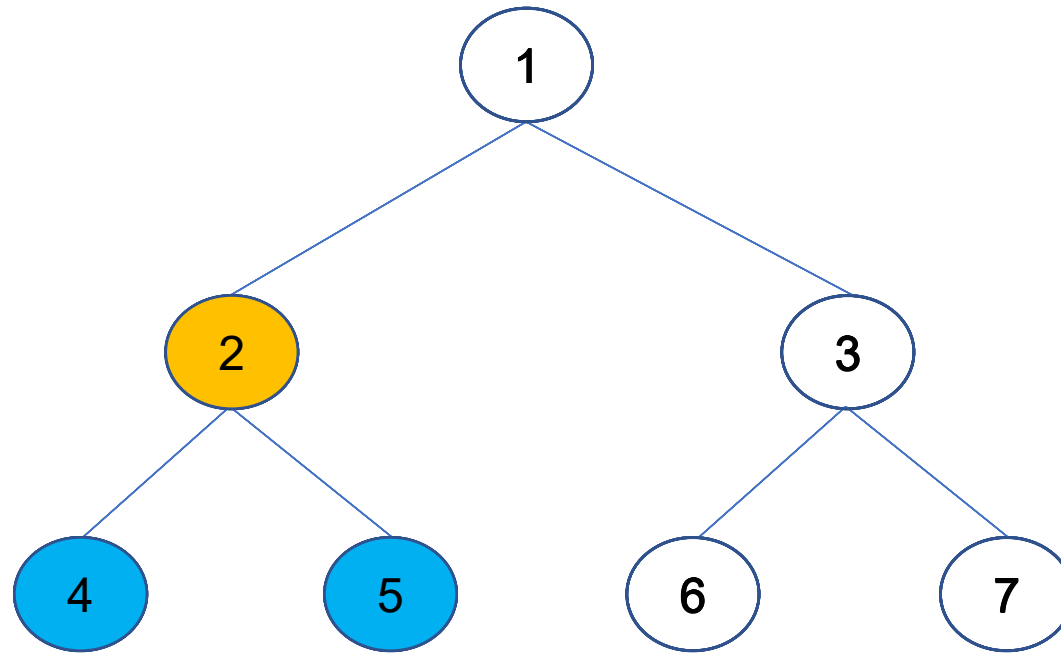
shared  arrive[2..n]: array of atomic bits, initial values = 0
        go[2..n]: array of atomic bits, initial values = 0

1  if i=1 then // root
2    await(arrive[2] = 1); arrive[2] := 0
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4    go[2] = 1; go[3] = 1
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7    await(arrive[2i+1] = 1); arrive[2i+1] := 0
8    arrive[i] := 1
9    await(go[i] = 1); go[i] := 0
10   go[2i] = 1; go[2i+1] := 1
11 else // leaf
12   arrive[i] := 1
13   await(go[i] = 1); go[i] := 0 fi
14 fi
  
```



A Tree-based Barrier

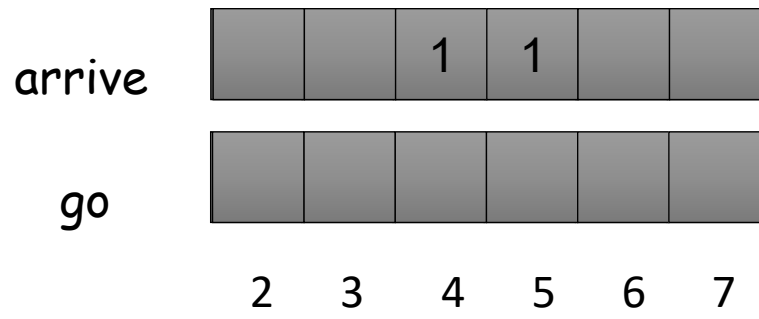
Example Run for n=7 threads



```

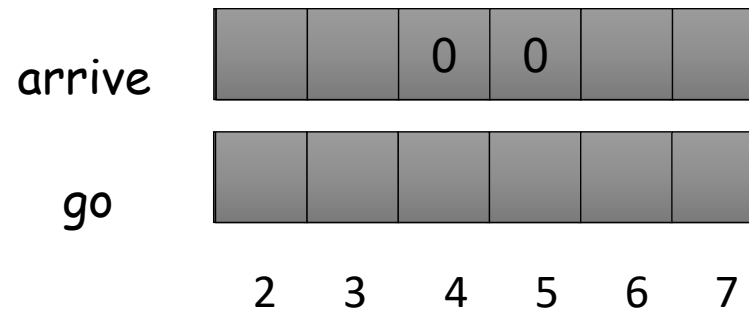
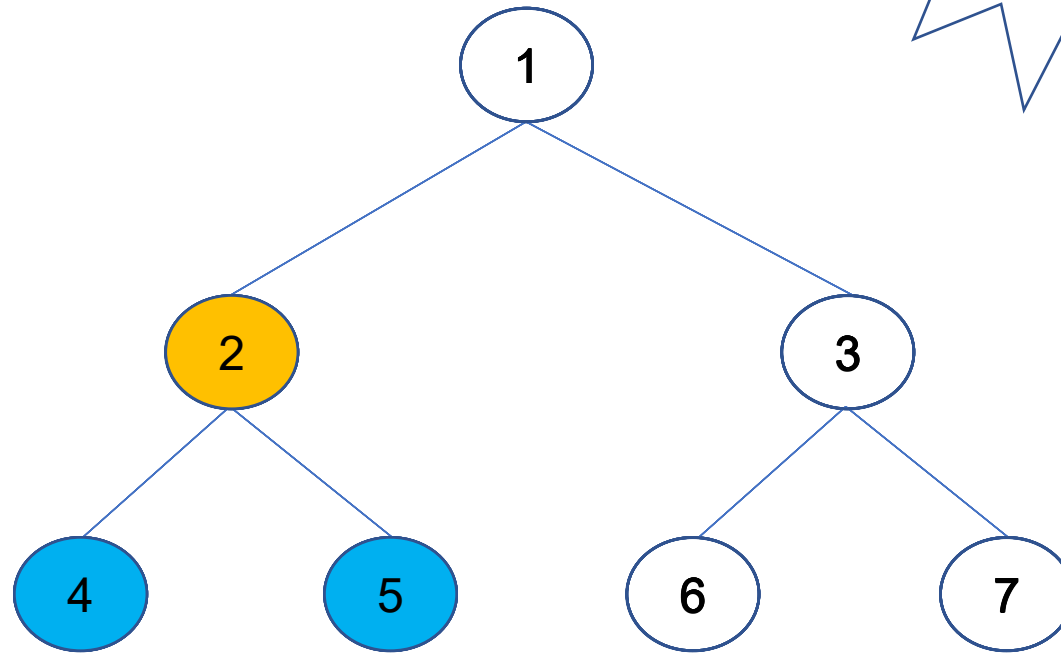
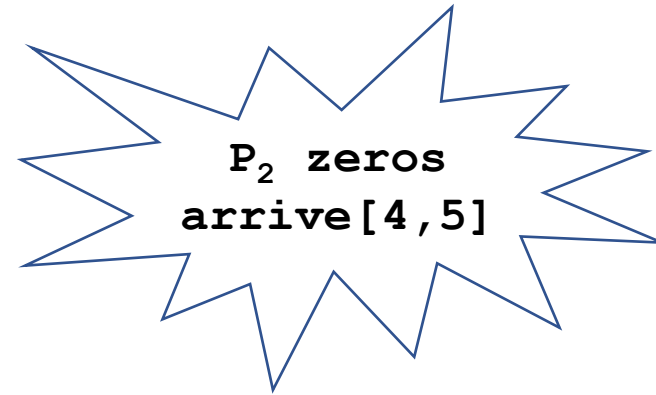
shared arrive[2..n]: array of atomic bits, initial values = 0
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2   await(arrive[2] = 1); arrive[2] := 0
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6   await(arrive[2i] = 1); arrive[2i] := 0
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8   arrive[i] := 1
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12  arrive[i] := 1
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14 fi
  
```



A Tree-based Barrier

Example Run for n=7 threads



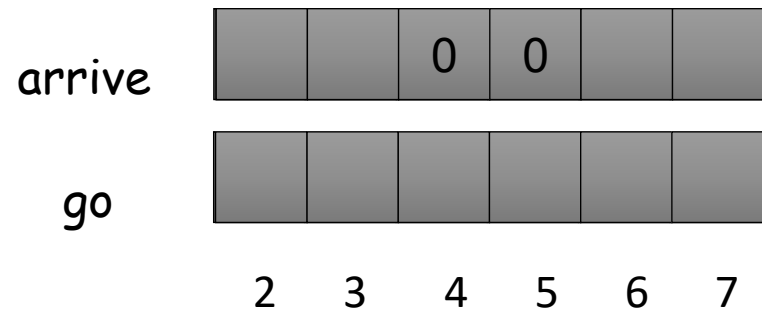
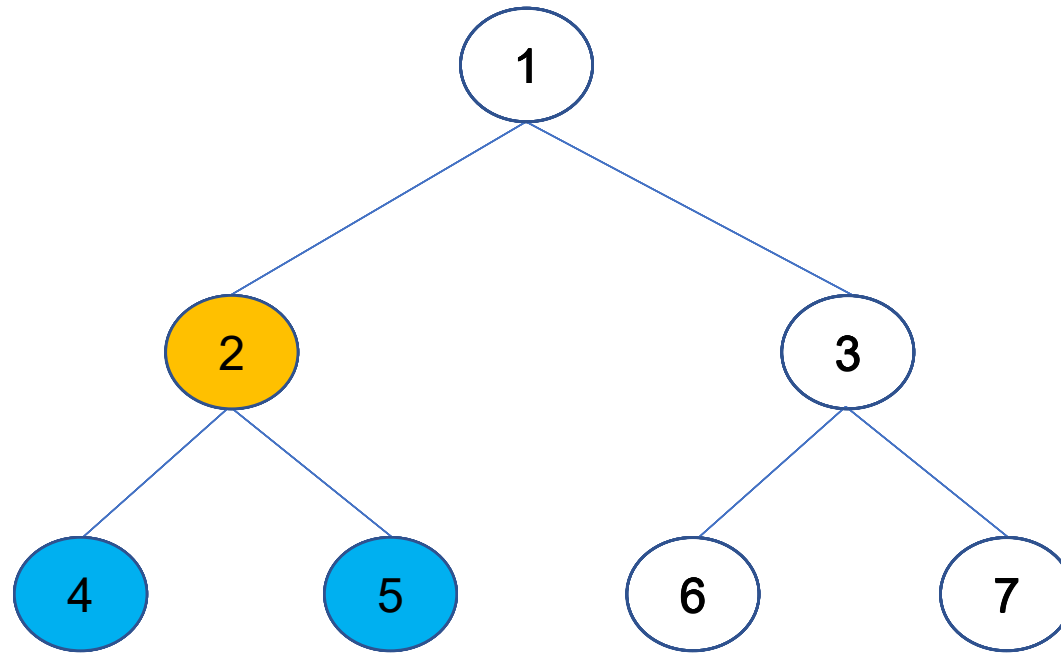
```

shared  arrive[2..n]: array of atomic bits, initial values = 0
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```

A Tree-based Barrier

Example Run for n=7 threads



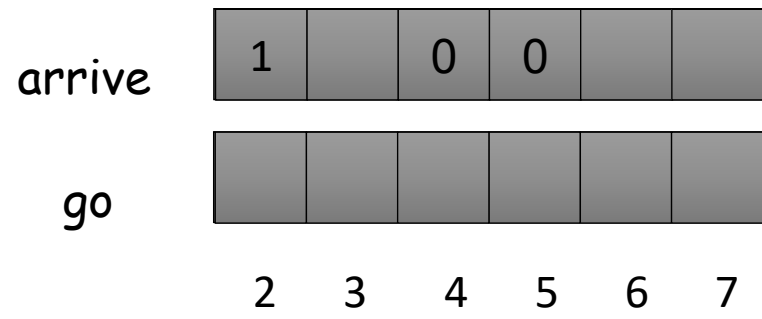
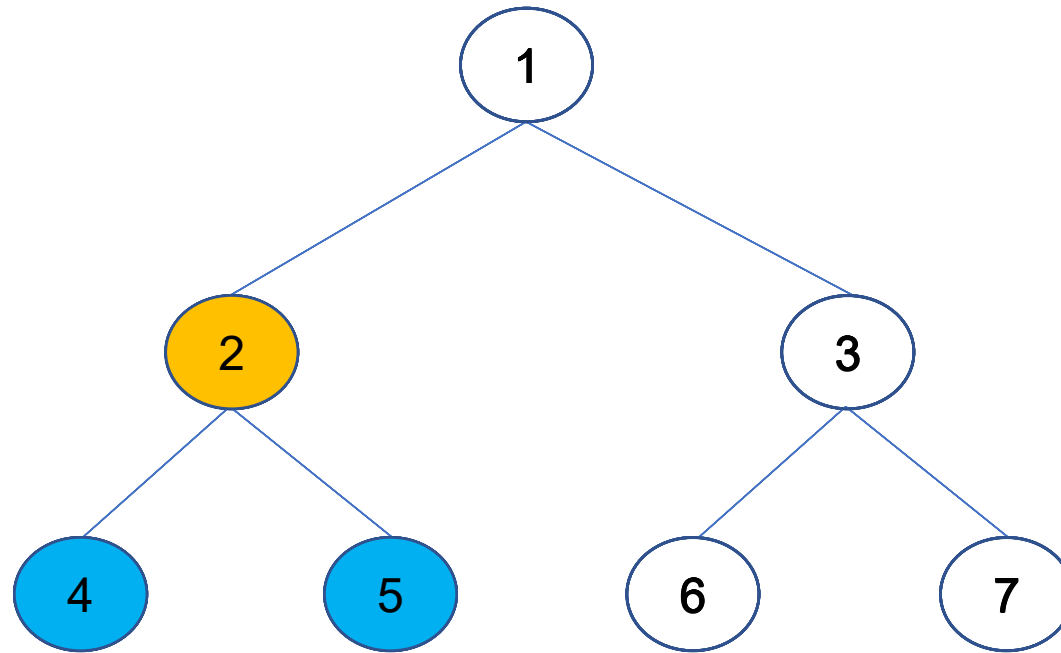
```

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A Tree-based Barrier

Example Run for n=7 threads



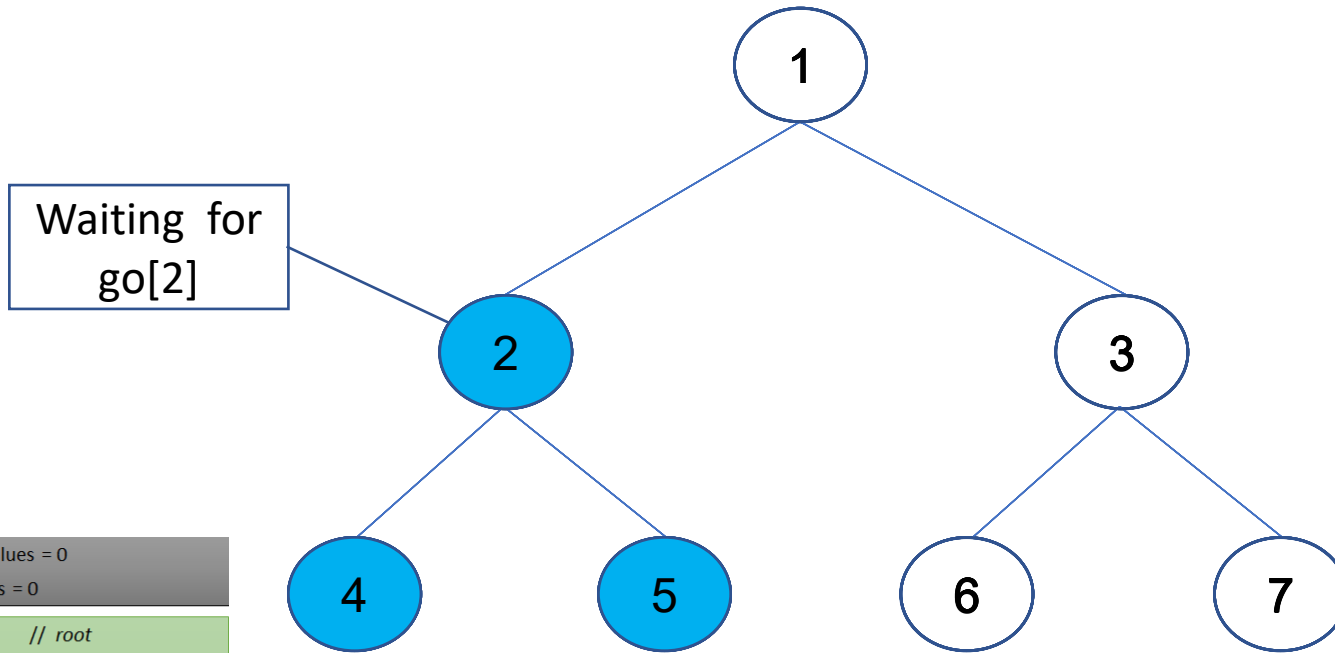
```

shared arrive[2..n]: array of atomic bits, initial values = 0
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A Tree-based Barrier

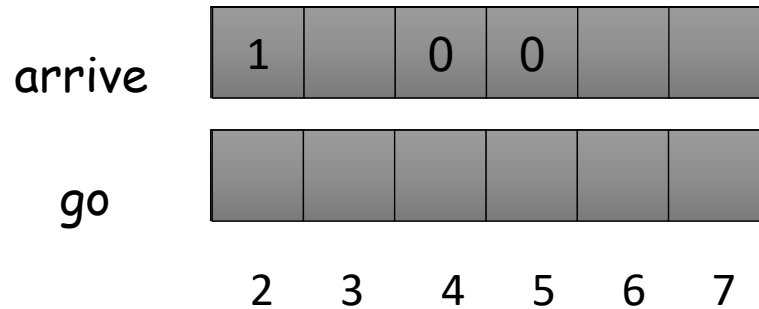
Example Run for n=7 threads



```

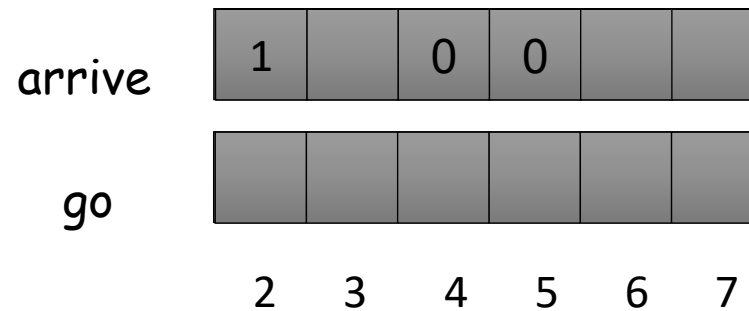
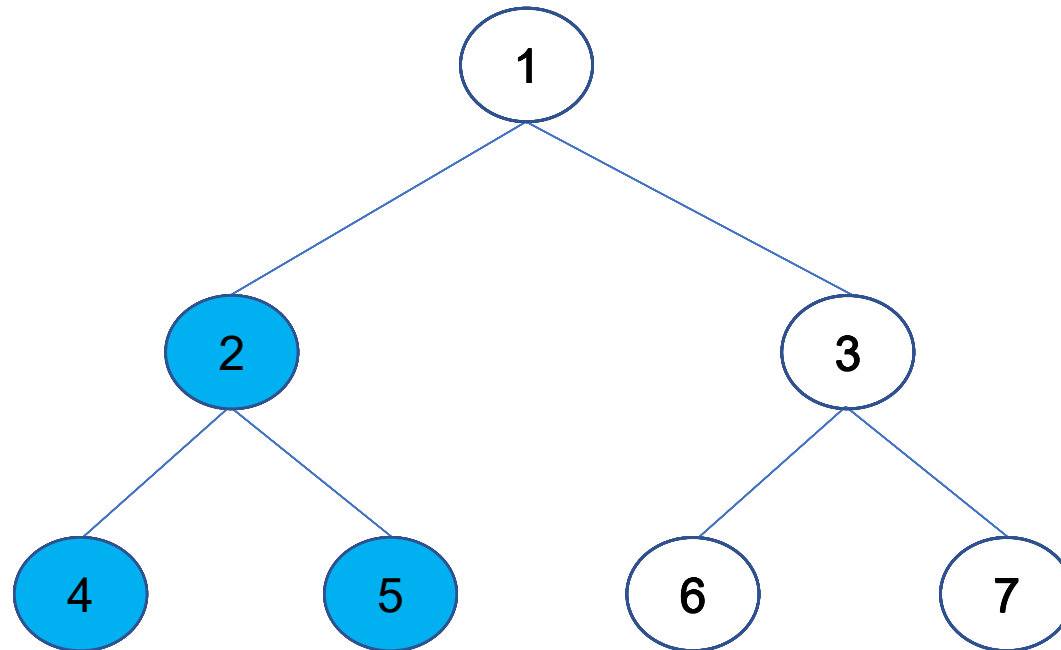
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A Tree-based Barrier

Example Run for n=7 threads



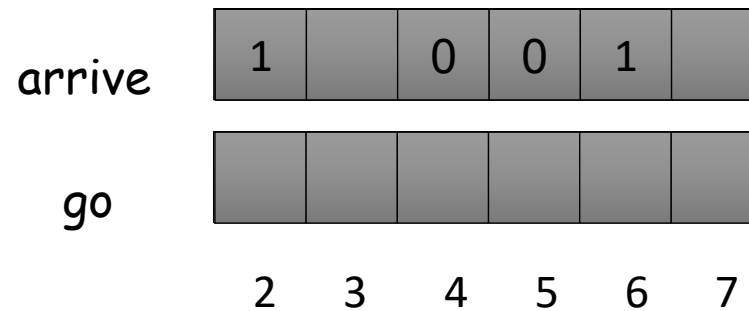
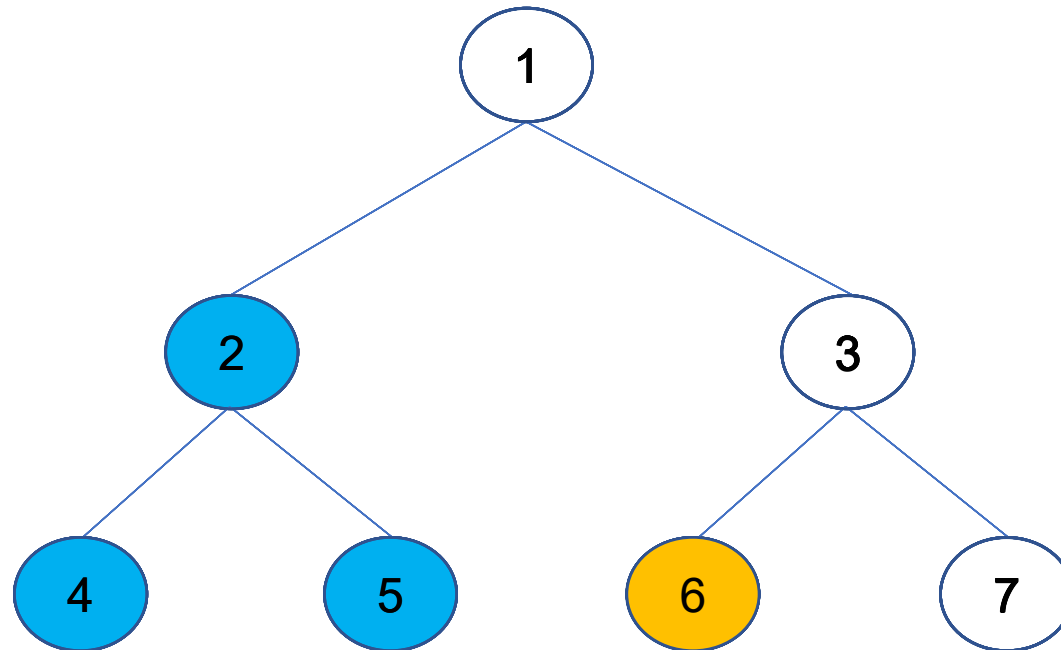
```

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shared go[2..n]: array of atomic bits, initial values = 0

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A Tree-based Barrier

Example Run for n=7 threads



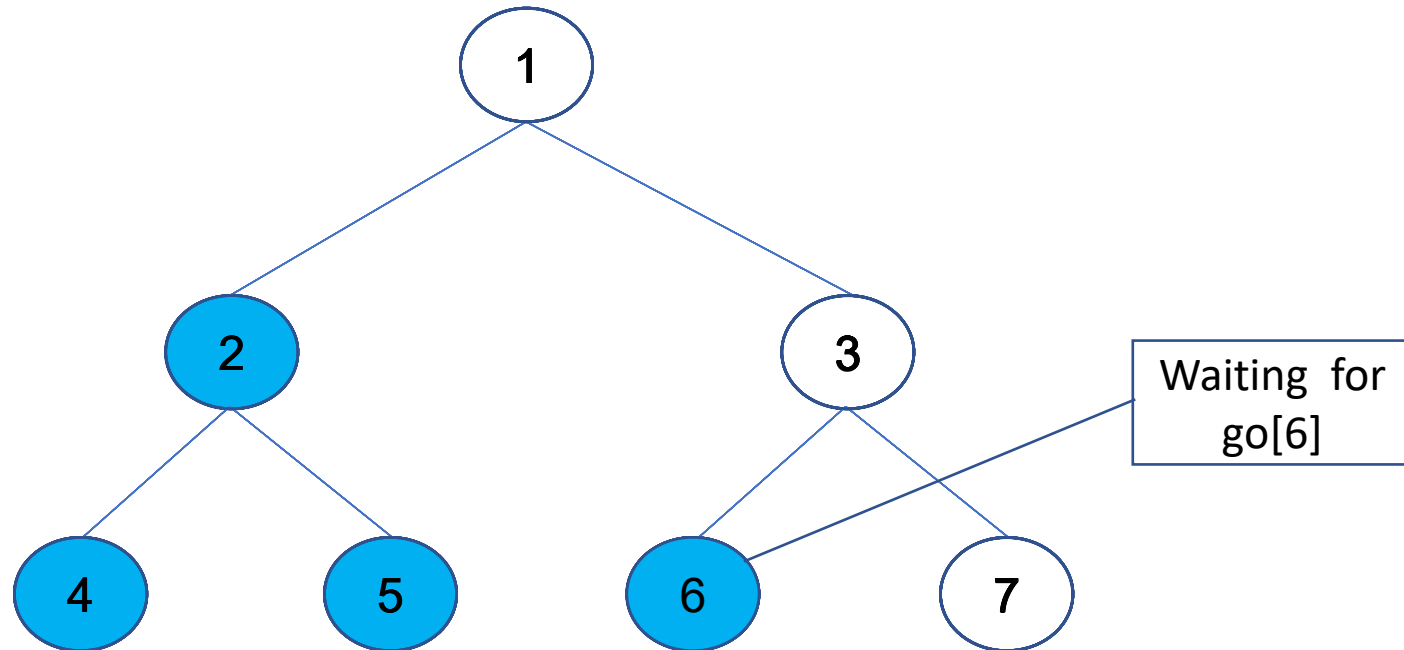
```

shared arrive[2..n]: array of atomic bits, initial values = 0
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A Tree-based Barrier

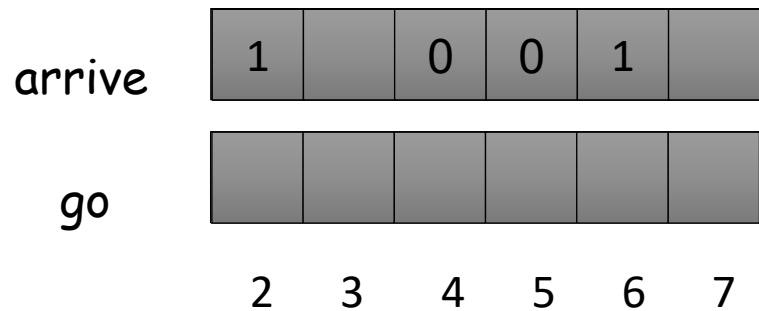
Example Run for n=7 threads



```

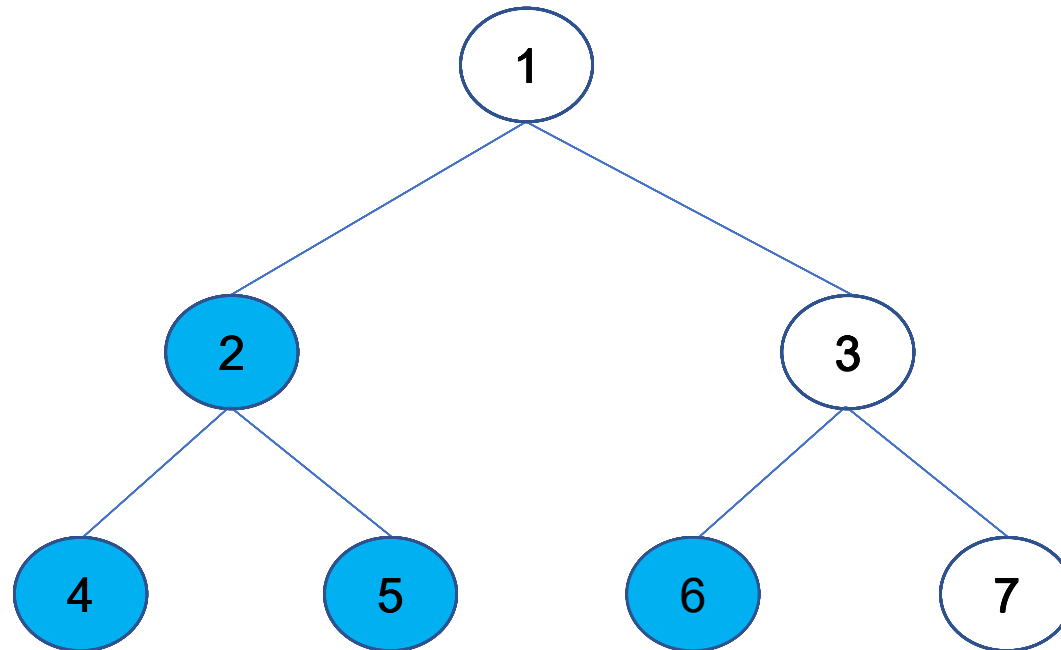
shared arrive[2..n]: array of atomic bits, initial values = 0
shared go[2..n]: array of atomic bits, initial values = 0

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5 else if i ≤ (n-1)/2 then // internal node
6   await(arrive[2i] = 1); arrive[2i] := 0
7   await(arrive[2i+1] = 1); arrive[2i+1] := 0
8   arrive[i] := 1
9   await(go[i] = 1); go[i] := 0
10  go[2i] = 1; go[2i+1] := 1
11 else // leaf
12  arrive[i] := 1
13  await(go[i] = 1); go[i] := 0 fi
14 fi
  
```



A Tree-based Barrier

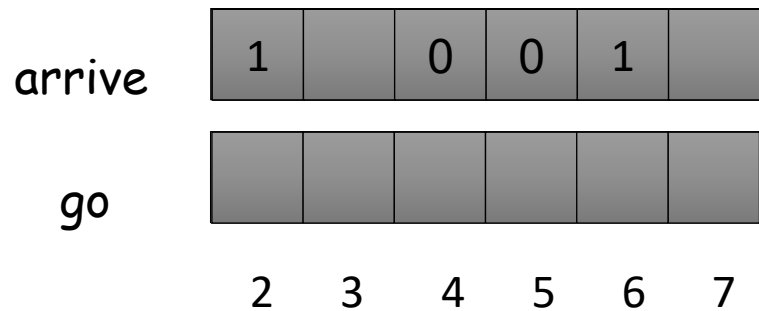
Example Run for n=7 threads



```

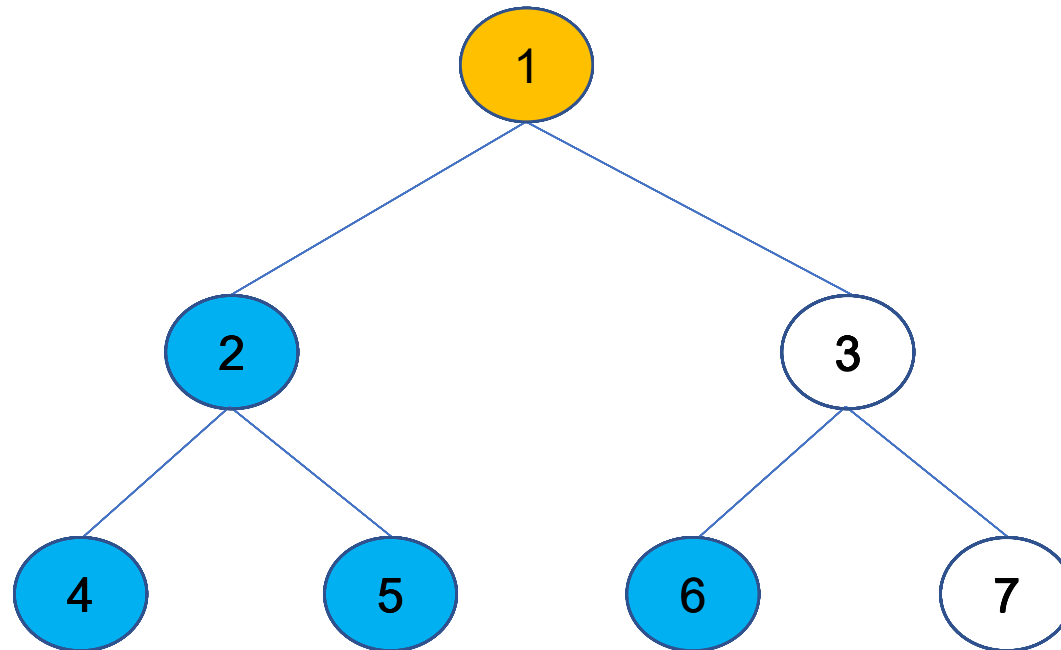
shared arrive[2..n]: array of atomic bits, initial values = 0
shared go[2..n]: array of atomic bits, initial values = 0

1 if i=1 then // root
2   await(arrive[2] = 1); arrive[2] := 0
3   await(arrive[3] = 1); arrive[3] := 0
4   go[2] = 1; go[3] = 1
5 else if i ≤ (n-1)/2 then // internal node
6   await(arrive[2i] = 1); arrive[2i] := 0
7   await(arrive[2i+1] = 1); arrive[2i+1] := 0
8   arrive[i] := 1
9   await(go[i] = 1); go[i] := 0
10  go[2i] = 1; go[2i+1] := 1
11 else // leaf
12  arrive[i] := 1
13  await(go[i] = 1); go[i] := 0 fi
14 fi
  
```



A Tree-based Barrier

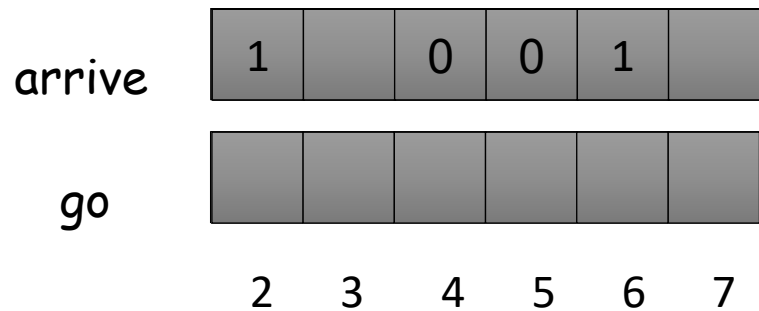
Example Run for n=7 threads



```

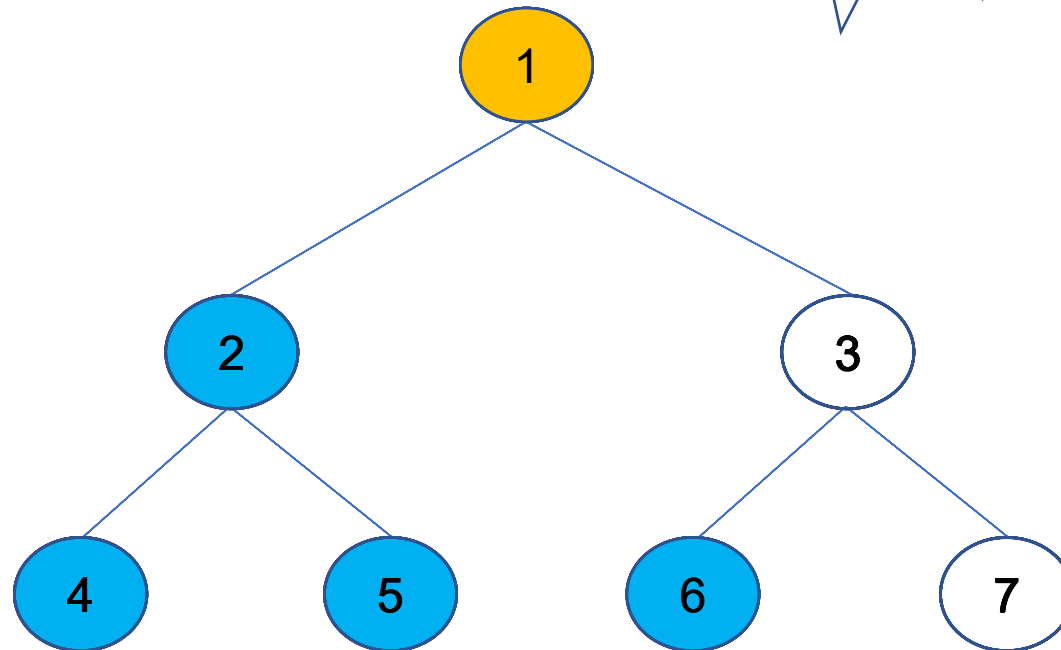
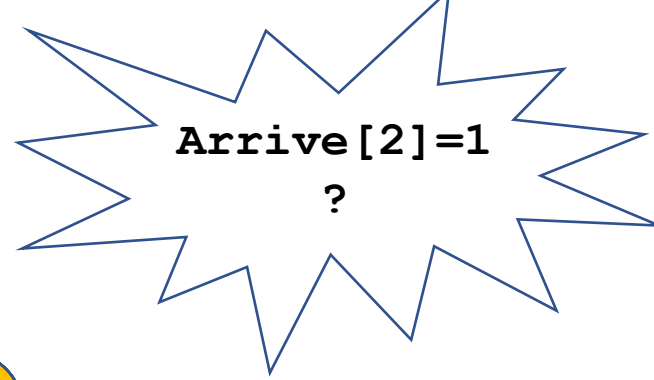
shared arrive[2..n]: array of atomic bits, initial values = 0
shared go[2..n]: array of atomic bits, initial values = 0

1  if i=1 then // root
2    await(arrive[2] = 1); arrive[2] := 0
3    await(arrive[3] = 1); arrive[3] := 0
4    go[2] = 1; go[3] = 1
5  else if i ≤ (n-1)/2 then // internal node
6    await(arrive[2i] = 1); arrive[2i] := 0
7    await(arrive[2i+1] = 1); arrive[2i+1] := 0
8    arrive[i] := 1
9    await(go[i] = 1); go[i] := 0
10   go[2i] = 1; go[2i+1] := 1
11 else // leaf
12   arrive[i] := 1
13   await(go[i] = 1); go[i] := 0 fi
14 fi
  
```



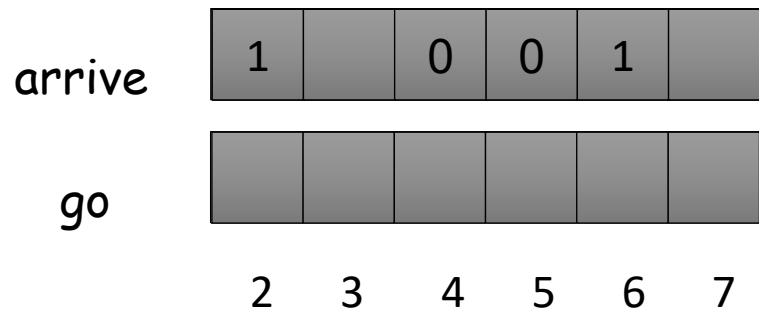
A Tree-based Barrier

Example Run for n=7 threads



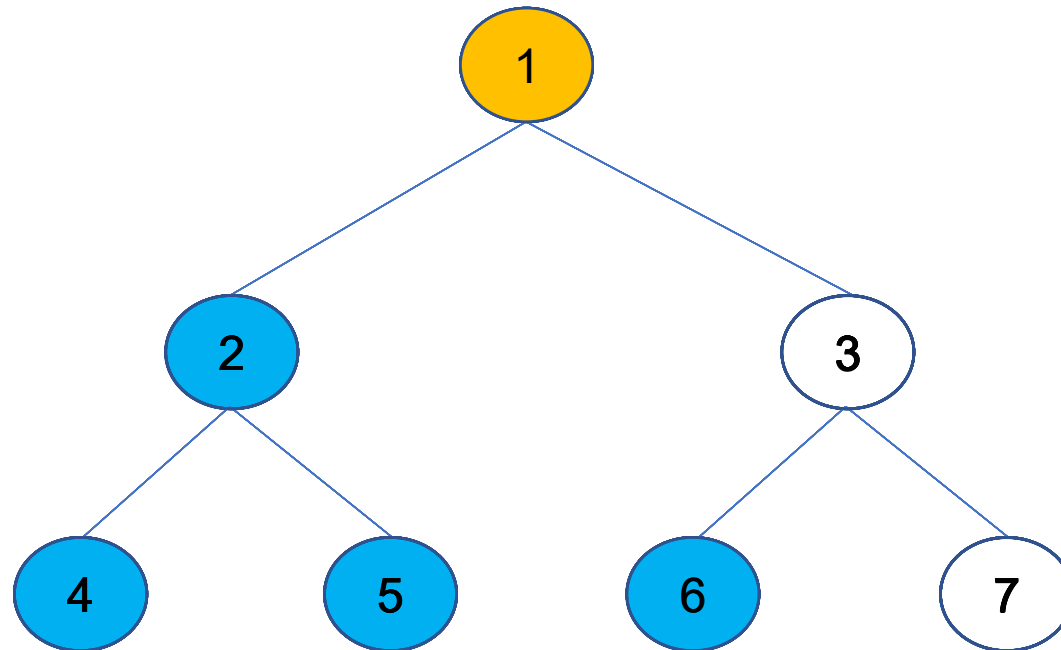
```
shared arrive[2..n]: array of atomic bits, initial values = 0
shared go[2..n]: array of atomic bits, initial values = 0
```

```
1 if i=1 then // root
2   await(arrive[2] = 1); arrive[2] := 0
3   await(arrive[3] = 1); arrive[3] := 0
4   go[2] = 1; go[3] = 1
5 else if i ≤ (n-1)/2 then // internal node
6   await(arrive[2i] = 1); arrive[2i] := 0
7   await(arrive[2i+1] = 1); arrive[2i+1] := 0
8   arrive[i] := 1
9   await(go[i] = 1); go[i] := 0
10  go[2i] = 1; go[2i+1] := 1
11 else // leaf
12  arrive[i] := 1
13  await(go[i] = 1); go[i] := 0 fi
14 fi
```



A Tree-based Barrier

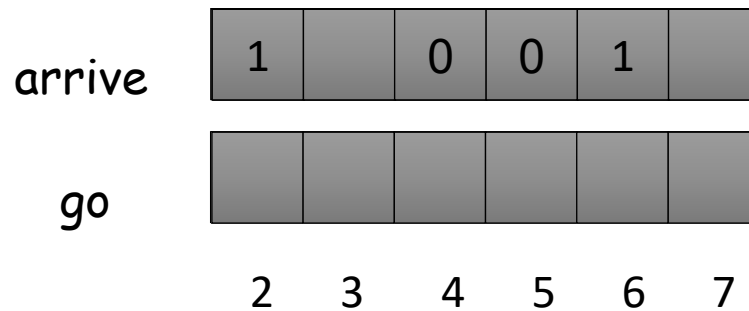
Example Run for n=7 threads



```

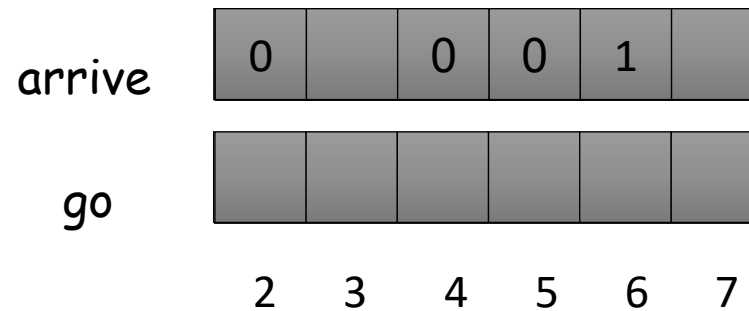
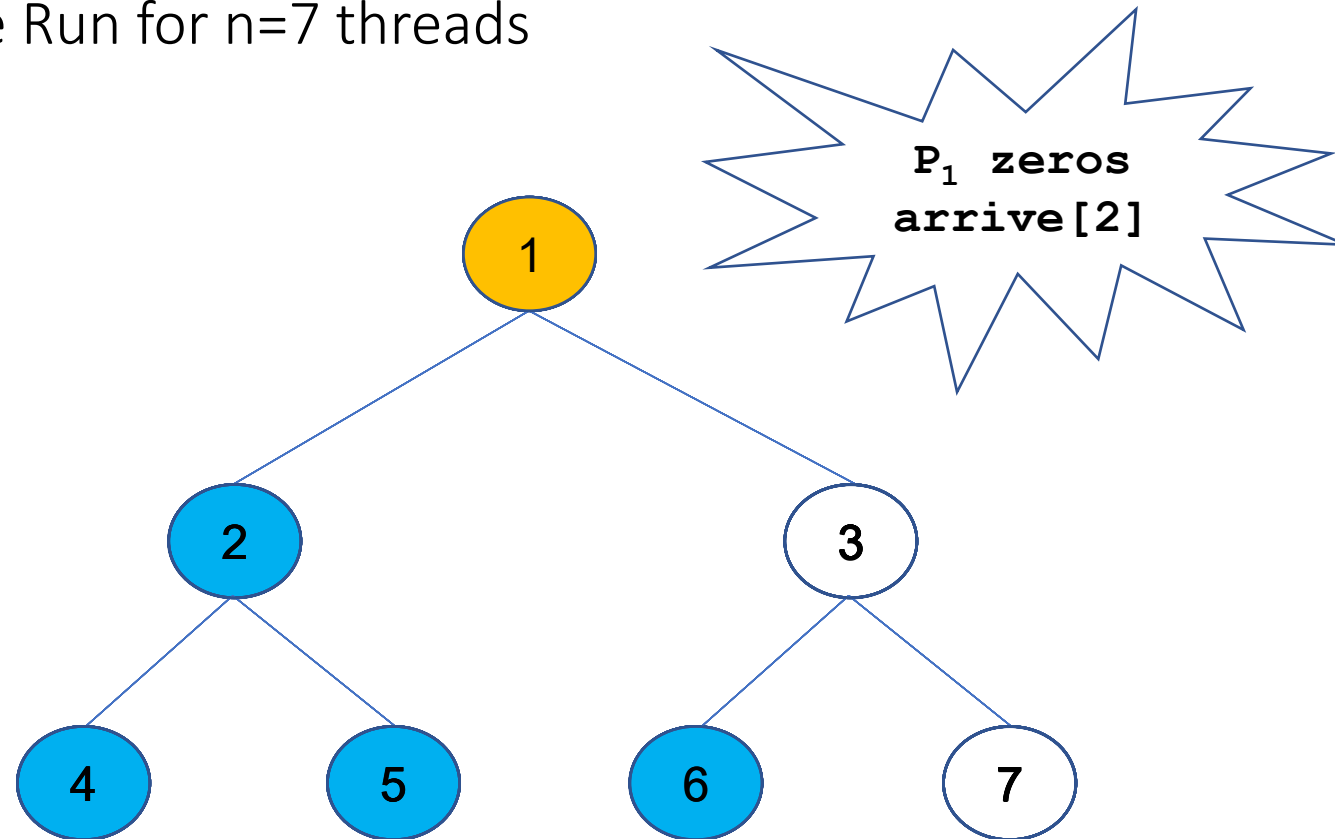
shared arrive[2..n]: array of atomic bits, initial values = 0
shared go[2..n]: array of atomic bits, initial values = 0

1 if i=1 then // root
2   await(arrive[2] = 1); arrive[2] := 0
3   await(arrive[3] = 1); arrive[3] := 0
4   go[2] = 1; go[3] = 1
5 else if i ≤ (n-1)/2 then // internal node
6   await(arrive[2i] = 1); arrive[2i] := 0
7   await(arrive[2i+1] = 1); arrive[2i+1] := 0
8   arrive[i] := 1
9   await(go[i] = 1); go[i] := 0
10  go[2i] = 1; go[2i+1] := 1
11 else // leaf
12  arrive[i] := 1
13  await(go[i] = 1); go[i] := 0 fi
14 fi
  
```



A Tree-based Barrier

Example Run for n=7 threads



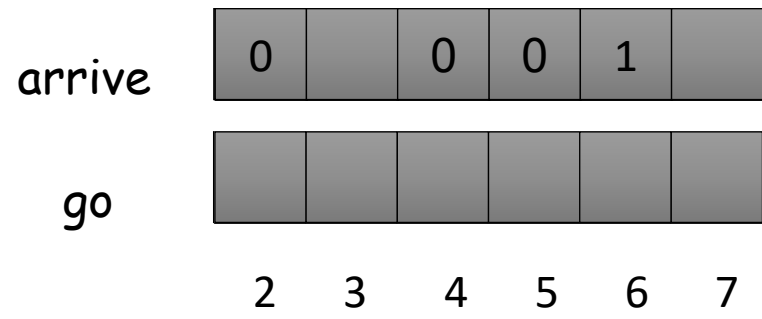
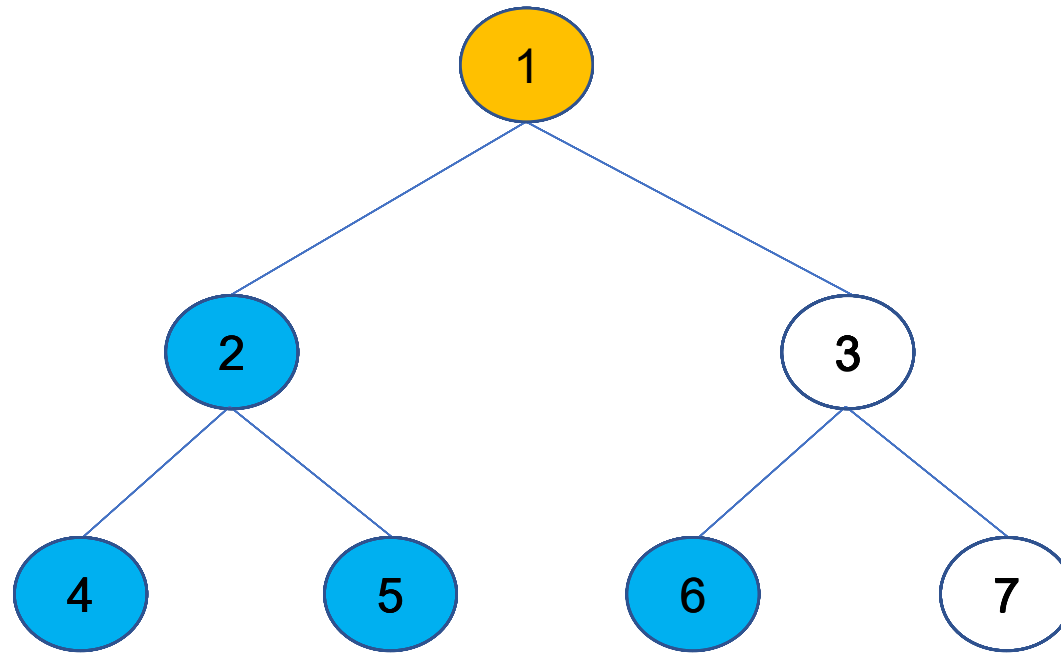
```

shared  arrive[2..n]: array of atomic bits, initial values = 0
        go[2..n]: array of atomic bits, initial values = 0

1  if i=1 then // root
2      await(arrive[2] = 1); arrive[2] := 0
3      await(arrive[3] = 1); arrive[3] := 0
4      go[2] = 1; go[3] = 1
5  else if i ≤ (n-1)/2 then // internal node
6      await(arrive[2i] = 1); arrive[2i] := 0
7      await(arrive[2i+1] = 1); arrive[2i+1] := 0
8      arrive[i] := 1
9      await(go[i] = 1); go[i] := 0
10     go[2i] = 1; go[2i+1] := 1
11 else // leaf
12     arrive[i] := 1
13     await(go[i] = 1); go[i] := 0 fi
14 fi
    
```

A Tree-based Barrier

Example Run for n=7 threads



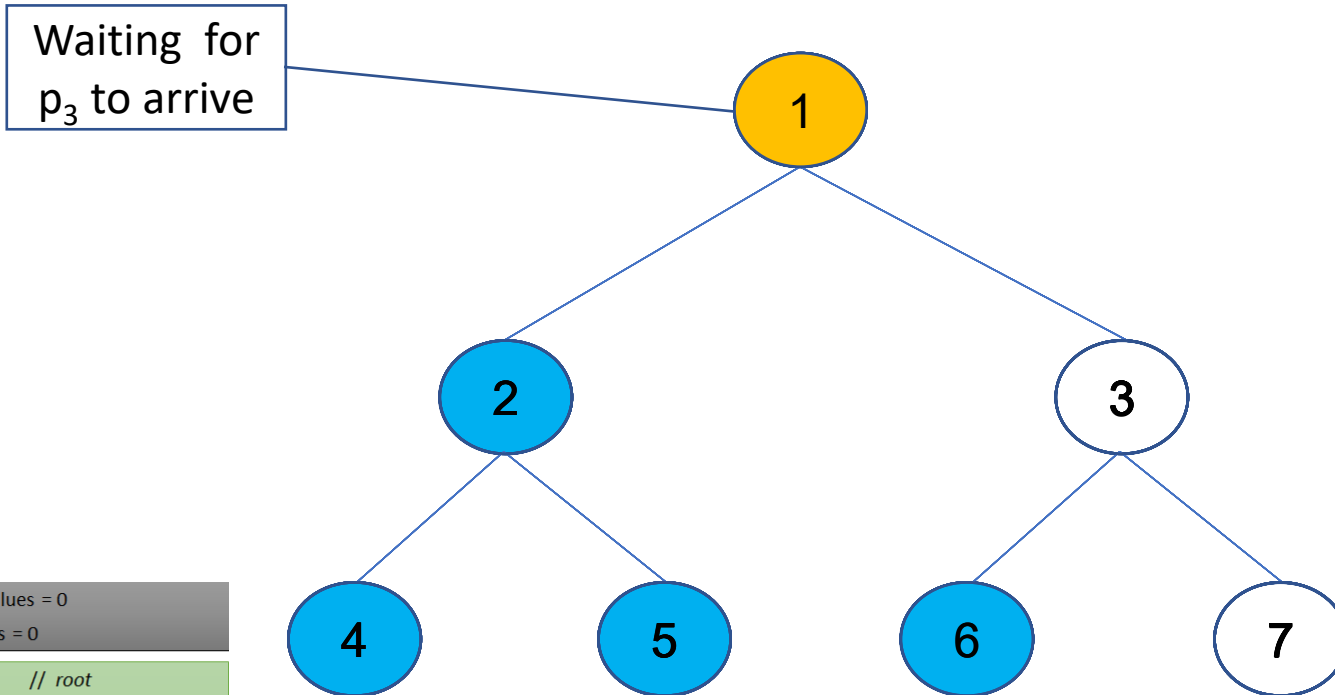
```

shared arrive[2..n]: array of atomic bits, initial values = 0
shared go[2..n]: array of atomic bits, initial values = 0

1 if i=1 then // root
2   await(arrive[2] = 1); arrive[2] := 0
3   await(arrive[3] = 1); arrive[3] := 0
4   go[2] = 1; go[3] = 1
5 else if i ≤ (n-1)/2 then // internal node
6   await(arrive[2i] = 1); arrive[2i] := 0
7   await(arrive[2i+1] = 1); arrive[2i+1] := 0
8   arrive[i] := 1
9   await(go[i] = 1); go[i] := 0
10  go[2i] = 1; go[2i+1] := 1
11 else // leaf
12  arrive[i] := 1
13  await(go[i] = 1); go[i] := 0 fi
14 fi
  
```


A Tree-based Barrier

Example Run for n=7 threads

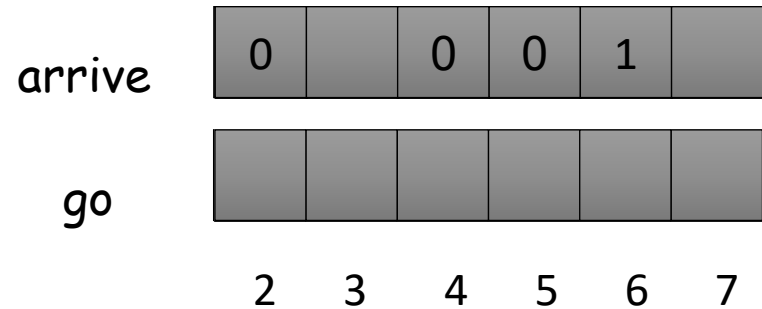


Waiting for p_3 to arrive

```

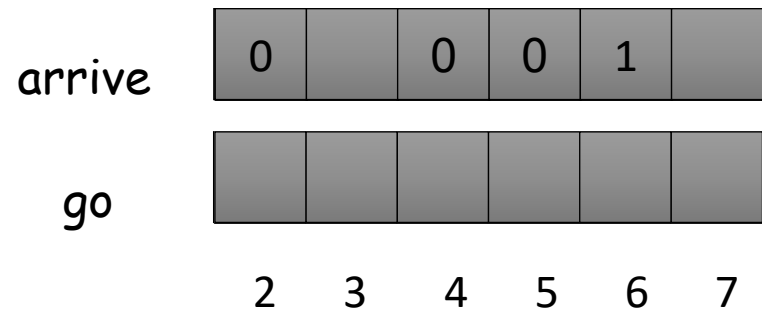
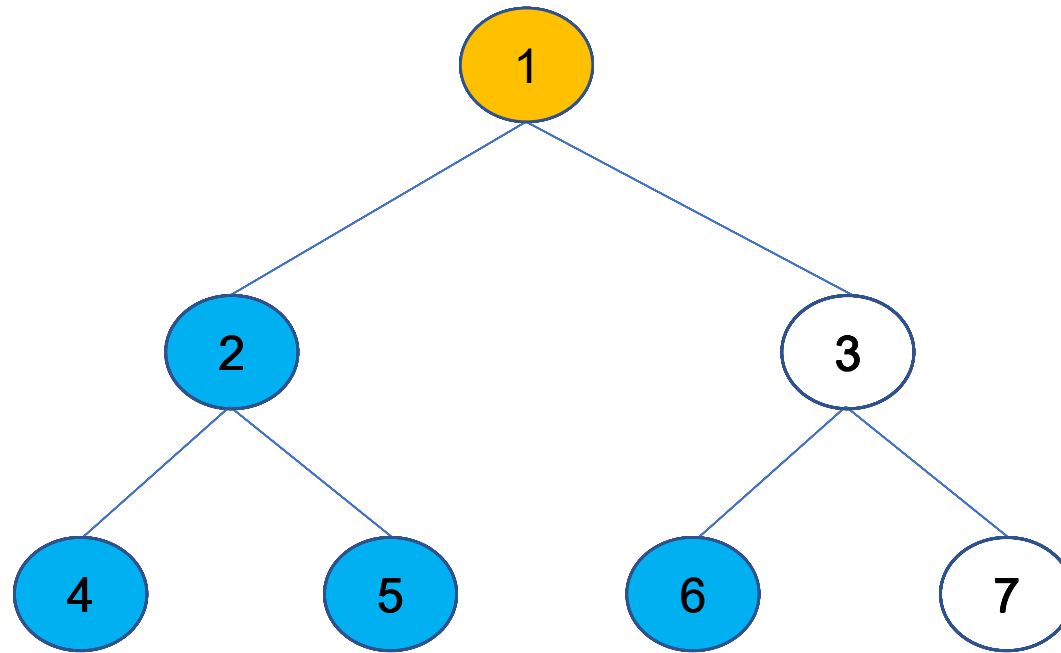
shared arrive[2..n]: array of atomic bits, initial values = 0
shared go[2..n]: array of atomic bits, initial values = 0

1  if i=1 then // root
2    await(arrive[2] = 1); arrive[2] := 0
3    await(arrive[3] = 1); arrive[3] := 0
4    go[2] = 1; go[3] = 1
5  else if i ≤ (n-1)/2 then // internal node
6    await(arrive[2i] = 1); arrive[2i] := 0
7    await(arrive[2i+1] = 1); arrive[2i+1] := 0
8    arrive[i] := 1
9    await(go[i] = 1); go[i] := 0
10   go[2i] = 1; go[2i+1] := 1
11 else // leaf
12   arrive[i] := 1
13   await(go[i] = 1); go[i] := 0 fi
14 fi
  
```



A Tree-based Barrier

Example Run for n=7 threads



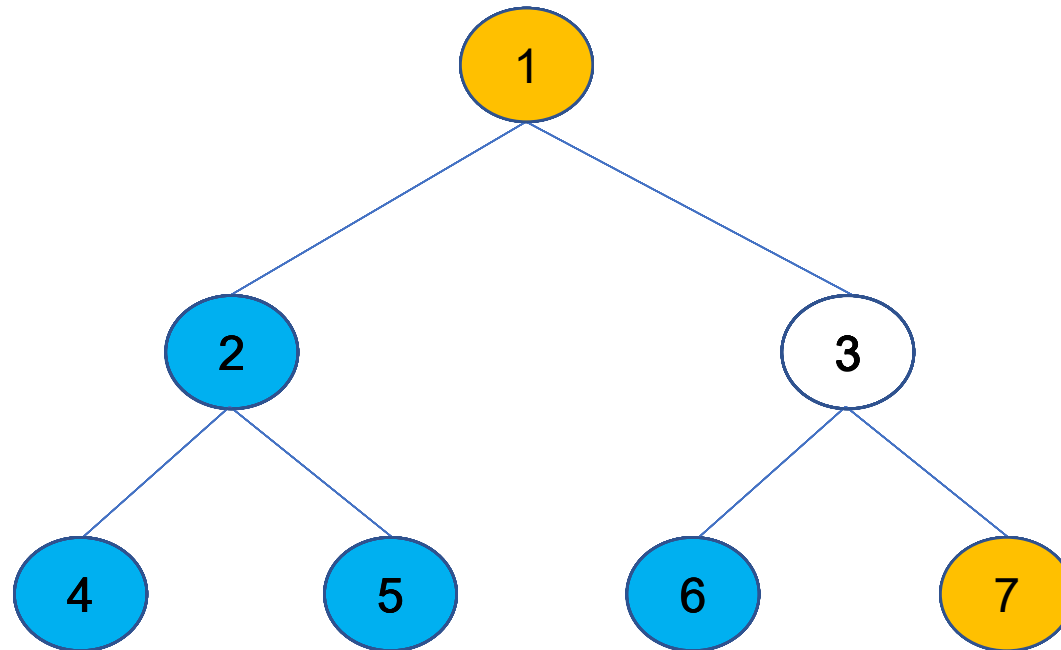
```

shared arrive[2..n]: array of atomic bits, initial values = 0
shared go[2..n]: array of atomic bits, initial values = 0

1 if i=1 then // root
2   await(arrive[2] = 1); arrive[2] := 0
3   await(arrive[3] = 1); arrive[3] := 0
4   go[2] = 1; go[3] = 1
5 else if i ≤ (n-1)/2 then // internal node
6   await(arrive[2i] = 1); arrive[2i] := 0
7   await(arrive[2i+1] = 1); arrive[2i+1] := 0
8   arrive[i] := 1
9   await(go[i] = 1); go[i] := 0
10  go[2i] = 1; go[2i+1] := 1
11 else // leaf
12  arrive[i] := 1
13  await(go[i] = 1); go[i] := 0 fi
14 fi
  
```

A Tree-based Barrier

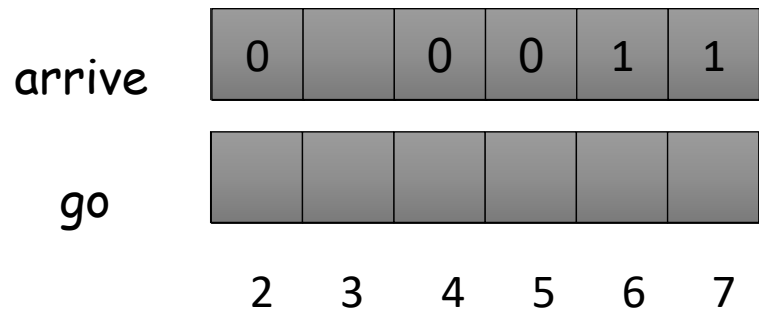
Example Run for n=7 threads



```

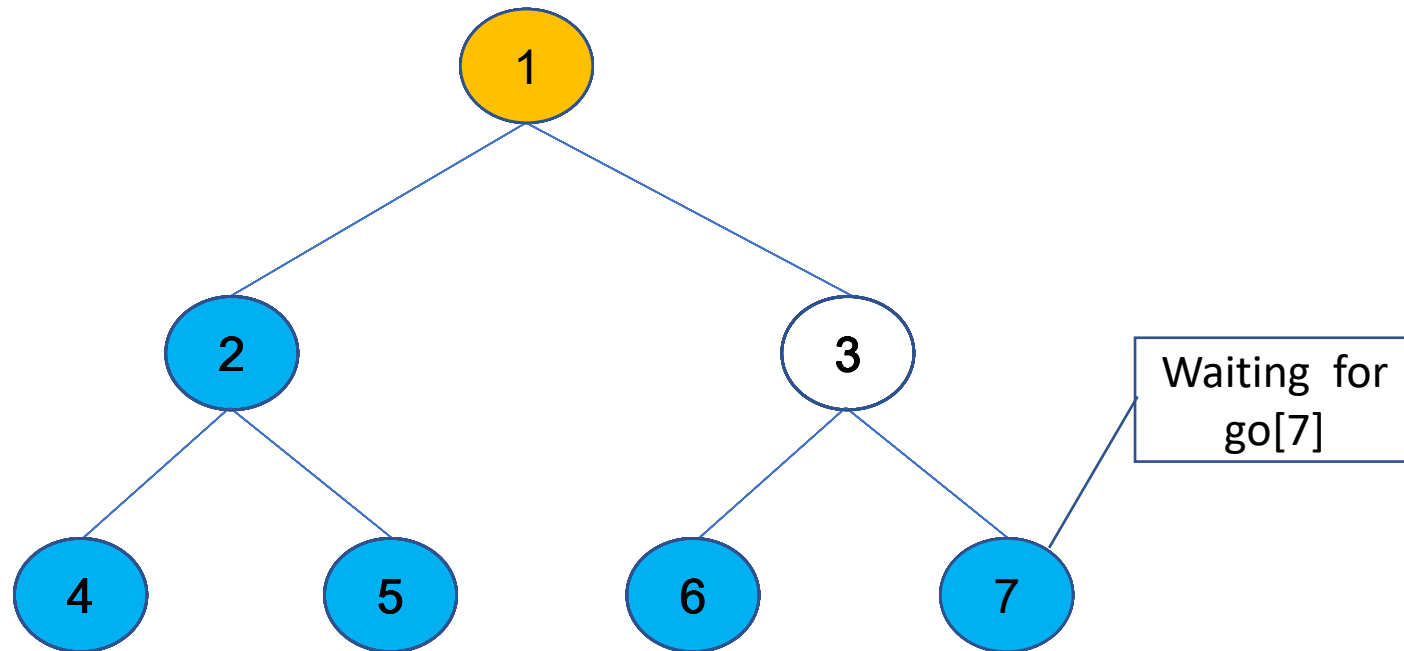
shared arrive[2..n]: array of atomic bits, initial values = 0
shared go[2..n]: array of atomic bits, initial values = 0

1 if i=1 then // root
2   await(arrive[2] = 1); arrive[2] := 0
3   await(arrive[3] = 1); arrive[3] := 0
4   go[2] = 1; go[3] = 1
5 else if i ≤ (n-1)/2 then // internal node
6   await(arrive[2i] = 1); arrive[2i] := 0
7   await(arrive[2i+1] = 1); arrive[2i+1] := 0
8   arrive[i] := 1
9   await(go[i] = 1); go[i] := 0
10  go[2i] = 1; go[2i+1] := 1
11 else // leaf
12  arrive[i] := 1
13  await(go[i] = 1); go[i] := 0 fi
14 fi
  
```



A Tree-based Barrier

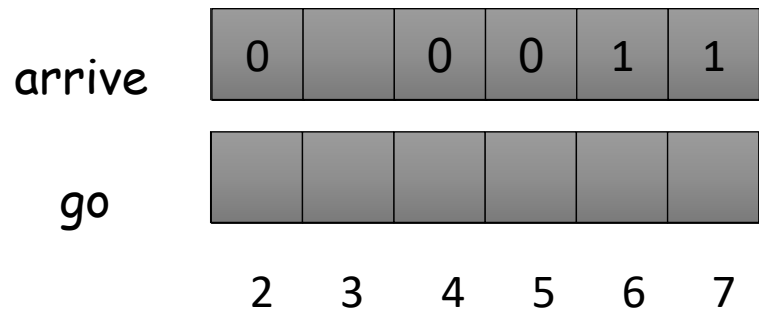
Example Run for n=7 threads



```

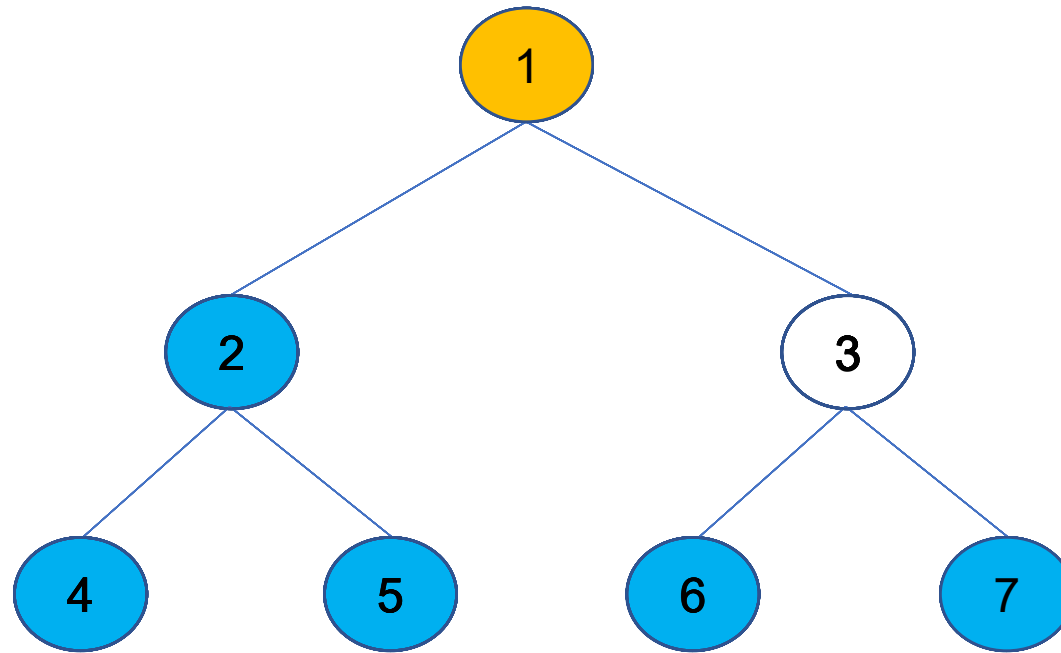
shared  arrive[2..n]: array of atomic bits, initial values = 0
        go[2..n]: array of atomic bits, initial values = 0

1  if i=1 then // root
2    await(arrive[2] = 1); arrive[2] := 0
3    await(arrive[3] = 1); arrive[3] := 0
4    go[2] = 1; go[3] = 1
5  else if i ≤ (n-1)/2 then // internal node
6    await(arrive[2i] = 1); arrive[2i] := 0
7    await(arrive[2i+1] = 1); arrive[2i+1] := 0
8    arrive[i] := 1
9    await(go[i] = 1); go[i] := 0
10   go[2i] = 1; go[2i+1] := 1
11 else // leaf
12   arrive[i] := 1
13   await(go[i] = 1); go[i] := 0 fi
14 fi
  
```



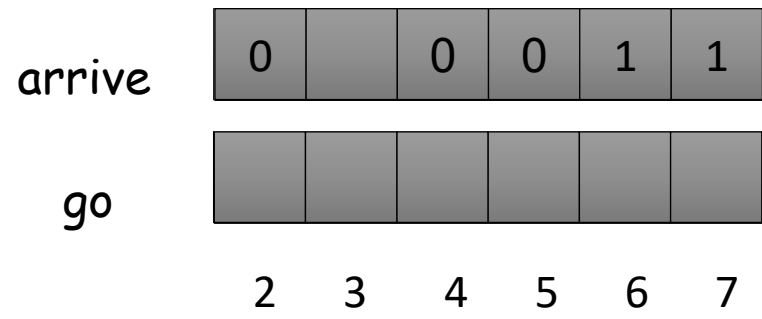
A Tree-based Barrier

Example Run for n=7 threads



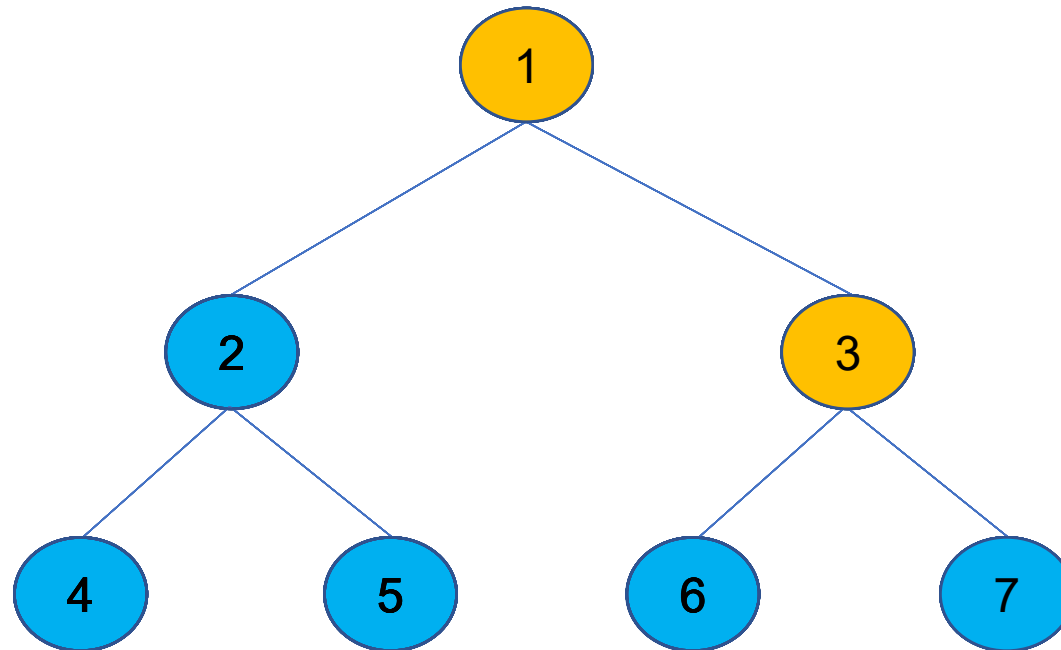
```
shared arrive[2..n]: array of atomic bits, initial values = 0
shared go[2..n]: array of atomic bits, initial values = 0
```

```
1 if i=1 then // root
2   await(arrive[2] = 1); arrive[2] := 0
3   await(arrive[3] = 1); arrive[3] := 0
4   go[2] = 1; go[3] = 1
5 else if i ≤ (n-1)/2 then // internal node
6   await(arrive[2i] = 1); arrive[2i] := 0
7   await(arrive[2i+1] = 1); arrive[2i+1] := 0
8   arrive[i] := 1
9   await(go[i] = 1); go[i] := 0
10  go[2i] = 1; go[2i+1] := 1
11 else // leaf
12  arrive[i] := 1
13  await(go[i] = 1); go[i] := 0 fi
14 fi
```



A Tree-based Barrier

Example Run for n=7 threads

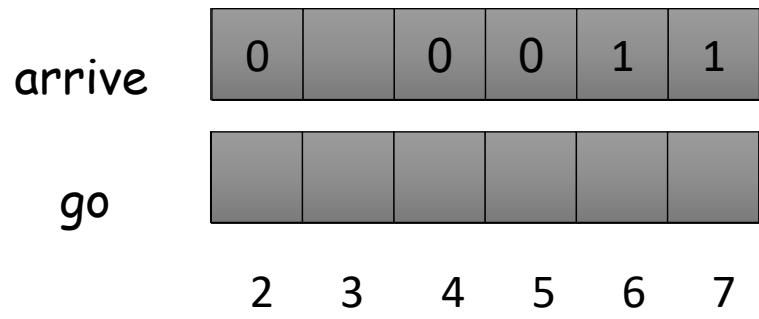


```

shared arrive[2..n]: array of atomic bits, initial values = 0
shared go[2..n]: array of atomic bits, initial values = 0
  
```

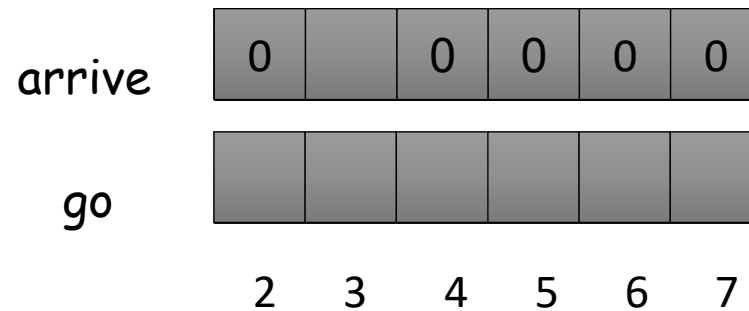
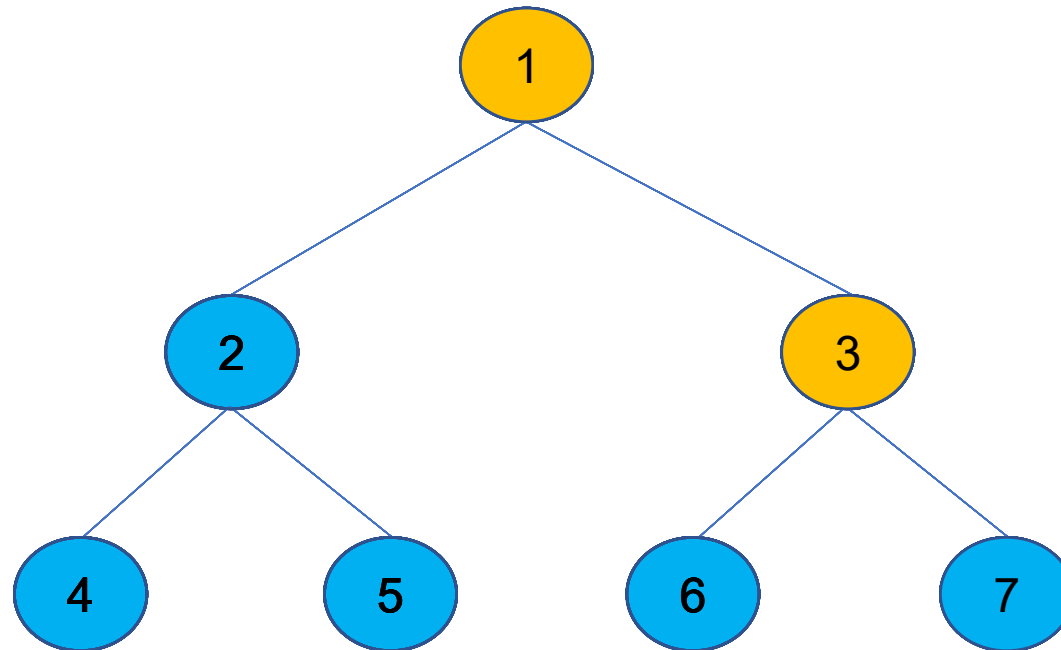
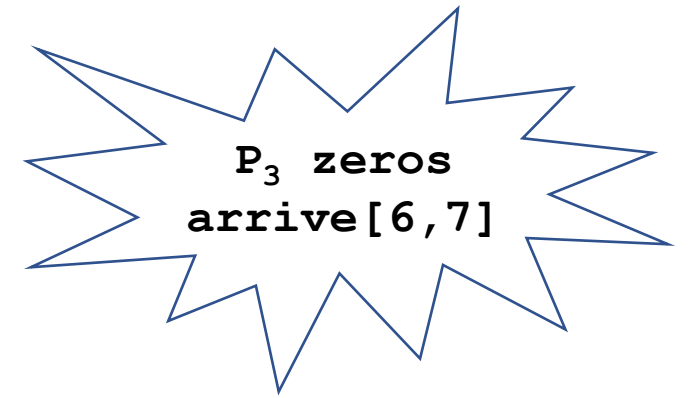
```

1  if i=1 then // root
2    await(arrive[2] = 1); arrive[2] := 0
3    await(arrive[3] = 1); arrive[3] := 0
4    go[2] = 1; go[3] = 1
5  else if i ≤ (n-1)/2 then // internal node
6    await(arrive[2i] = 1); arrive[2i] := 0
7    await(arrive[2i+1] = 1); arrive[2i+1] := 0
8    arrive[i] := 1
9    await(go[i] = 1); go[i] := 0
10   go[2i] = 1; go[2i+1] := 1
11 else // leaf
12   arrive[i] := 1
13   await(go[i] = 1); go[i] := 0 fi
14 fi
  
```



A Tree-based Barrier

Example Run for n=7 threads



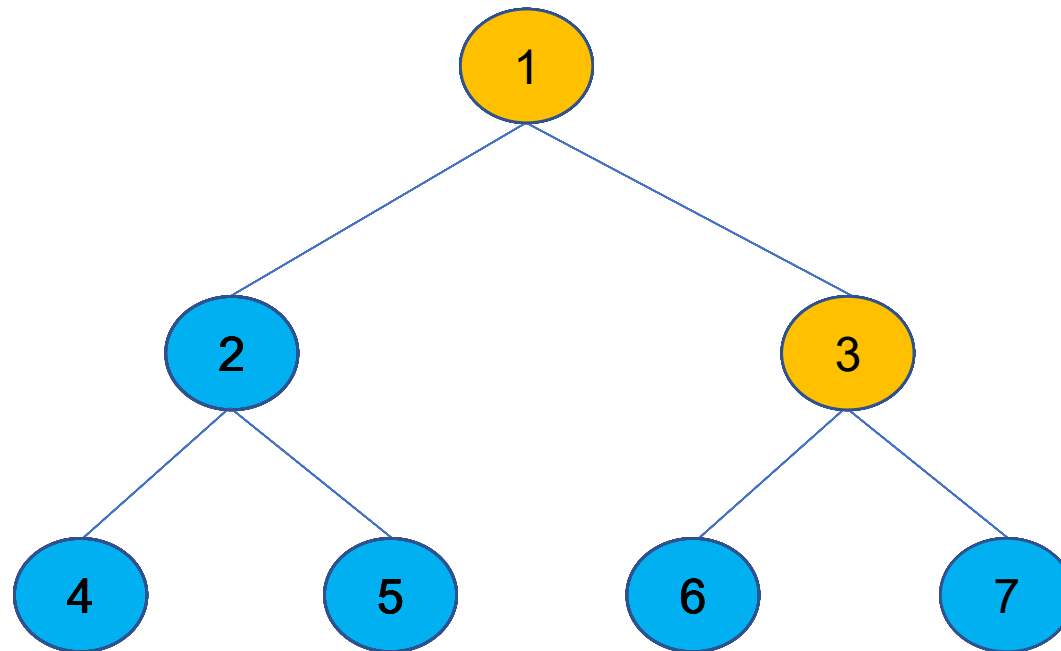
```

shared  arrive[2..n]: array of atomic bits, initial values = 0
        go[2..n]: array of atomic bits, initial values = 0

1  if i=1 then // root
2      await(arrive[2] = 1); arrive[2] := 0
3      await(arrive[3] = 1); arrive[3] := 0
4      go[2] = 1; go[3] = 1
5  else if i ≤ (n-1)/2 then // internal node
6      await(arrive[2i] = 1); arrive[2i] := 0
7      await(arrive[2i+1] = 1); arrive[2i+1] := 0
8      arrive[i] := 1
9      await(go[i] = 1); go[i] := 0
10     go[2i] = 1; go[2i+1] := 1
11 else // leaf
12     arrive[i] := 1
13     await(go[i] = 1); go[i] := 0 fi
14 fi
    
```

A Tree-based Barrier

Example Run for n=7 threads

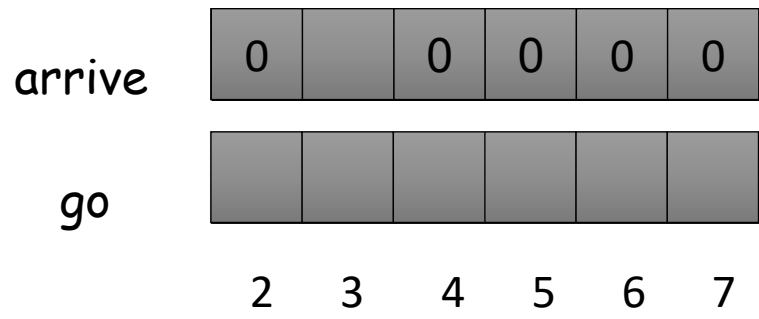


```

shared arrive[2..n]: array of atomic bits, initial values = 0
shared go[2..n]: array of atomic bits, initial values = 0
  
```

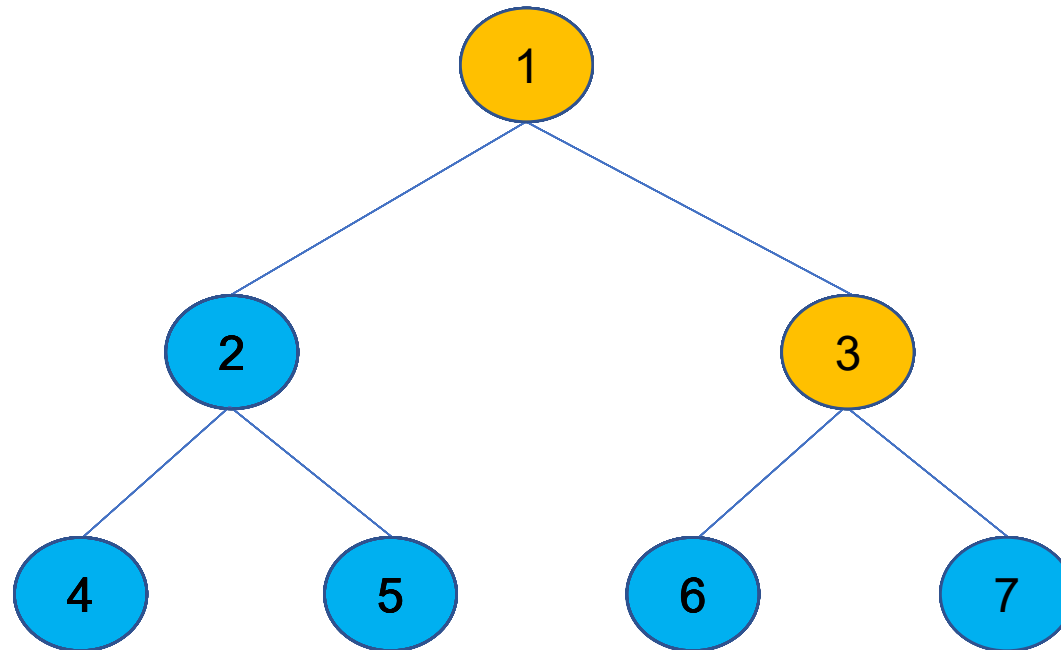
```

1  if i=1 then // root
2    await(arrive[2] = 1); arrive[2] := 0
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4    go[2] = 1; go[3] = 1
5  else if i ≤ (n-1)/2 then // internal node
6    await(arrive[2i] = 1); arrive[2i] := 0
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8    arrive[i] := 1
9    await(go[i] = 1); go[i] := 0
10   go[2i] = 1; go[2i+1] := 1
11 else // leaf
12   arrive[i] := 1
13   await(go[i] = 1); go[i] := 0 fi
14 fi
  
```



A Tree-based Barrier

Example Run for n=7 threads

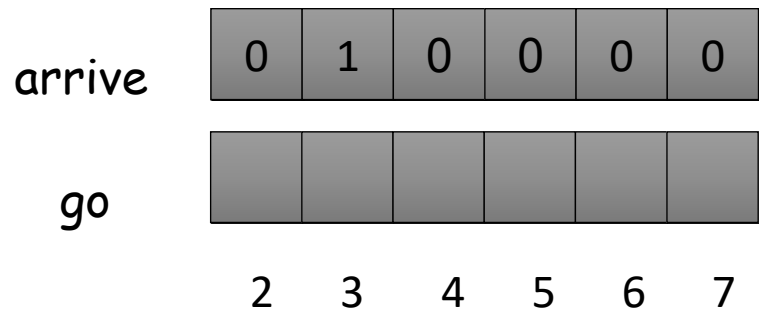


```

shared arrive[2..n]: array of atomic bits, initial values = 0
shared go[2..n]: array of atomic bits, initial values = 0
  
```

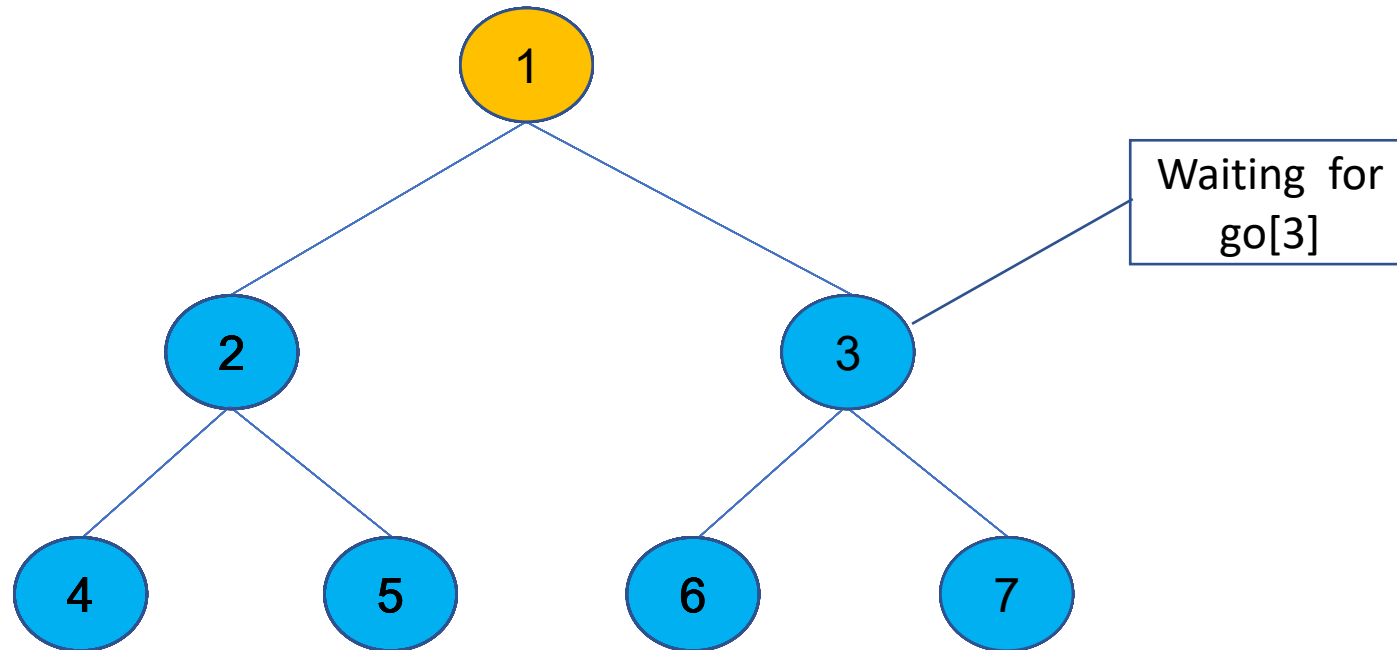
```

1 if i=1 then // root
2   await(arrive[2] = 1); arrive[2] := 0
3   await(arrive[3] = 1); arrive[3] := 0
4   go[2] = 1; go[3] = 1
5 else if i ≤ (n-1)/2 then // internal node
6   await(arrive[2i] = 1); arrive[2i] := 0
7   await(arrive[2i+1] = 1); arrive[2i+1] := 0
8   arrive[i] := 1
9   await(go[i] = 1); go[i] := 0
10  go[2i] = 1; go[2i+1] := 1
11 else // leaf
12  arrive[i] := 1
13  await(go[i] = 1); go[i] := 0 fi
14 fi
  
```



A Tree-based Barrier

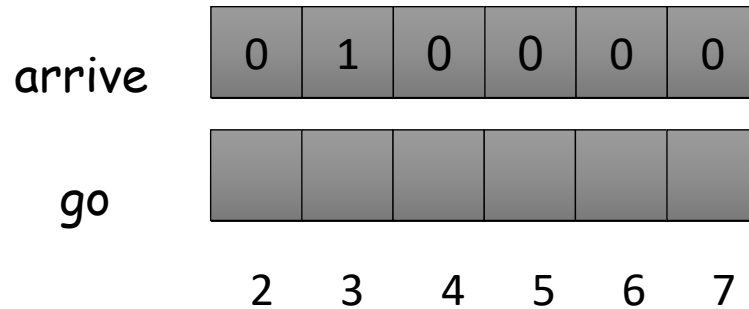
Example Run for n=7 threads



```

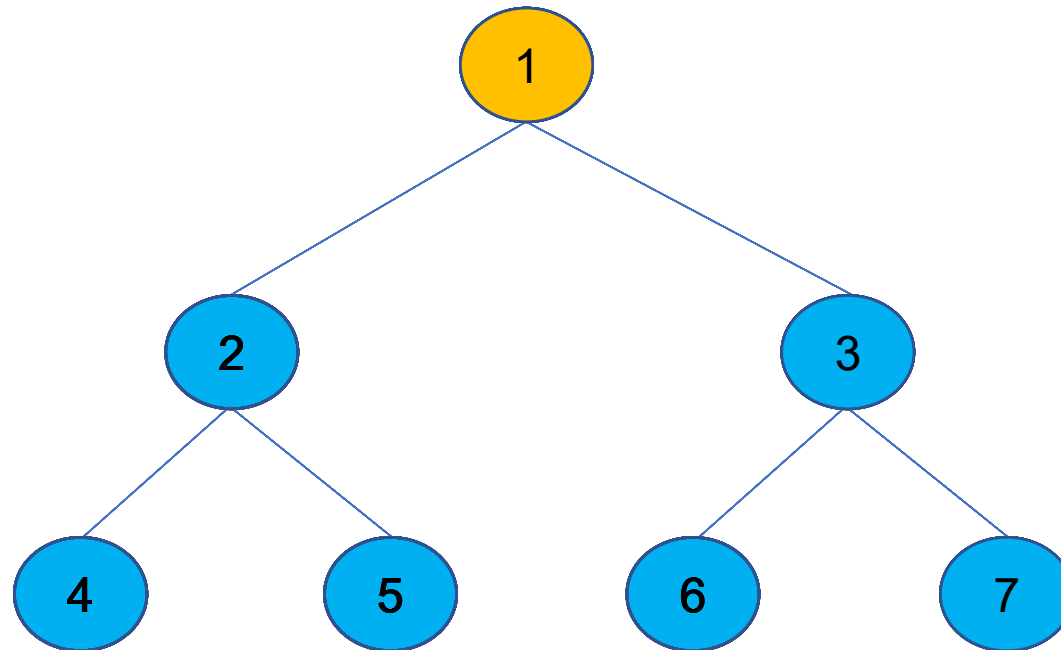
shared  arrive[2..n]: array of atomic bits, initial values = 0
        go[2..n]: array of atomic bits, initial values = 0

1  if i=1 then // root
2    await(arrive[2] = 1); arrive[2] := 0
3    await(arrive[3] = 1); arrive[3] := 0
4    go[2] = 1; go[3] = 1
5  else if i ≤ (n-1)/2 then // internal node
6    await(arrive[2i] = 1); arrive[2i] := 0
7    await(arrive[2i+1] = 1); arrive[2i+1] := 0
8    arrive[i] := 1
9    await(go[i] = 1); go[i] := 0
10   go[2i] = 1; go[2i+1] := 1
11 else // leaf
12   arrive[i] := 1
13   await(go[i] = 1); go[i] := 0 fi
14 fi
  
```



A Tree-based Barrier

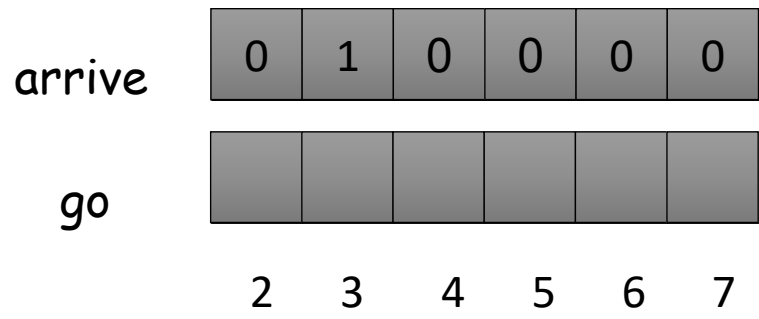
Example Run for n=7 threads



```

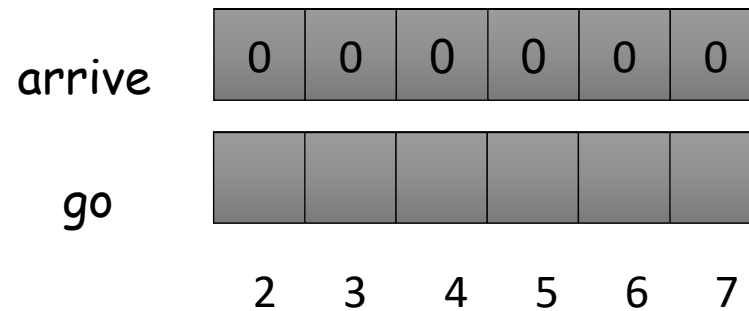
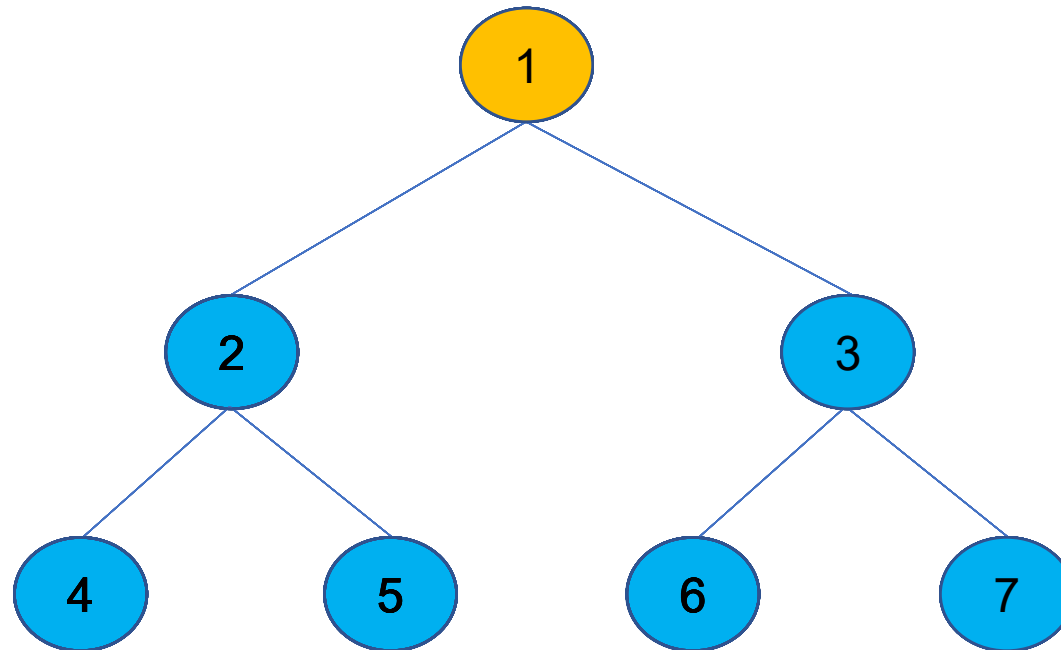
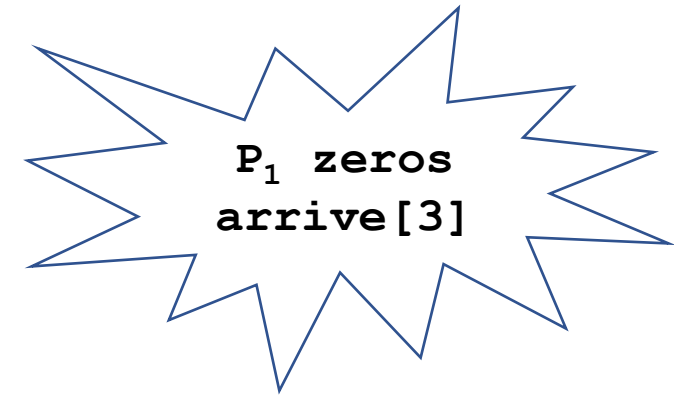
shared arrive[2..n]: array of atomic bits, initial values = 0
shared go[2..n]: array of atomic bits, initial values = 0

1 if i=1 then // root
2   await(arrive[2] = 1); arrive[2] := 0
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4   go[2] = 1; go[3] = 1
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A Tree-based Barrier

Example Run for n=7 threads



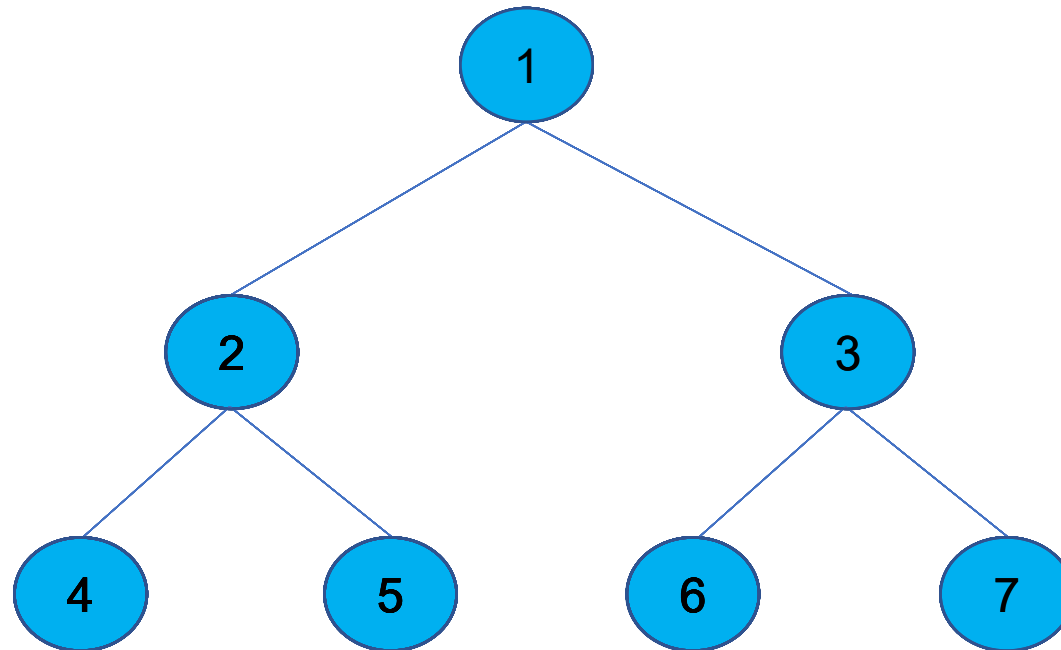
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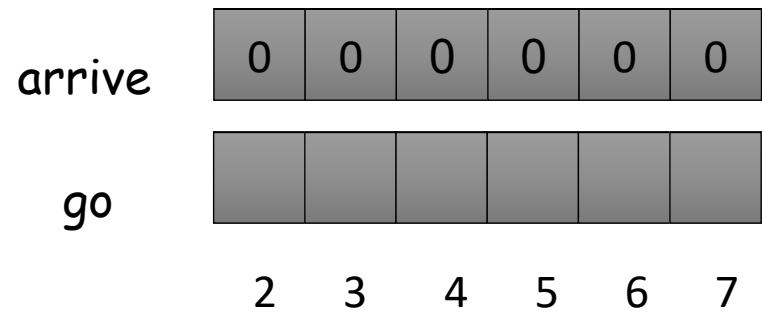
A Tree-based Barrier

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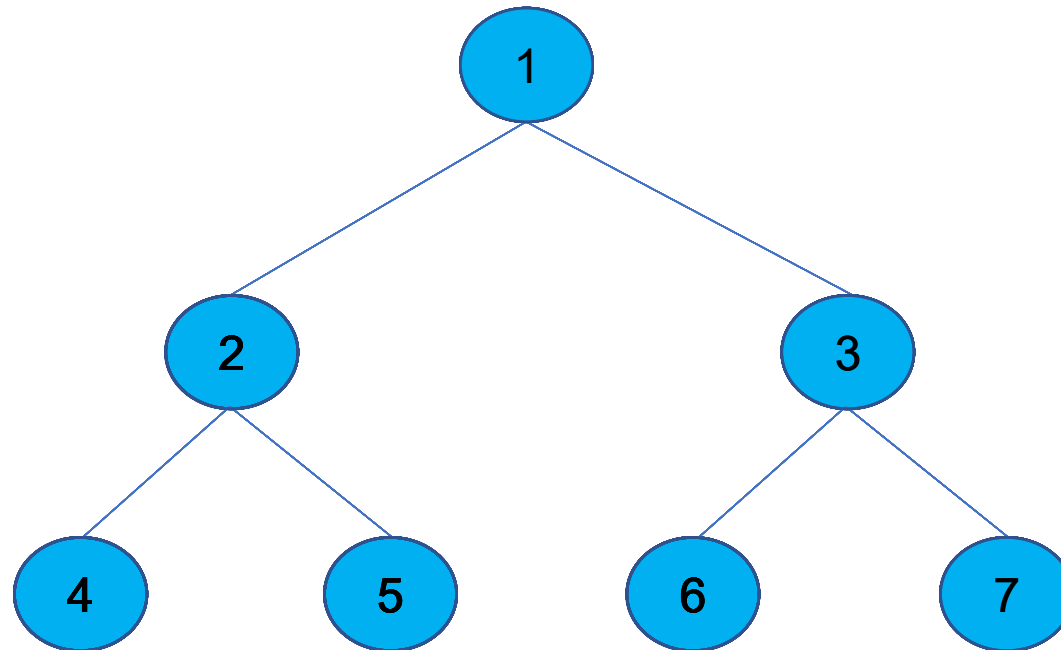
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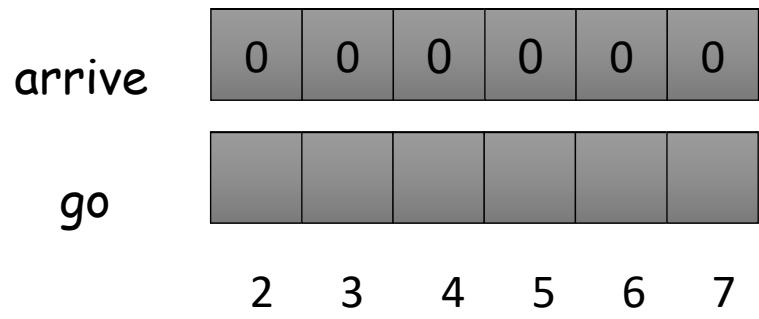


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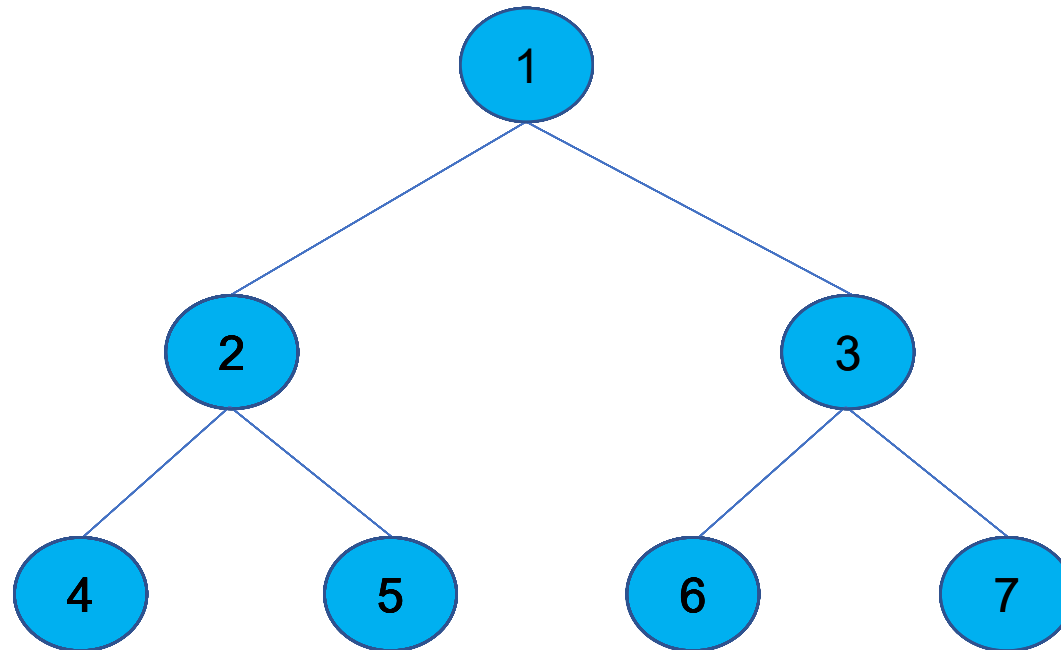
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At this point all non-root threads in some await(go) case

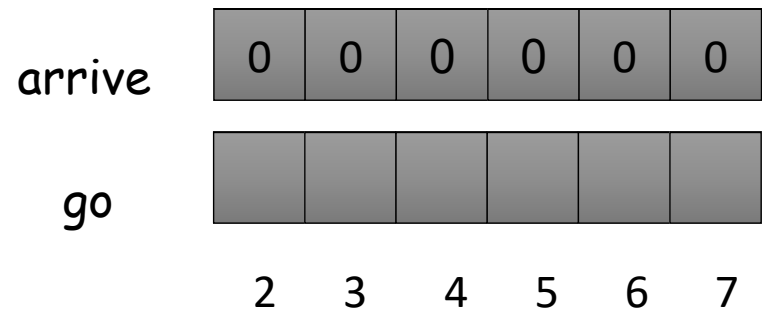
A Tree-based Barrier

Example Run for n=7 threads



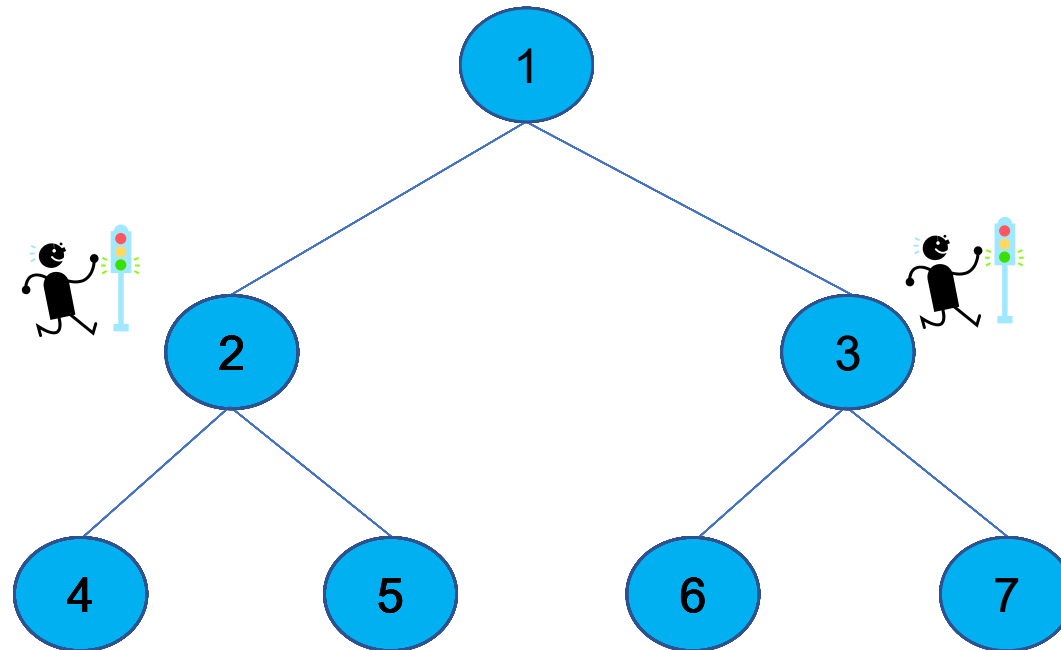
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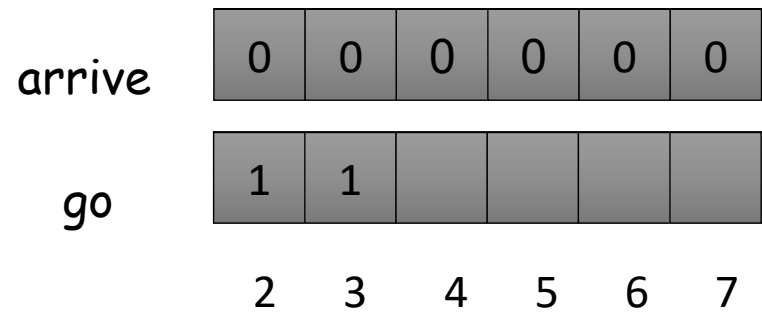
Example Run for n=7 threads



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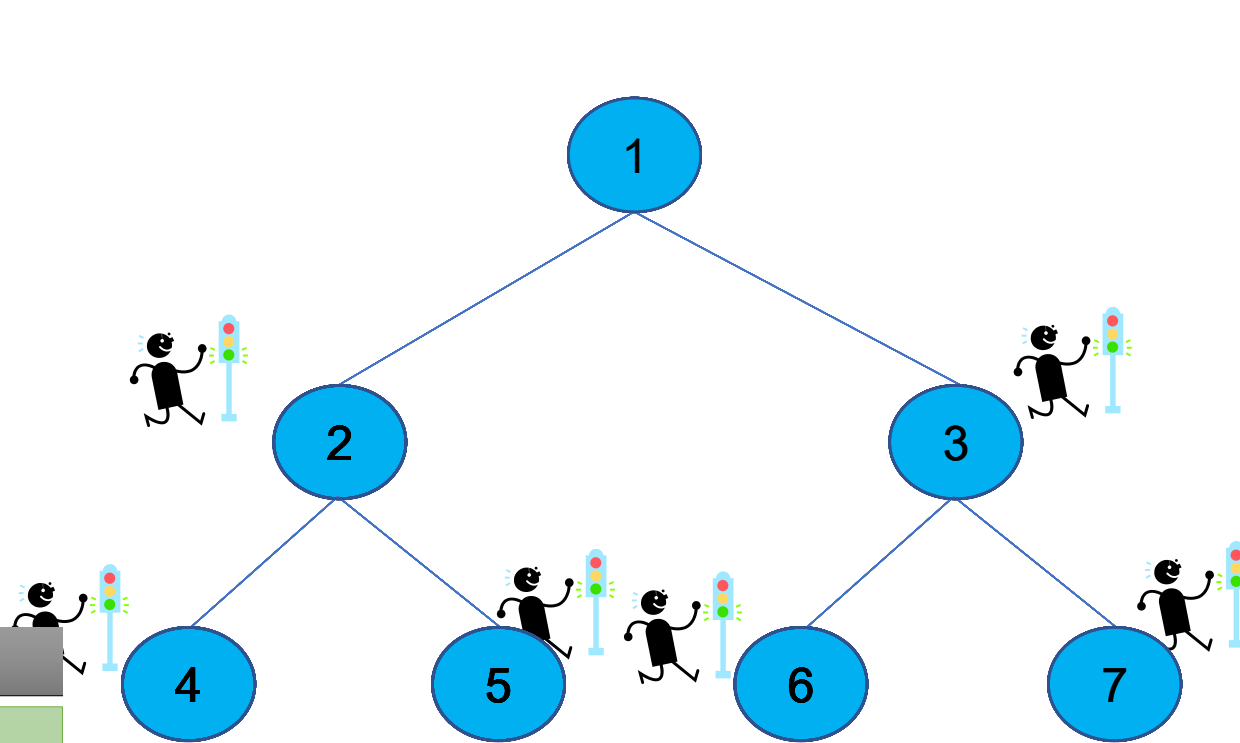
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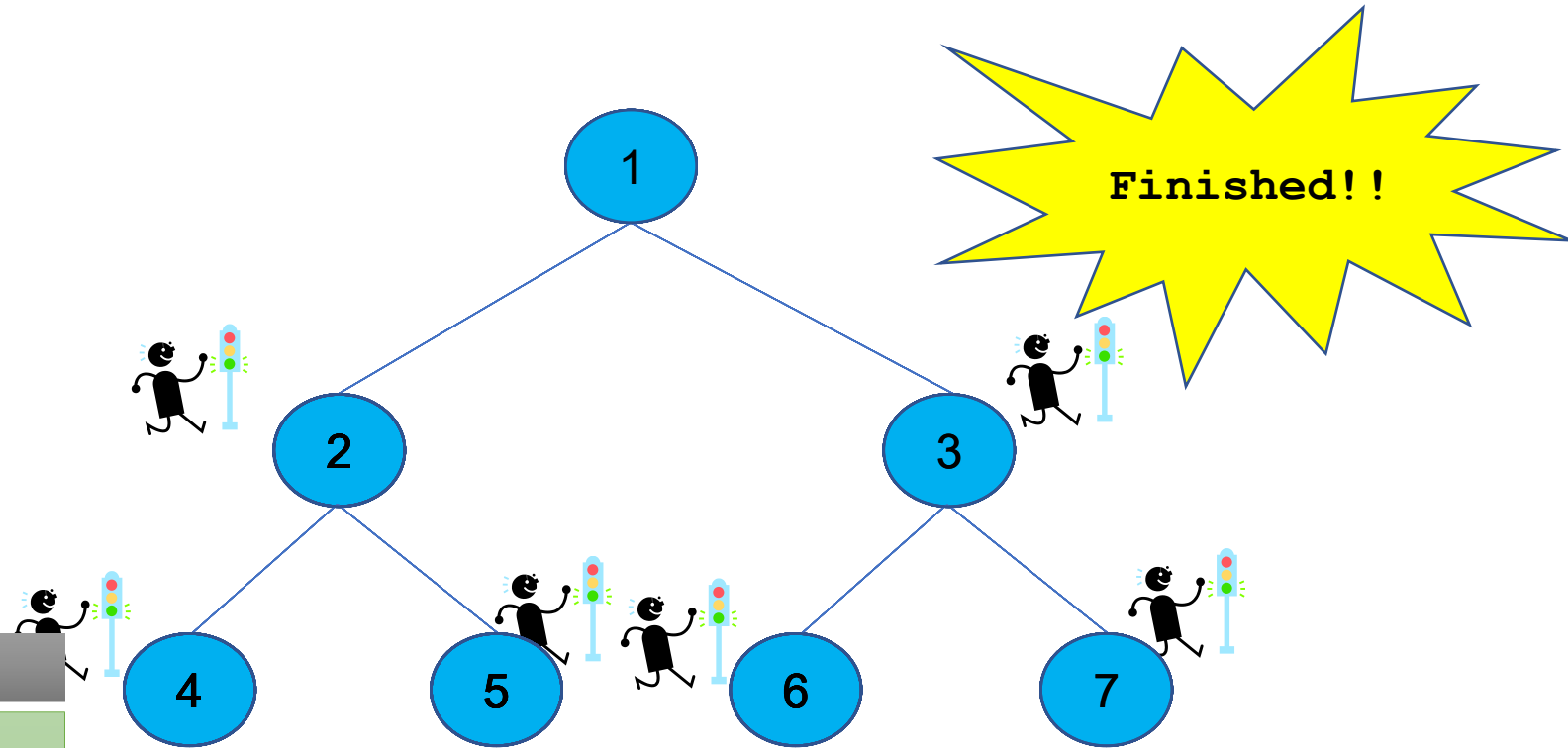
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go	1	1	1	1	1	1
	2	3	4	5	6	7

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Tree Barrier Tradeoffs

- Pros:

- Cons:

Tree Barrier Tradeoffs

- **Pros:**

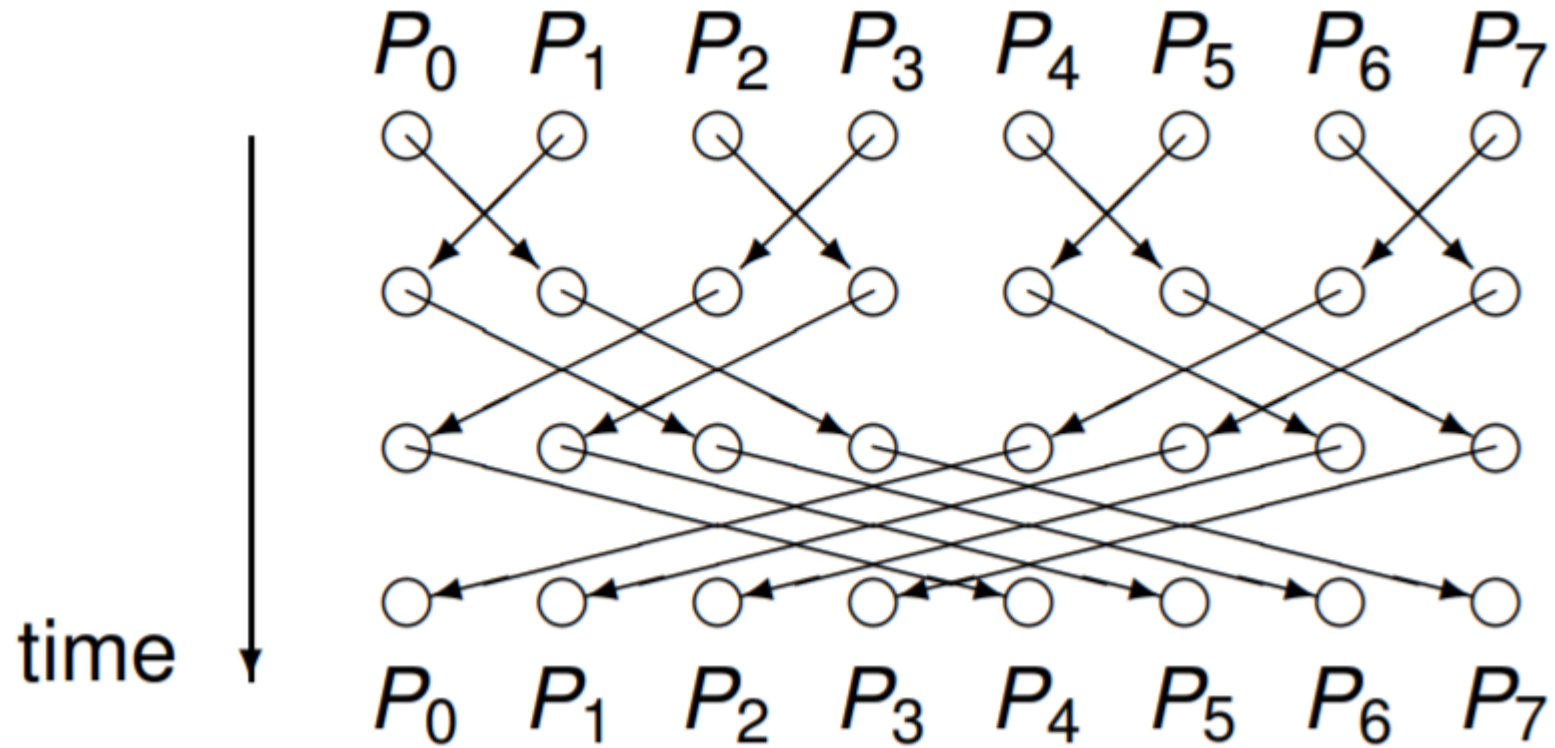
- Low shared memory contention
 - No wait object is shared by more than 2 processes
 - Good for larger n
- Fast – information from the root propagates after $\log(n)$ steps
- Can use only atomic primitives (no special objects)
- On some models:
 - each process spins on a locally accessible bit
 - # (remote memory ref.) = $O(1)$ per process

- **Cons:**

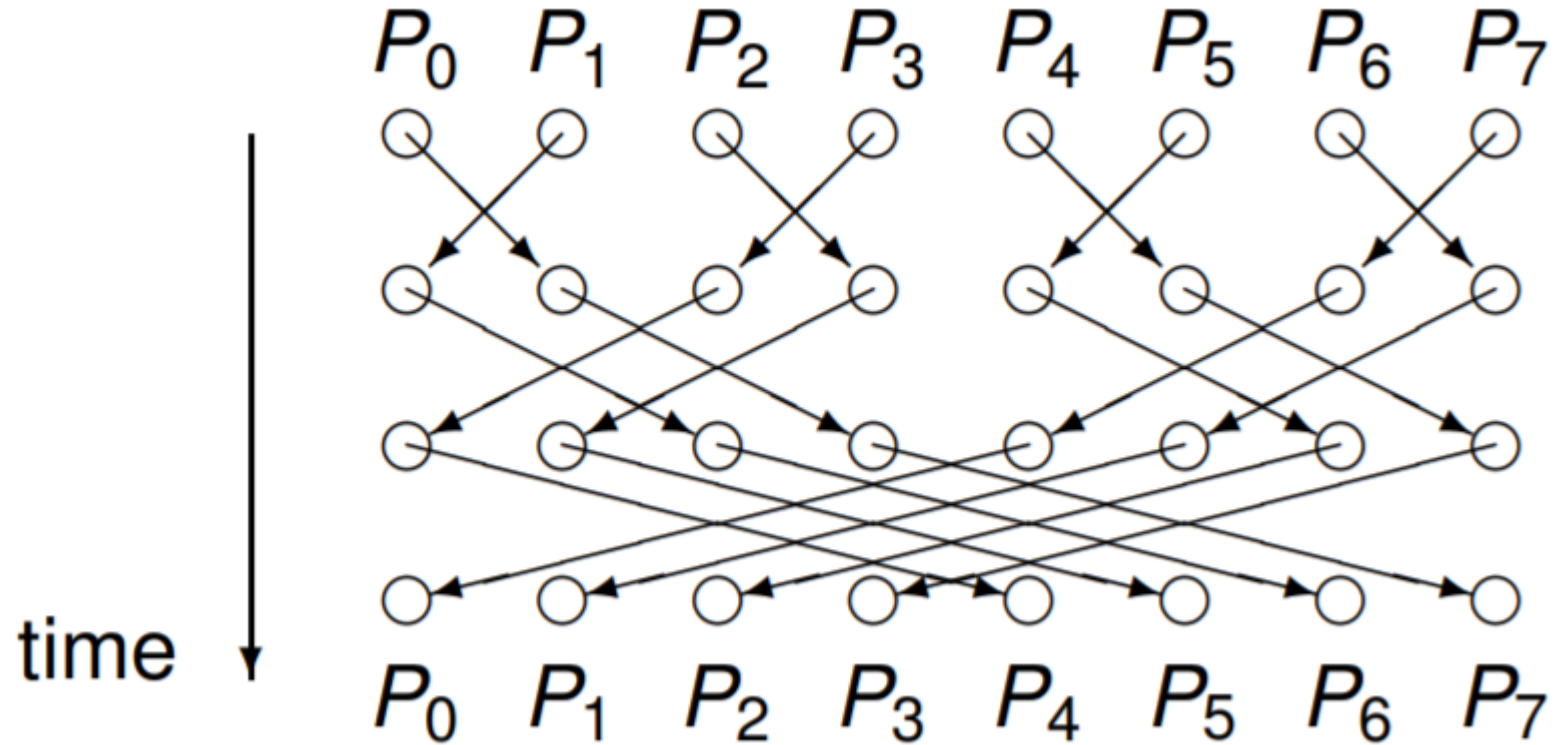
- Shared memory space complexity – $O(n)$
- Asymmetric – all the processes don't the same amount of work
- Corner cases for $n \neq 2^k - 1$

Butterfly Barrier

Butterfly Barrier

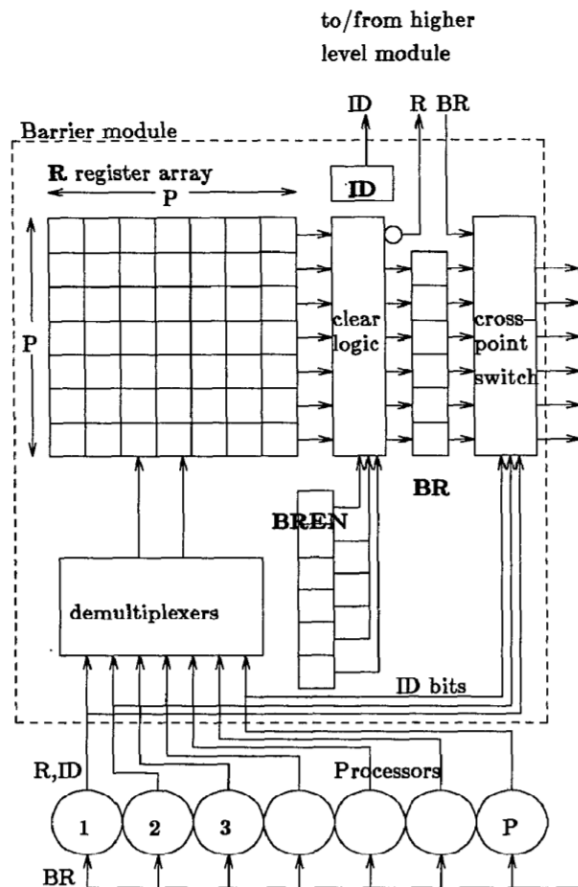


Butterfly Barrier



- When would this be preferable?

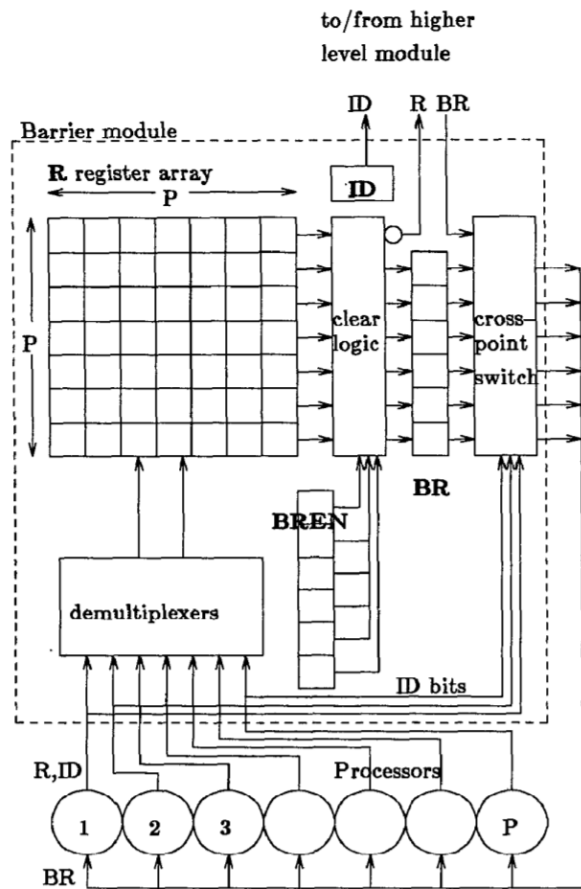
Hardware Supported Barriers



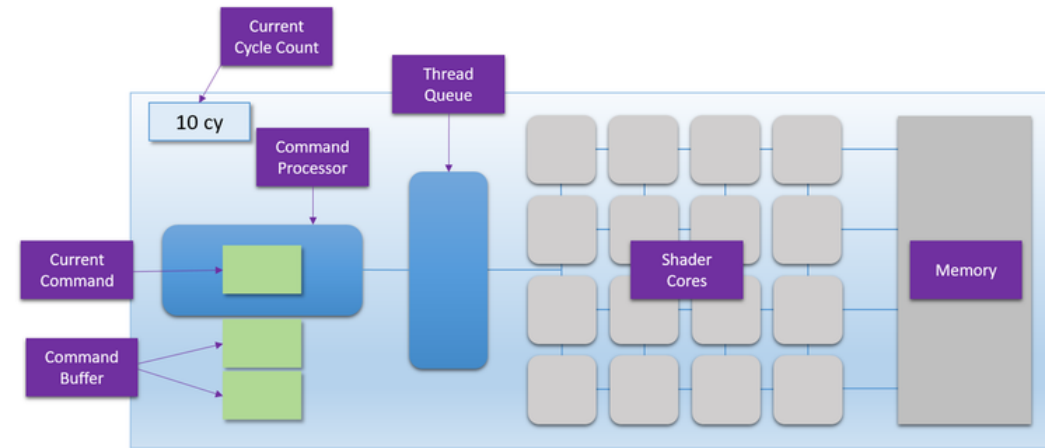
CPU

- When would this be useful?

Hardware Supported Barriers



CPU



GPU

- When would this be useful?

Barriers Summary

Seen:

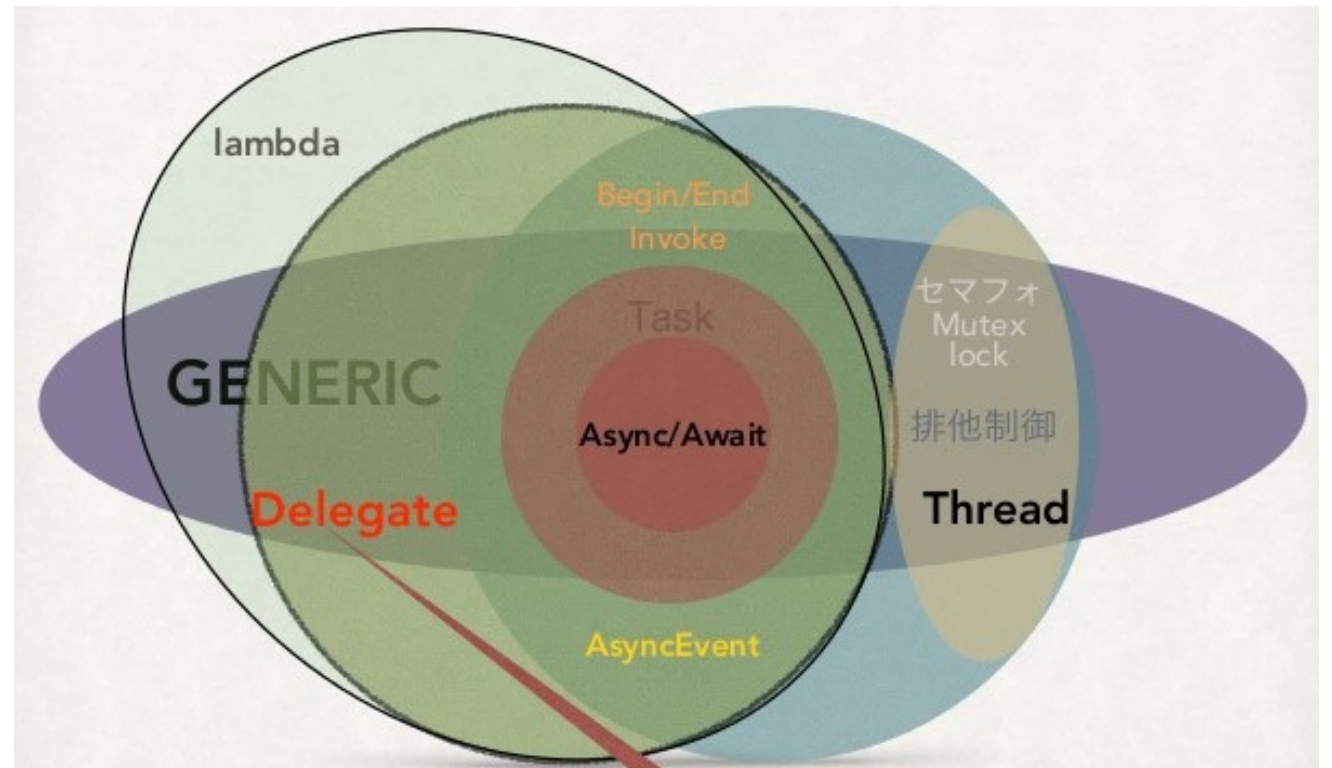
- Semaphore-based barrier
- Simple barrier
 - Based on atomic fetch-and-increment counter
- Local spinning barrier
 - Based on atomic fetch-and-increment counter and go array

- Tree-based barrier

Not seen:

- Test-and-Set barriers
 - Based on test-and-test-and-set objects
 - One version without memory initialization
- See-Saw barrier
- Book has condition barriers

Asynchronous Programming Events, Promises, and Futures



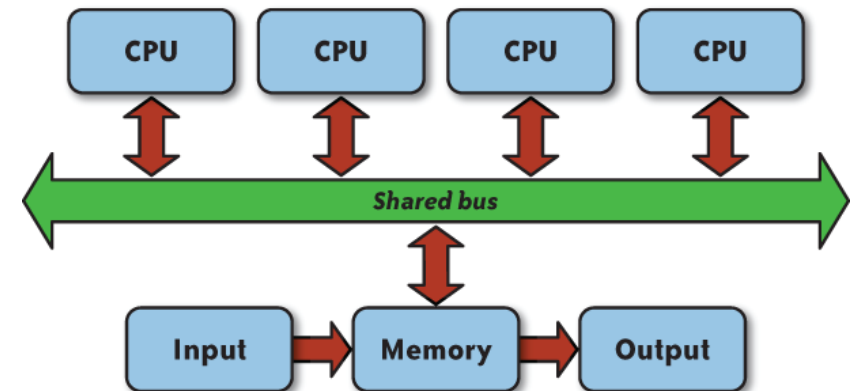
Programming Models for Concurrency

Programming Models for Concurrency

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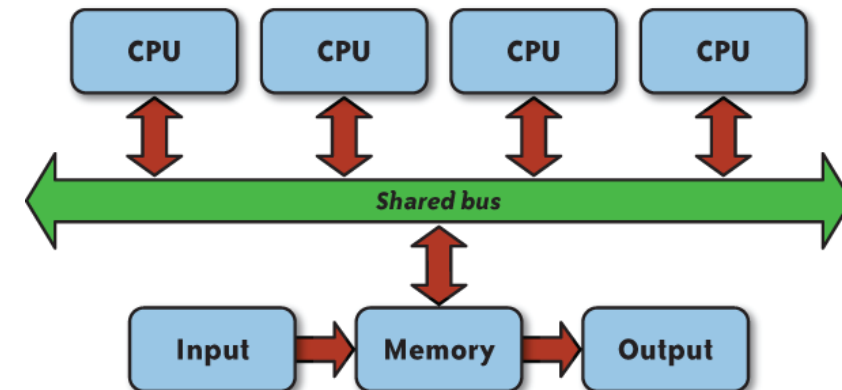
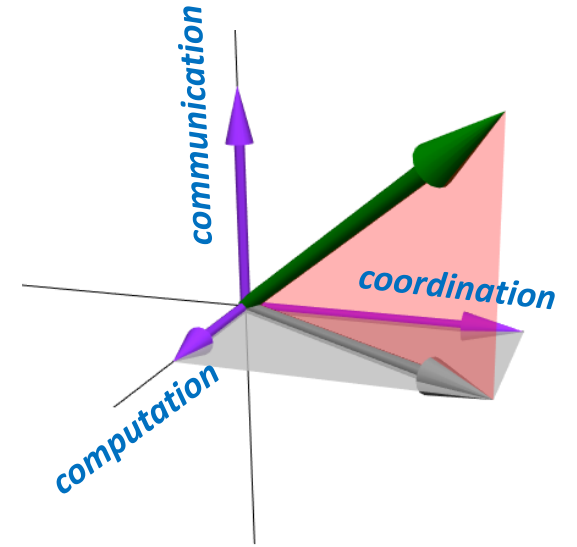
Programming Models for Concurrency

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 - CPU(s) execute instructions sequentially



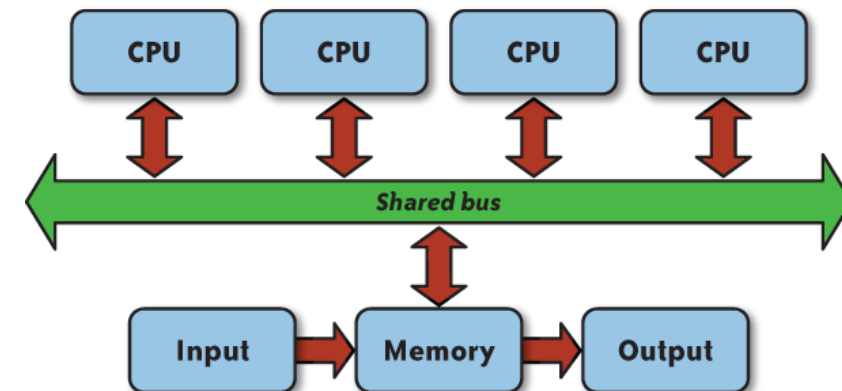
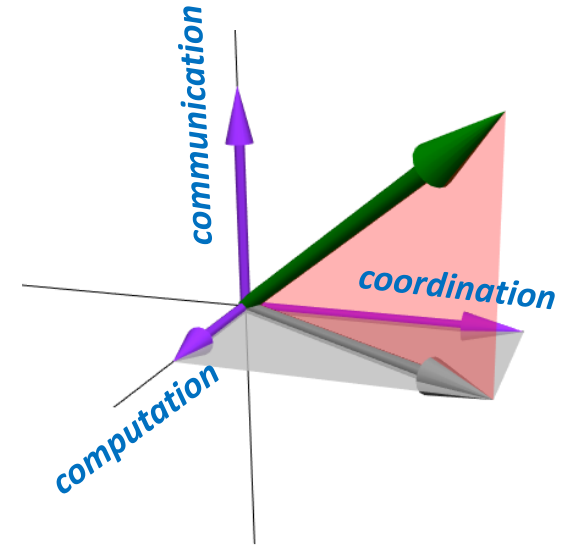
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 - How to specify computation
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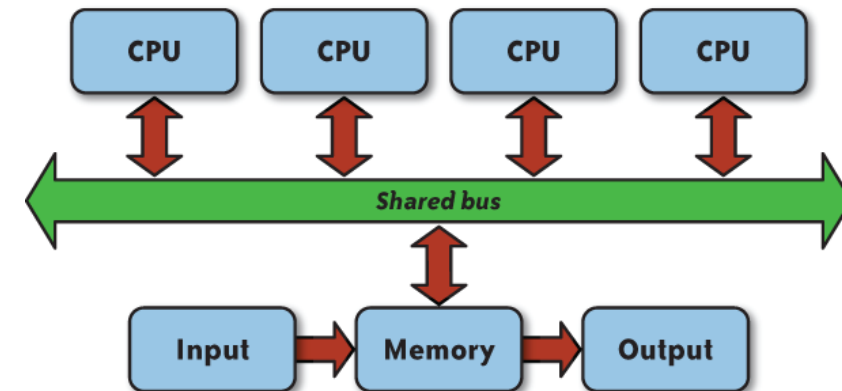
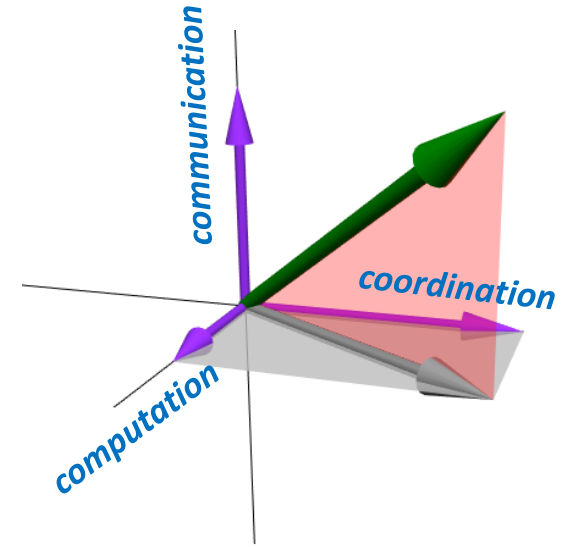
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 - Preemption vs Non-preemption



Programming Models for Concurrency

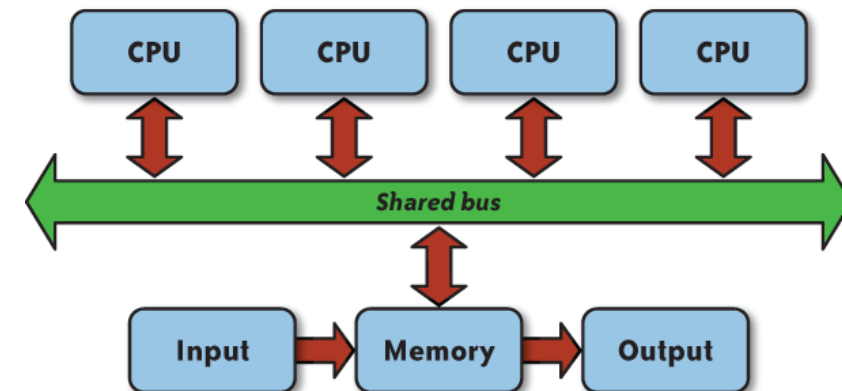
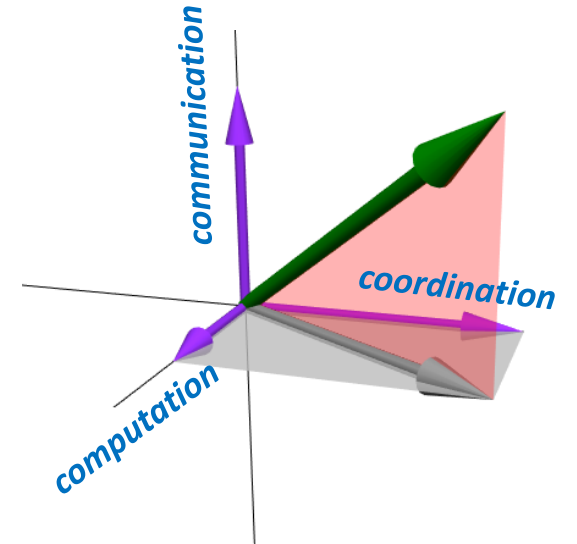
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Futures & Promises touch all three dimension



Futures & Promises

Futures & Promises

- *Values that will eventually become available*

Futures & Promises

- *Values that will eventually become available*
- Time-dependent states:
 - **Completed/determined**
 - Computation complete, value concrete
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Futures & Promises

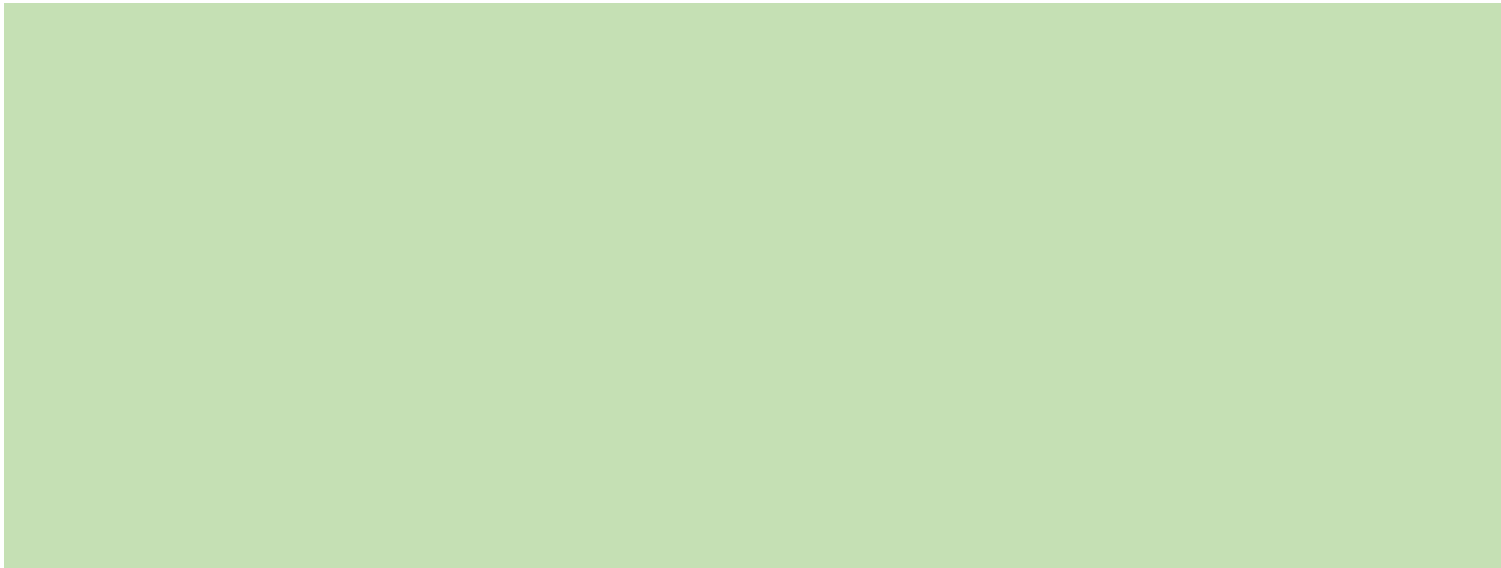
- *Values that will eventually become available*
- Time-dependent states:
 - **Completed/determined**
 - Computation complete, value concrete
 - **Incomplete/undetermined**
 - Computation not complete yet
- Construct (future X)
 - immediately returns value
 - concurrently executes X

Java Example

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1 static void runAsyncExample() {  
2     CompletableFuture cf = CompletableFuture.runAsync(() -> {  
3         assertTrue(Thread.currentThread().isDaemon());  
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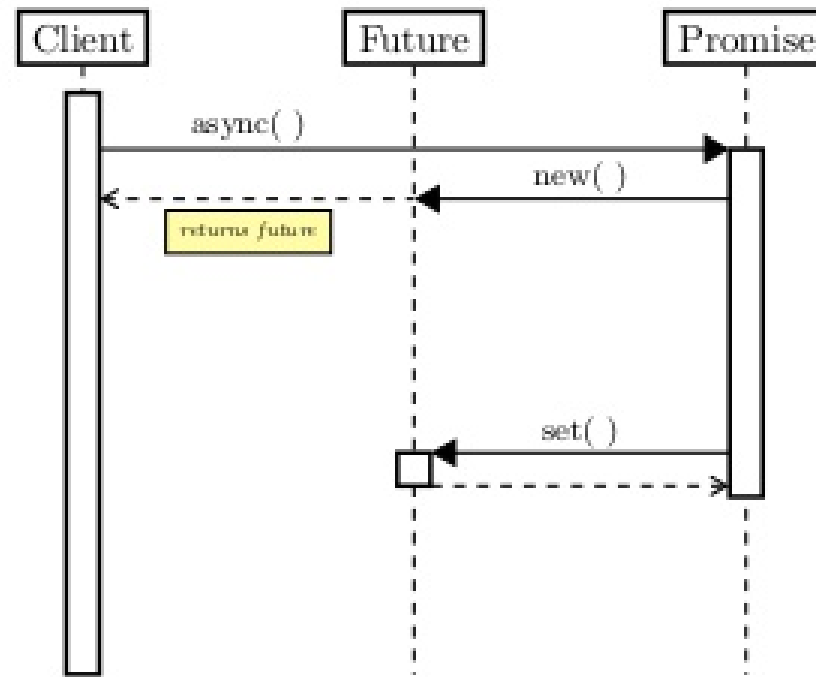
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- runAsync() immediately returns a waitable object (cf)
- Where (on what thread) does the lambda expression run?

Futures and Promises:

Why two kinds of objects?

```
future<int> f1 = async(foo1);  
...  
int result = f1.get();
```

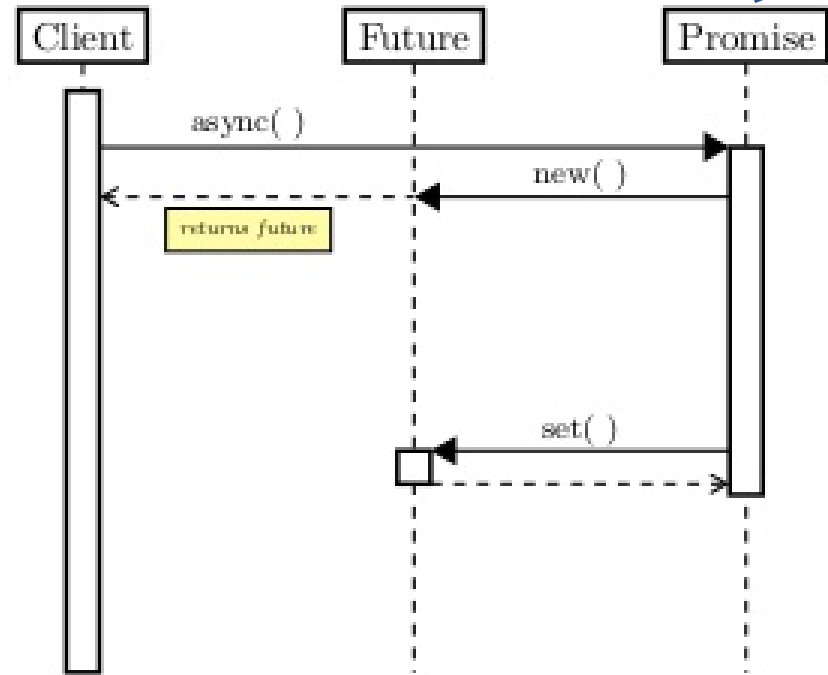


Futures and Promises:

Why two kinds of objects?

Promise: "thing to be done"

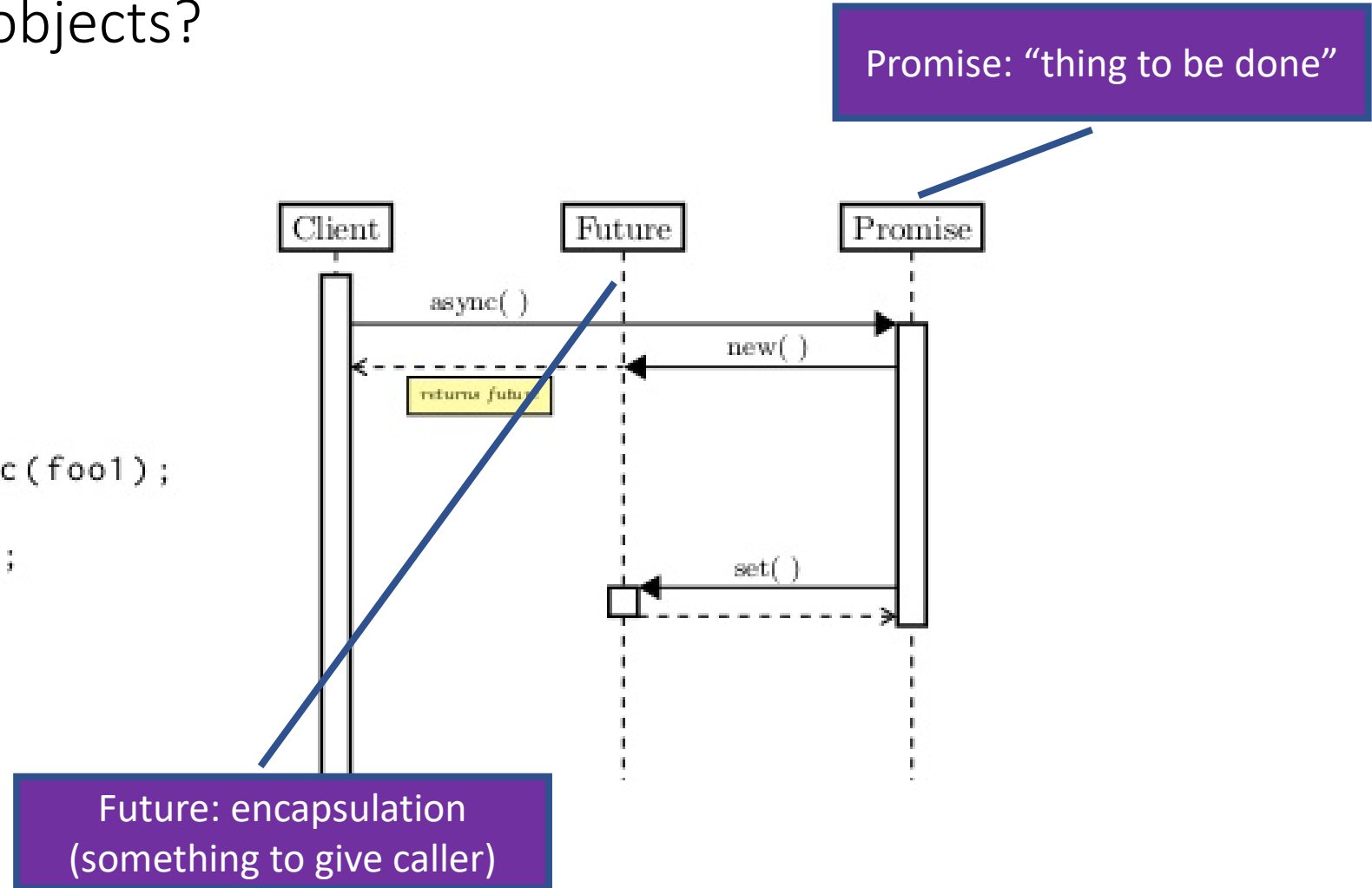
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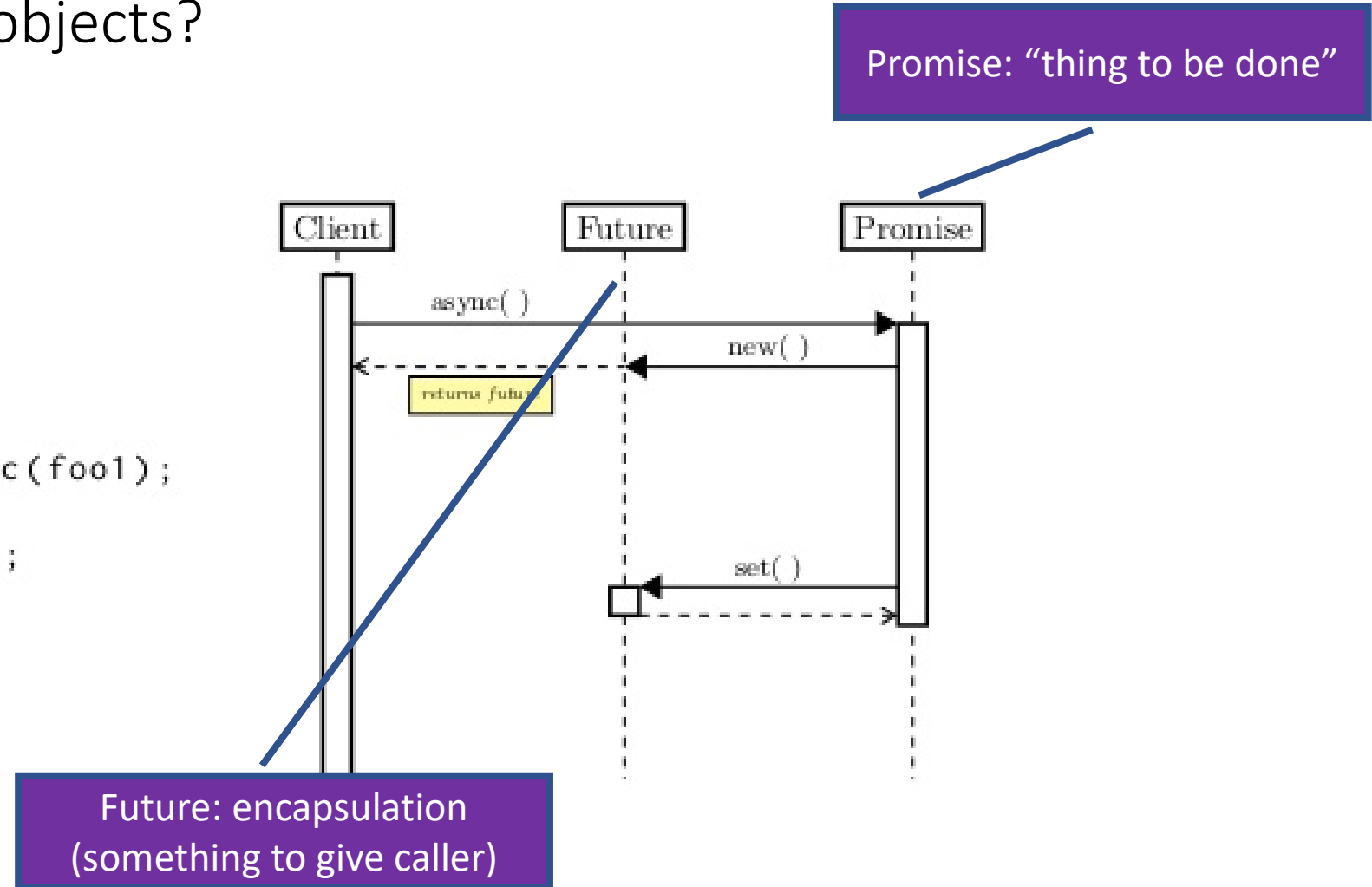
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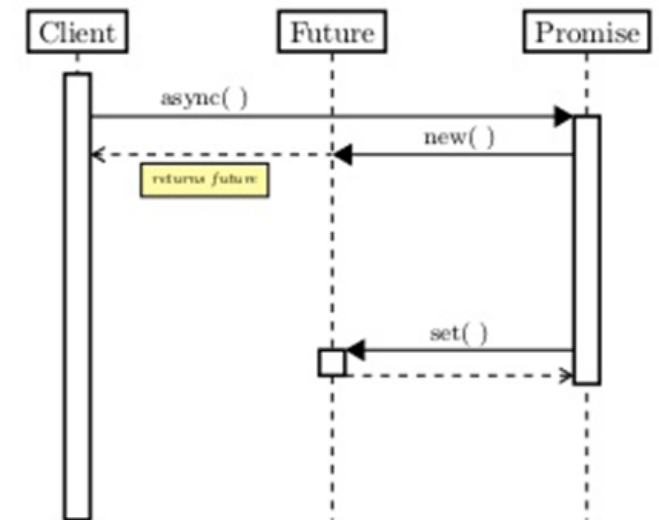
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Promise to do something in the future

Futures vs Promises

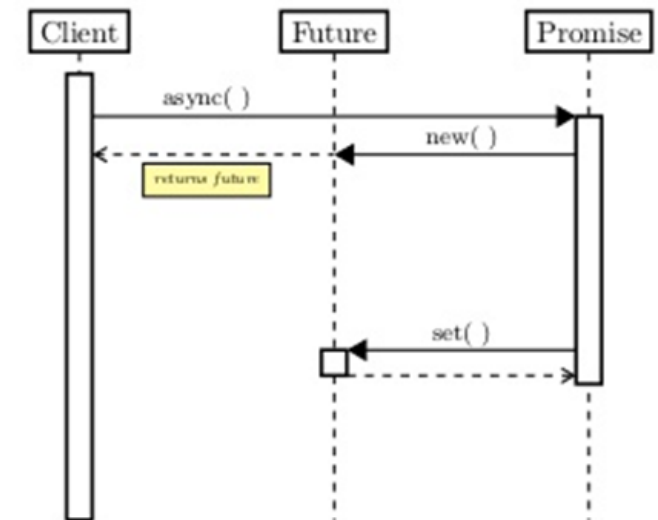
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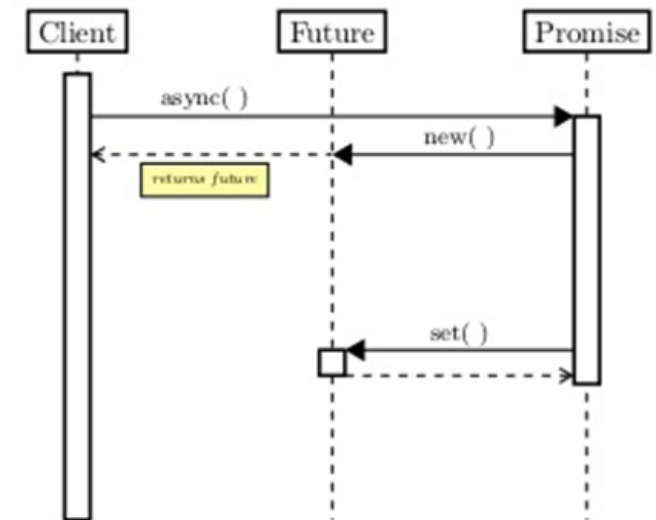
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Algol	Thunk	Address of async result
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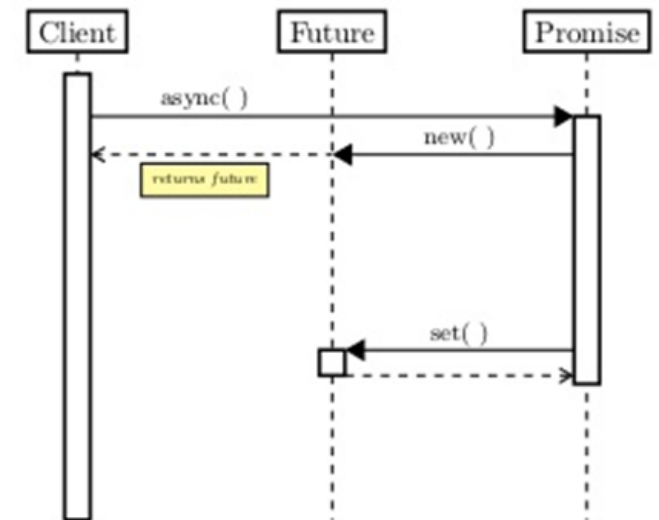


Futures vs Promises

Mnemonic:
Promise to *do* something
Make a promise *for* the future

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My unvarnished opinion

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 - Runtime: scheduler, task queues, thread-pools are *transparent*
- Programming remains **mostly** imperative/sequential
 - Threads of control peppered with asynchronous/concurrent tasks

Putting Futures in Context

My unvarnished opinion

Futures:

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Compromise Programming Model between:

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Compromise Programming Model between:

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- Thread-based programming

Putting Futures in Context

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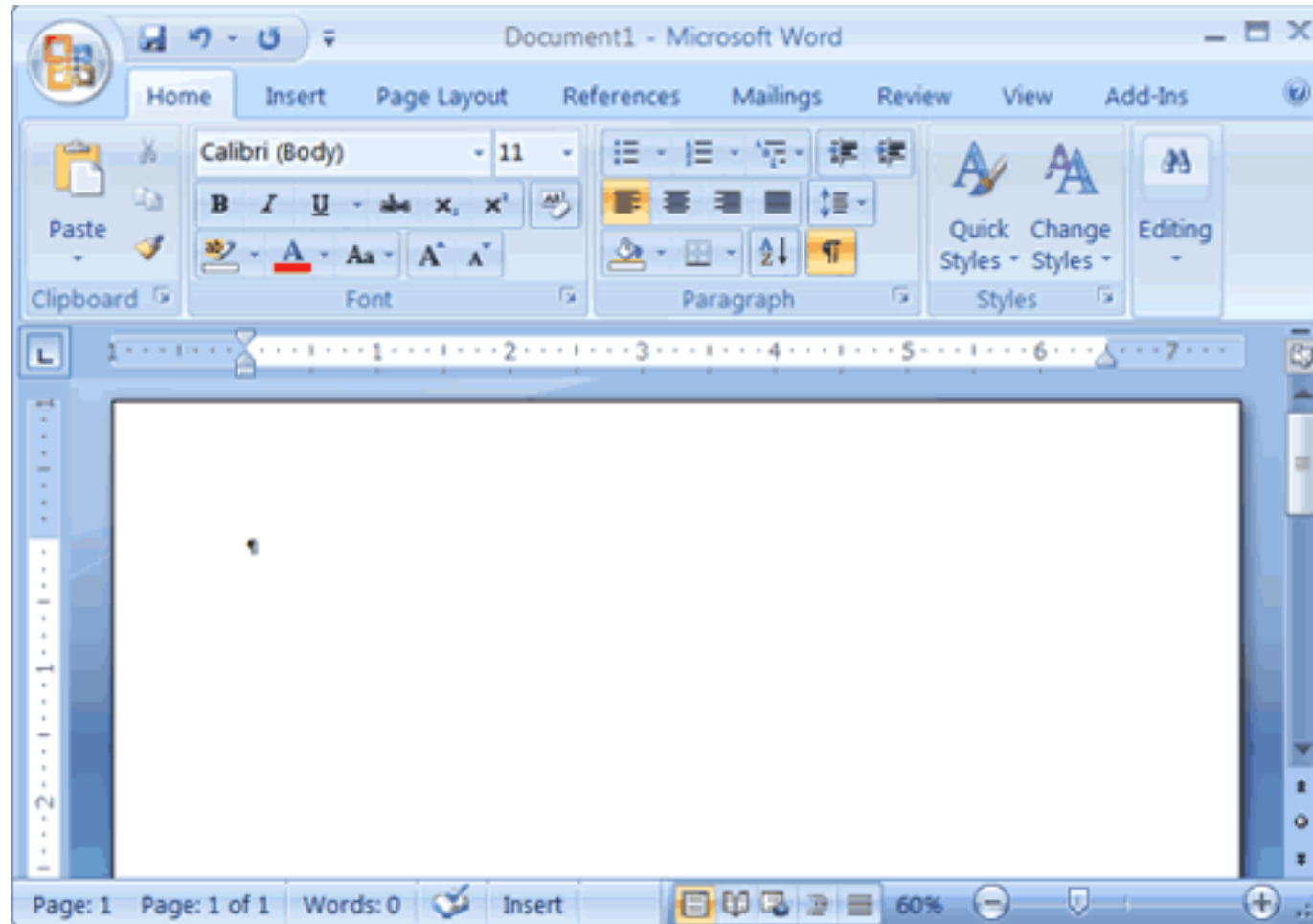
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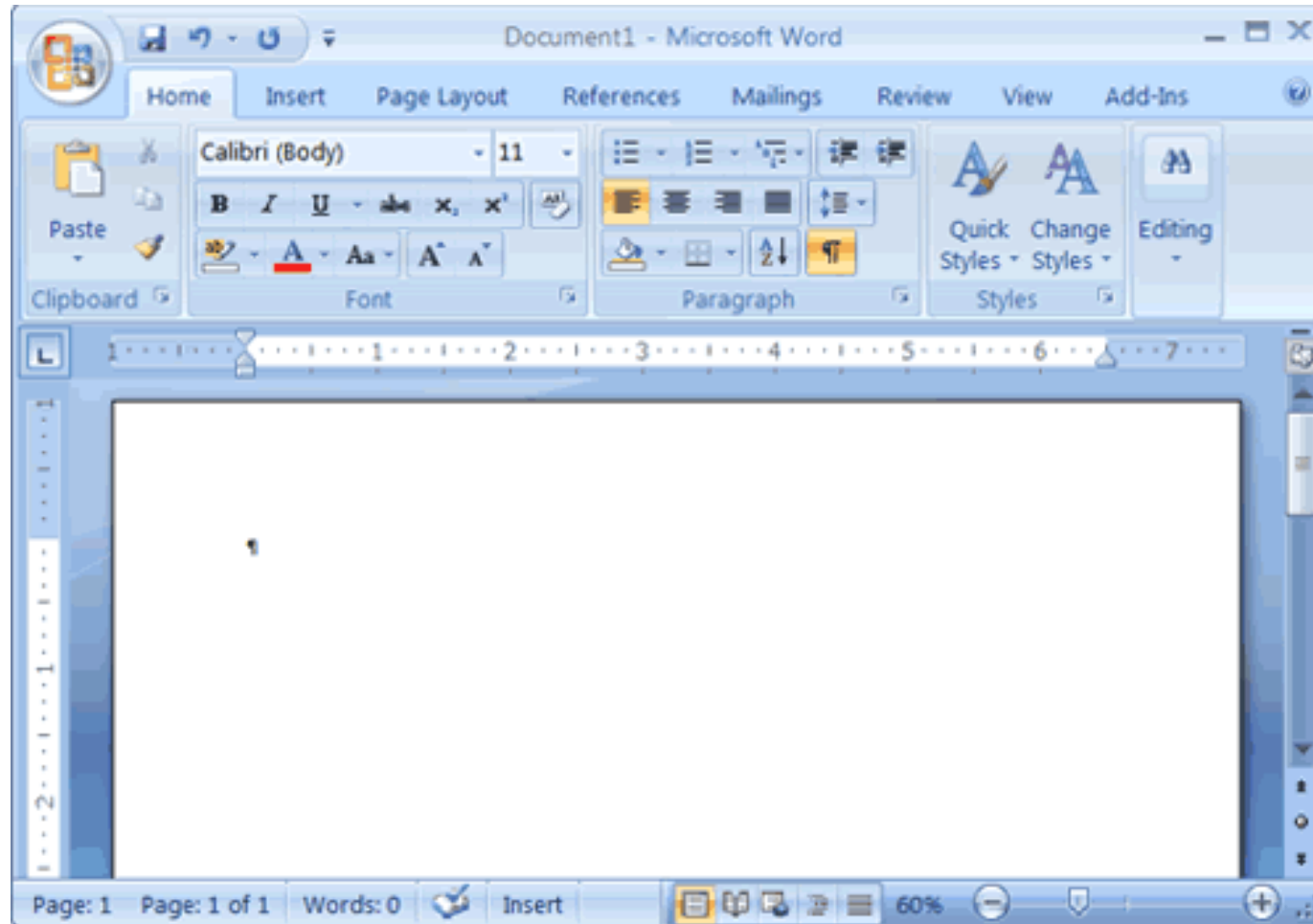
- Event-based programming
- Thread-based programming

Events vs. Threads!

GUI Programming



GUI Programming



```
do {  
    WaitForSomething();  
    RespondToThing();  
} until (forever);
```

GUI Programming

```
int WINAPI WinMain(HINSTANCE hInstance, HINSTANCE hPrevInstance,
LPSTR lpCmdLine, int nCmdShow)
{
    WNDCLASSEX wc;
    HWND hwnd;
    MSG Msg;

    //Step 1: Registering the Window Class
    wc.cbSize      = sizeof(WNDCLASSEX);
    wc.style       = 0;
    wc.lpfnWndProc = WndProc;
    wc.cbClsExtra  = 0;
    wc.cbWndExtra  = 0;
    wc.hInstance   = hInstance;
    wc.hIcon       = LoadIcon(NULL, IDI_APPLICATION);
    wc.hCursor     = LoadCursor(NULL, IDC_ARROW);
    wc.hbrBackground = (HBRUSH)(COLOR_WINDOW+1);
    wc.lpszMenuName = NULL;
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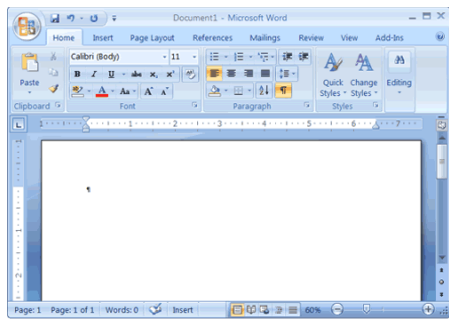
    if(!RegisterClassEx(&wc))
    {
        MessageBox(NULL, "Window Registration Failed!", "Error!",
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        return 0;
    }

    // Step 2: Creating the Window
    hwnd = CreateWindowEx(
        WS_EX_CLIENTEDGE,
        g_szClassName,
        "The title of my window",
        WS_OVERLAPPEDWINDOW,
        CW_USEDEFAULT, CW_USEDEFAULT, 240, 120,
        NULL, NULL, hInstance, NULL);

    if(hwnd == NULL)
    {
        MessageBox(NULL, "Window Creation Failed!", "Error!",
            MB_ICONEXCLAMATION | MB_OK);
        return 0;
    }

    ShowWindow(hwnd, nCmdShow);
    UpdateWindow(hwnd);

    // Step 3: The Message Loop
    while(GetMessage(&Msg, NULL, 0, 0) > 0)
    {
        TranslateMessage(&Msg);
        DispatchMessage(&Msg);
    }
    return Msg.wParam;
}
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GUI Programming

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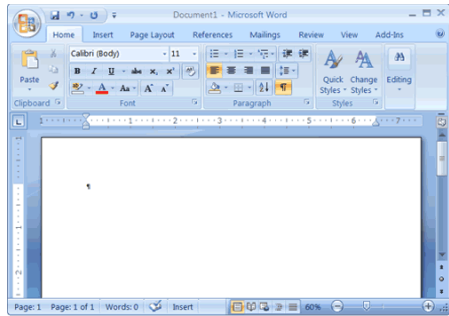
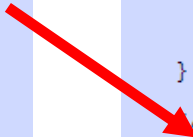
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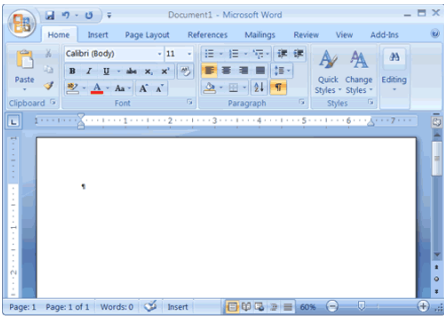
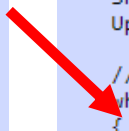
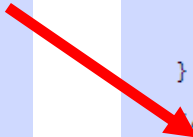
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```



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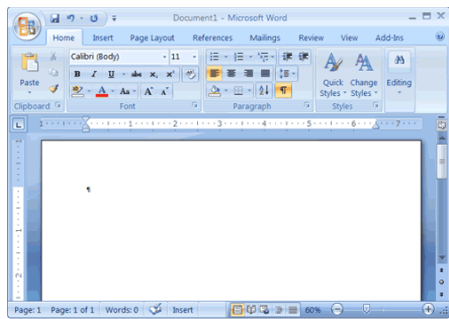
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switch (message)
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    //case WM_COMMAND:
    //    // handle menu selections etc.
    //break;
    //case WM_PAINT:
    //    // draw our window - note: you must paint something here or not trap it!
    //break;
    case WM_DESTROY:
        PostQuitMessage(0);
    break;
    default:
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        return DefWindowProc(hWnd, message, wParam, lParam);
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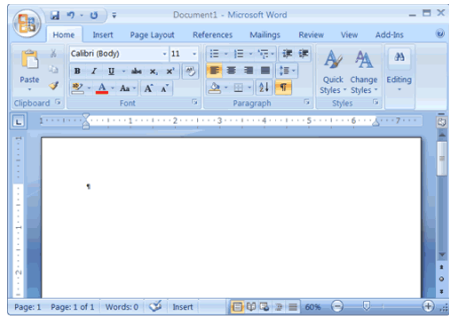
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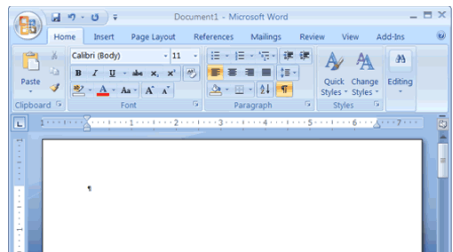
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Hex	Decimal	Symbolic
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0003	3	WM_MOVE
0005	5	WM_SIZE
0006	6	WM_ACTIVATE
0007	7	WM_SETFOCUS
0008	8	WM_KILLFOCUS
000a	10	WM_ENABLE
000b	11	WM_SETREDRAW
000c	12	WM_SETTEXT
000d	13	WM_GETTEXT
000e	14	WM_GETTEXTLENGTH
000f	15	WM_PAINT
0010	16	WM_CLOSE
0011	17	WM_QUERYENDSESSION
0012	18	WM_QUIT
0013	19	WM_QUERYOPEN
0014	20	WM_ERASEBKGDND

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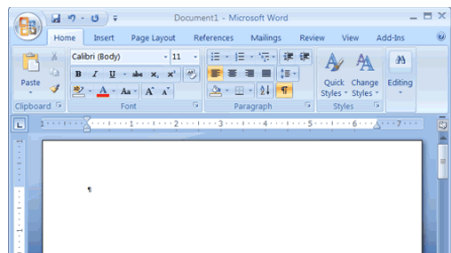
Over 1000 last time I checked!

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GUI Programming Distilled

```
1  winmain (...) {  
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GUI Programming Distilled

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Pros

- Simple imperative programming

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Pros

- Simple imperative programming
- Good fit for uni-processor

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- Awkward/verbose

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- Simple imperative programming
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Cons

- Awkward/verbose
- **Obscures available parallelism**

GUI Programming Distilled

```
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2   while(true) {
3     message = GetMessage();
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5       case WM_LONGRUNNING_CPU_HOG: HogCPU(); break;
6       case WM_HIGH_LATENCY_IO: BlockForALongTime(); break;
7       case WM_DO_QUICK_IMPORTANT_THING: HopeForTheBest(); break;
8     }
9   }
10 }
11 }
```

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- Good fit for uni-processor

Cons

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- **Obscures available parallelism**

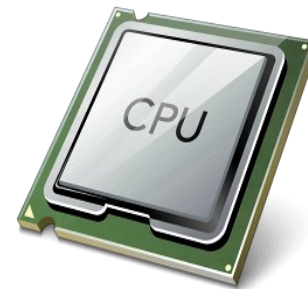
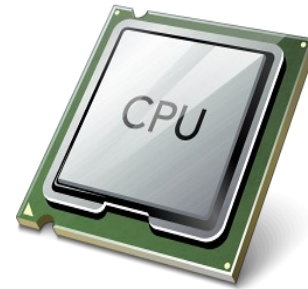
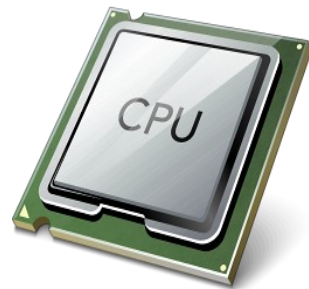
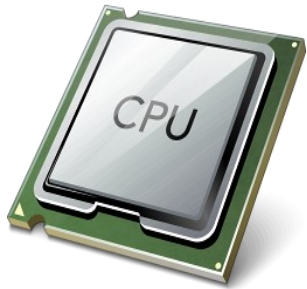
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How can we
parallelize
this?

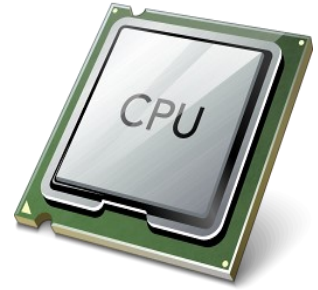


Parallel GUI Implementation 1

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11 }
```

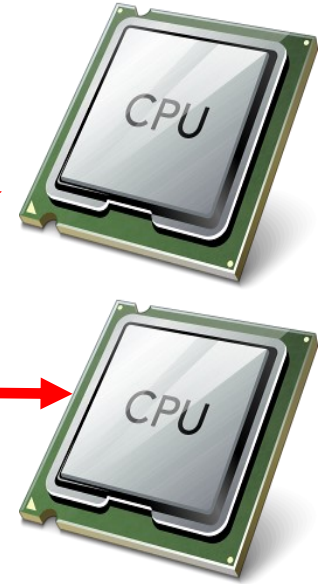
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```



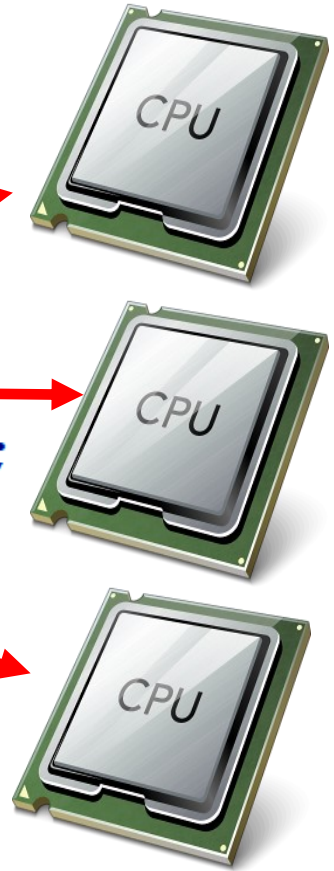
Parallel GUI Implementation 1

```
1 winmain(...) {  
2   while(true) {  
3     message = GetMessage();  
4     switch(message) {  
5       case WM_THIS: DoThis(); break;  
6       case WM_THAT: DoThat(); break;  
7       case WM_OTHERTHING: DoOtherThing(); break;  
8       case WM_DONE: return;  
9     }  
10  }  
11 }
```



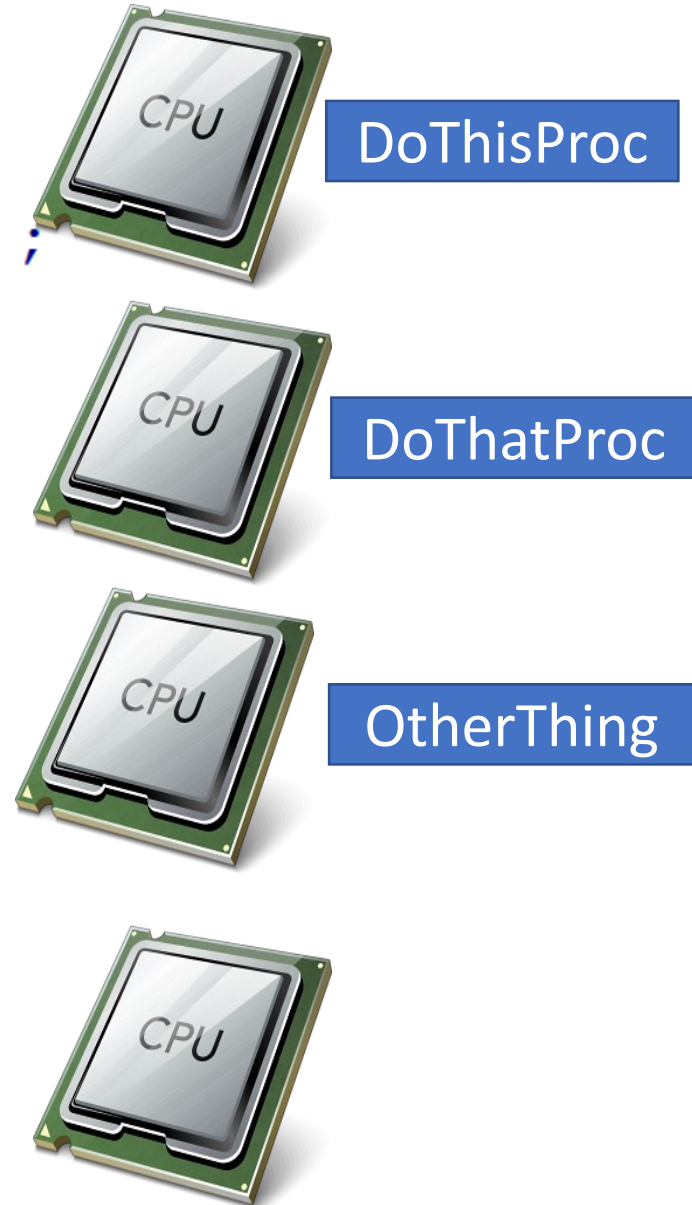
Parallel GUI Implementation 1

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11 }
```



Parallel GUI Implementation 1

```
winmain() {  
    pthread_create(&tids[i++], DoThisProc);  
    pthread_create(&tids[i++], DoThatProc);  
    pthread_create(&tids[i++], DoOtherThingProc);  
    for(j=0; j<i; j++)  
        pthread_join(&tids[j]);  
}  
  
DoThisProc() {  
    while(true) {  
        if(ThisHasHappened)  
            DoThis();  
    }  
}
```

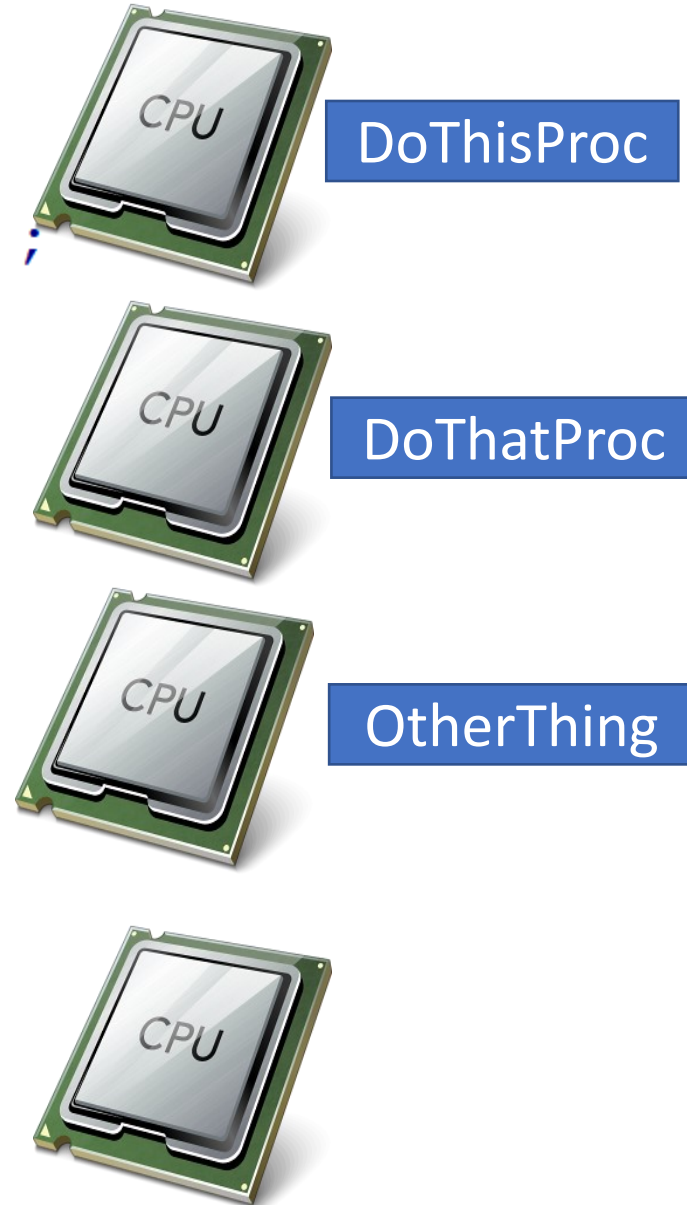


Parallel GUI Implementation 1

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winmain() {  
    pthread_create(&tids[i++], DoThisProc);  
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    pthread_create(&tids[i++], DoOtherThingProc);  
    for(j=0; j<i; j++)  
        pthread_join(&tids[j]);  
}
```

Pros/cons?

```
DoThisProc() {  
    while(true) {  
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}
```



Parallel GUI Implementation 1

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}  
  
DoThisProc() {  
    while(true) {  
        if(ThisHasHappened) {  
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        }  
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}
```

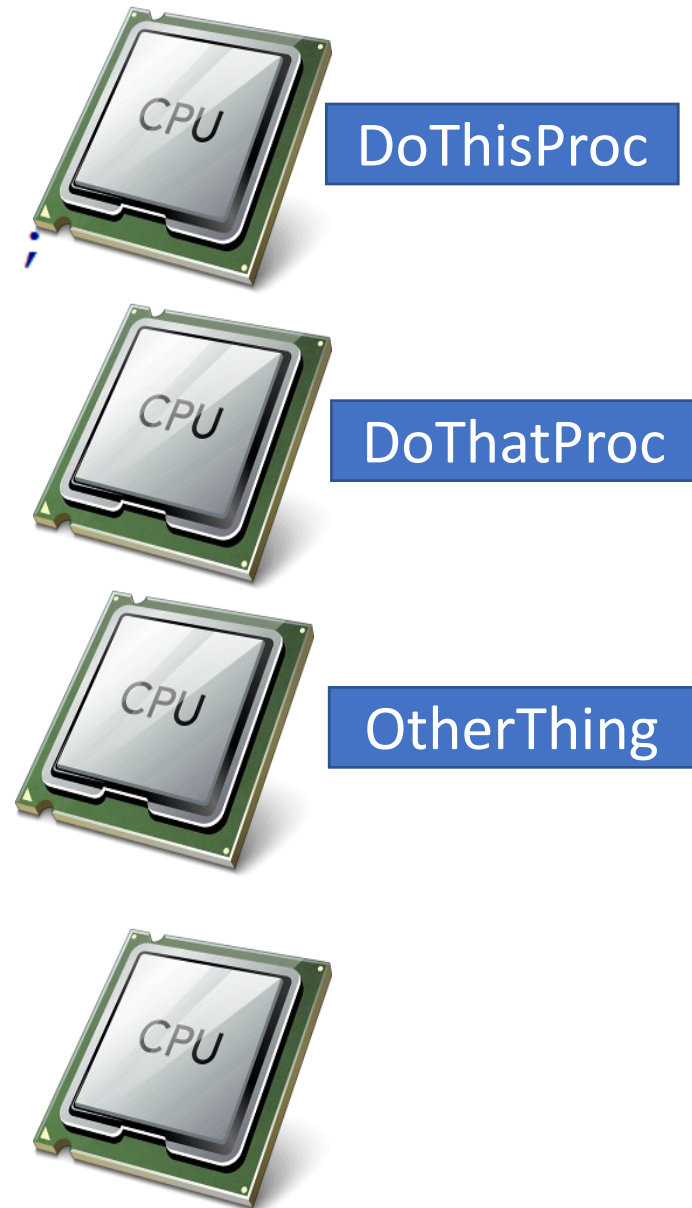
Pros/cons?

Pros:

- Encapsulates parallel work

Cons:

- Obliterates original code structure
- How to assign handlers → CPUs?
- Load balance?!?
- Utilization



Parallel GUI Implementation 2

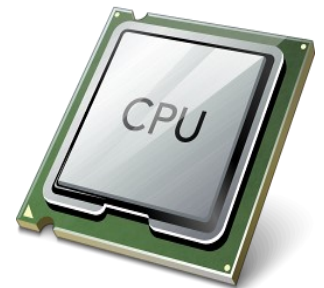
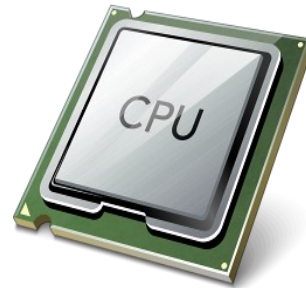
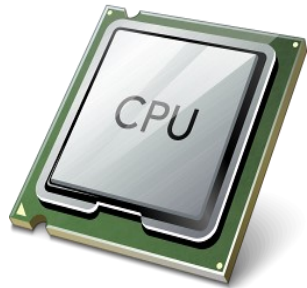
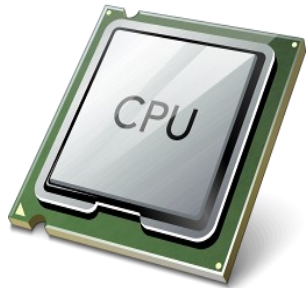
```
winmain() {  
    for(i=0; i<NUMPROCS; i++)  
        pthread_create(&tids[i], HandlerProc);  
    for(i=0; i<NUMPROCS; i++)  
        pthread_join(&tids[i]);  
}
```

```
threadproc(...) {  
    while(true) {  
        message = GetMessage();  
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            case WM_THAT: DoThat();  
        }  
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Parallel GUI Implementation 2

Pros/cons?

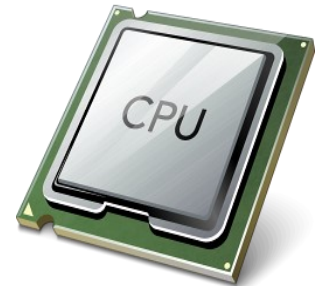
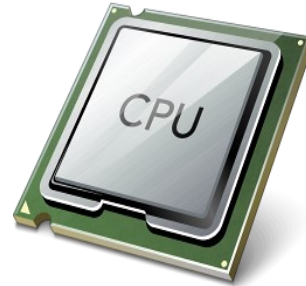
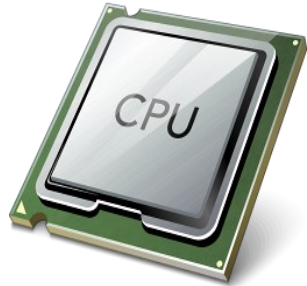
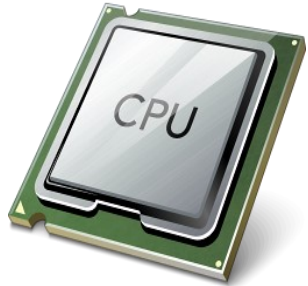
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Parallel GUI Implementation 2

Pros/cons?

Pros:

- Preserves programming model
- Can recover some parallelism

Cons:

- Workers still have same problem
- How to load balance?
- Shared mutable state a problem

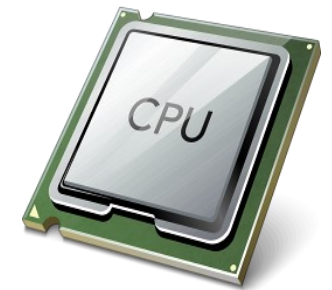
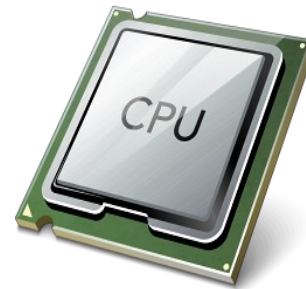
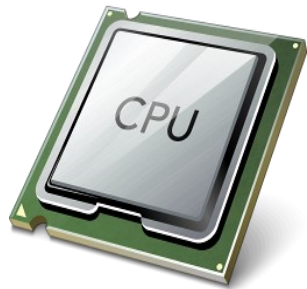
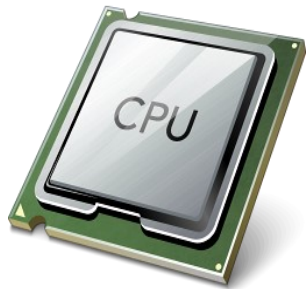
```
winmain() {  
    for(i=0; i<NUMPROCS; i++)  
        pthread_create(&tids[i], H...  
    for(i=0; i<NUMPROCS; i++)  
        pthread_join(&tids[i]);  
}
```

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    while(true) {  
        message = GetMessage();  
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Parallel GUI Implementation 2

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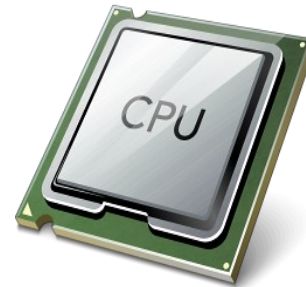
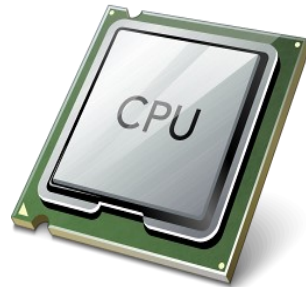
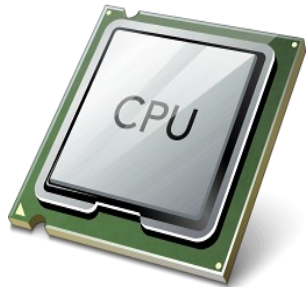
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*Extremely difficult to solve
without changing the whole
programming model...so*

change it

Questions?