# Superpages

Emmett Witchel

CS380L

## Virtualization/Superpages faux quiz (pick 2, 5 min)

- 1. Define PTE replication? Do we still need it?
- 2. FreeBSD always breaks superpages on a write. What are the alternatives/tradeoffs?
- 3. Define reference/dirty bit emulation.
- 4. What is a population map?
- 5. Why super-pages instead of big segments?

## Virtual Memory: Goals...what are they again?

- Abstraction: contiguous, isolated memory
  - Remember overlays?
- Prevent illegal operations
  - Access to others/OS memory
  - Fail fast (e.g. segv on \*(NULL))
  - Prevent exploits that try to execute program data
- Sharing mechanism/IPC substrate

**Oxdeadbeef** 

Virtual Address







- Segmentation cannot be disabled
  - Can be made a no-op (flat mode)



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- Segment = <base,len,type(code,data,stack)>
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```
// global int x = 1
int y; // stack
if (x) {
    y = 1;
    printf ("Boo");
} else
    y = 0;
 ds:x = 1; // data
ss:y; // stack
if (ds:x) {
    ss:y = 1;
    cs:printf(ds:"Boo");
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• Q: how many ops to translate?



- Q: how many ops to translate?
- Q: why does this perform *at all*?

#### Handle a page fault





### Sidebar: KASLR, KPTI

- KASLR → Kernel address space layout randomization
- KPTI/KAISER → Kernel pagetable isolation separates most kernel memory from user
- In user-space, map only information needed for interrupts, sys enter/exit



#### TLB organization

- How big does TLB actually have to be?
  - Usually small: 128-512 entries
  - Not very big, can support higher associativity
- TLB usually organized as fully-associative cache
  - Lookup is by Virtual Address
  - Returns Physical Address + other info
  - Recent architectures: set associativity in multi-level TLBs

Virtual Address	Physical Address	Dirty	Ref	Valid	Access	ASID
0xFA00	0x0003	Y N N	N	Y	R/W	34
0x0040	0x0010		Y	Y	R	0
0x0041	0x0011		Y	Y	R	0

Example: MIPS R3000

## It's all about the TLB!





## It's all about the TLB!





## It's all about the TLB!



Front End

Branch

Predicition

Instruction

Cache Tag

µOP Cache

Tag

L1 Instruction Cache

32KB 8-Way

Instruction

TLB

Zeroing Idioms

Data TLB

Cycle

Memory

L2 Cache

Simple

# TLB coverage trend TLB coverage as percentage of main memory

Factor of 1000 decrease in 15 years



# TLB coverage trend TLB coverage as percentage of main memory



# TLB coverage trend TLB coverage as percentage of main memory



#### How to increase TLB coverage

- Typical TLB coverage  $\approx$  1 MB (before ~2015)
- Use superpages!
  - large and small pages
- Increase TLB coverage
  - no increase in TLB size
  - no internal fragmentation
- Paper covers the challenges

#### Superpage Concepts

- Memory pages of larger sizes
  - supported by most modern CPUs via MMU
- Otherwise, same as normal pages
  - power of 2 size
  - use only one TLB entry
  - contiguous
  - aligned (physically and virtually)
  - uniform protection attributes
  - one reference bit, one dirty bit

![](_page_32_Figure_1.jpeg)

![](_page_33_Figure_1.jpeg)

![](_page_34_Figure_1.jpeg)

![](_page_35_Figure_1.jpeg)

- Promotion: create a superpage out of a set of smaller pages
  - mark page table entry of each base page
- When to promote?

![](_page_36_Picture_4.jpeg)

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![](_page_37_Figure_4.jpeg)

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![](_page_38_Figure_4.jpeg)

- Promotion: create a superpage out of a set of smaller pages
  - mark page table entry of each base page
- When to promote?

![](_page_39_Picture_4.jpeg)

#### Issue 3: demotion

Demotion: "splinter" superpage into smaller pages

#### When?

- page attributes of base pages in superpage become non-uniform
- during partial pageouts

## Group activity

- Summarize the paper's policy for
  - Allocation
  - Promotion
  - Demotion
- Give a specific example of where each would happen

### Issue 4: fragmentation

- Memory becomes fragmented due to
  - use of multiple page sizes
  - persistence of file cache pages
  - scattered *wired* (non-pageable) pages
- Contiguity: contended resource
- OS must
  - use contiguity restoration techniques
  - trade off impact of contiguity restoration against superpage benefits

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![](_page_43_Figure_9.jpeg)

#### FreeBSD Design

![](_page_45_Figure_2.jpeg)

![](_page_46_Figure_2.jpeg)

![](_page_47_Figure_2.jpeg)

![](_page_48_Figure_2.jpeg)

![](_page_49_Figure_2.jpeg)

Allocation: managing reservations

![](_page_50_Figure_1.jpeg)

Allocation: managing reservations

![](_page_51_Figure_1.jpeg)

best candidate for preemption at front:

 reservation whose most recently populated frame was populated the least recently

#### Incremental promotions

Promotion policy: opportunistic

![](_page_52_Figure_2.jpeg)

#### Speculative demotions

- One reference bit per superpage
  - How do we detect portions of a superpage not referenced anymore?
- On memory pressure, demote superpages when resetting ref bit
- Re-promote (incrementally) as pages are referenced

## Demotions: dirty superpages

- One dirty bit per superpage
  - what's dirty and what's not?
  - page out entire superpage
- Demote on first write to clean superpage

![](_page_54_Figure_5.jpeg)

 Re-promote (incrementally) as other pages are dirtied

#### Fragmentation control

- Low contiguity: modified page daemon
  - restore contiguity
    - move clean, inactive pages to the free list
  - minimize impact
    - prefer pages that contribute the most to contiguity
    - keep contents for as long as possible (even when part of a reservation: if reactivated, break reservation)
- Cluster wired pages

## Intel's Sunny Cove 2019

- 5-Level Paging
  - Large virtual address (57 bits, up from 48 bits)
  - Large virtual address space (128 PiB, up from 256 TiB)
- DTLB Load (DTLB split for loads and stores)
  - 4KB; 64 entries; 4-way set associative
  - 2MB; 32 entries; 4-way set associative
  - 1GB; 8 entries; 8-way set associative
- DTLB Stores
  - 4KB, 2MB, 1GB; 16 entries; 16-way set associative
- STLB, 2048 entries
  - 2,048 entries; 16-way set associative

## Huge pages improve TLB coverage

- Architecture supports larger page size (e.g., 2MB page)
  - Intel: 0 to 1,536 entries in 2 years (2013 ~ 2015)
- · Operating system has the burden of better huge page support

![](_page_57_Figure_4.jpeg)

#### TLB coverage proportional to 64 GB DRAM

# High address translation cost

Virtualization requires additional address translation

![](_page_58_Picture_2.jpeg)

![](_page_58_Figure_3.jpeg)

#### Guest superpages Or Host superpages?

• Need both!

Workloads	$h_B g_H$	h_H g_B	h_H g_H
429.mcf	1.18	1.13	1.43
Canneal	1.11	1.10	1.32
SVM	1.14	1.17	1.53
Tunkrank	1.11	1.11	1.30
Nutch	1.01	1.07	1.12
MovieRecmd	1.03	1.02	1.11
Olio	1.43	1.08	1.46
Redis	1.12	1.04	1.20
MongoDB	1.08	1.22	1.37

Table 3: Application speed up for huge page (2 MB) support relative to host (h) and guest (g) using base (4 KB) pages. For example, h\_B means the host uses base pages and h\_H means the host uses both base and huge pages.

# Operating system support for huge pages

- User-controlled huge page management
  - Admin reserves huge page in advance
  - New APIs for memory allocation/deallocation
  - It could fail to reserve huge pages when memory is fragmented
- Transparent huge page management
  - Developers do not know about huge page
  - OS Transparently allocates/deallocates huge pages
  - OS manages memory fragmentation

# Huge pages improve performance

Application speed up over using base pages only

![](_page_61_Figure_2.jpeg)

3

Port: 6379 PID: 30064

Redis 3.1.103 (3bba4842/1) 64 bit

Running in standalone mode

http://redis.io

30064:M 04 Aug 17:19:08.927 # WARNING: The TCP backlog setting of 511 cannot be enfor ced because /proc/sys/net/core/somaxconn is set to the lower value of 128. 30064:M 04 Aug 17:19:08.927 # Server started. Redis version 3.1.103 30064:M 04 Aug 17:19:08.927 # WARNING you have Transparent Huge Pages (THP) support e nabled in your kernel. This will create latency and memory usage issues with Redis. T o fix this issue run the command 'echo never > /sys/kernel/mm/transparent\_hugepage/en abled' as root, and add it to your /etc/rc.local in order to retain the setting after a reboot. Redis must be restarted after THP is disabled.

**\_ • \_** mongoDB DOCUMENTATION SERVER DRIVERS CLOUD SERVICES Was this page helpful? Yes No Introduction Administration > MongoDB Performance > Disable Transparent Huge Pages (THP) Installation Disable Transparent Huge Pages (THP) The mongo Shell On this page MongoDB CRUD Operations Init Script Aggregation Using tuned and ktune Test Your Changes Text Search Data Models Transparent Huge Pages (THP) is a Linux memory management system that reduces the overhead of Translation Lookaside Buffer (TLB) lookups on machines with large amounts of memory by using larger Administration memory pages. Production Notes However, database workloads often perform poorly with THP, because they tend to have sparse rather than Operations Checklist contiguous memory access patterns. You should disable THP on Linux machines to ensure best performance 30 Development Checklist for with MongoDB. Performance Database Profiler 30004:M 04 AUG 17:19:00.927 30064:M 04 Aug 17:19:08.927 # WARNING you have Transparent Huge Pages (THP) support e nabled in your kernel. This will create latency and memory usage issues with Redis. T o fix this issue run the command 'echo never > /sys/kernel/mm/transparent\_hugepage/en abled' as root, and add it to your /etc/rc.local in order to retain the setting after a reboot. Redis must be restarted after THP is disabled.

3

![](_page_64_Picture_0.jpeg)

#### Disable Transparent Huge Pages (THP)

#### 2.3.2. Disable Transparent Huge Pages

2.0.2. Disable fransp	arent nuge i uges					
Tran map		IBM Support				
			Support & Services 🗸 My Accou	nt▼ Search D		
s e cloude	era	Why Cloudera Products	Services & Support	Solutions Get Sta	arted	
okta		PRODUCT	DOCS DISCUSSION	SUPPORT Q	GET STARTE	
Transparent H	luge Pages: Thanks for y	our helpplease don't	help			
By the next morning C	CPU contention was worse.					
The alarmingly high sy problem, <i>what the he</i> e	vstem CPU usage that we'd seen in the p ck was this?	previous 3 months was always due to	MySQL using kernel m	utex. But since we'd	fixed that	
We discussed turning ultimately strengthene	off TCMalloc, but that would've been a r ed our platform.	nistake. Implementing TCMalloc was	a critical link in the chai	n of problems and so	olutions that	
We discovered very q default in most Linux o They effectively increa	uickly that the culprit this time was a <i>khu</i> distributions). Huge pages are designed ase the page size from the standard 4kb	<b>gepaged</b> enabled by a Linux kernel to improve performance by helping t to 2MB or 1Gb (depending on how it	flag called <b>Transparent</b> the operating system ma t is configured).	Huge Pages (THP; tu anage large amounts	Irned on by of memory.	
THP makes huge page memory-intensive ope	es easier to use by, among other things, erations.	arranging your memory into larger c	hunks. It works great for	app servers that are	not performing	
<ul> <li>► High Availabil</li> <li>► Backup and D</li> <li>► Cloudera Man</li> </ul>	bisaster Recovery Mager Administration Most Linux platforms	supported by CDH 5 include a feature call	<b>ction</b> ed <b>transparent hugepage co</b>	<b>mpaction</b> which		

 Cloudera Navigator Data Management Component Administration
 interacts poorly with Hadoop workloads and can seriously degrade performance.

## Huge page pathologies in Linux

- High page fault latency
  - Due to synchronous allocation in fault handler
- Memory bloating
  - Huge pages greedily allocated
- Unfair huge page allocation
  - E.g., one VM gets huge pages, maintains improved performance

### Page fault latency

- Fault handler gets huge page from allocator and zeroes it (terrible for application tail latency)
  - 4KB page : 3.6 us
  - 2MB page : 378.0 us (mostly from page zeroing)
- Fault handler can trigger memory compaction
  - 2 minutes to fragment 24 GB
  - All memory sizes eventually fragment

## Memory bloating

- Greedy allocation in Linux
  - Allocate a huge page on first fault to huge page region
  - The huge page region may not be fully used
- Greedy allocation causes severe internal fragmentation
  - Memory use often sparse
- What kind of fragmentation is this?

![](_page_68_Picture_7.jpeg)

#### Used virtual address

#### Unused virtual address

![](_page_68_Picture_10.jpeg)

## Memory bloating

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![](_page_69_Figure_5.jpeg)

![](_page_69_Picture_6.jpeg)

#### Used virtual address

#### Unused virtual address

![](_page_69_Picture_9.jpeg)